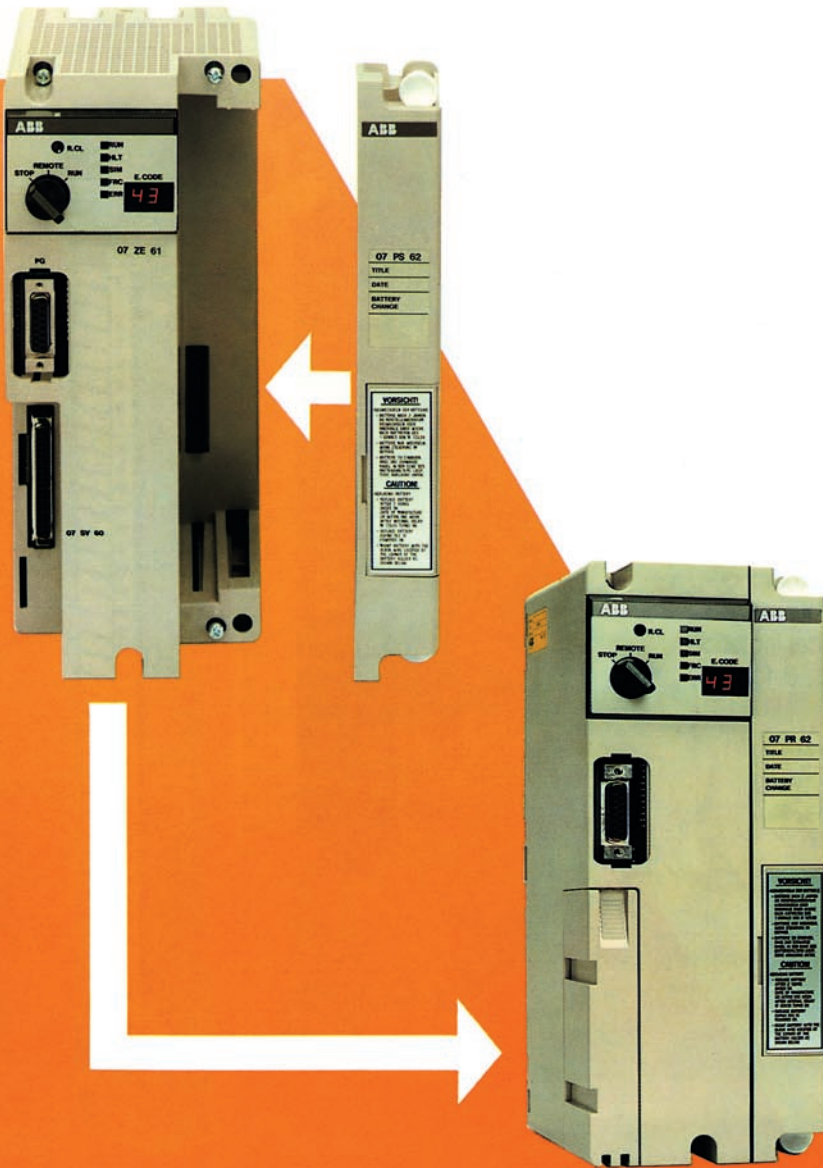


ABB Procontic T200

Central Units, Program Memories



complete central unit

All ABB Procontic T200 central units use the same instruction set; they differ only in the size of the program memory, the station expansion capacity and the operating speed. ABB Procontic T200 processors contain a realtime clock and online programming with the memories of class R3.

Central units

07 ZE 60 R302
Central unit for max. 7.6 k user program, for max. 1 module rack with max. 8 I/O slots, cycle time approx. 2.5 ms/1 k binary instruction.

07 ZE 62 R302
Central unit for max. 48.5 k user program, for max. 6 module racks with max. 58 I/O slots, cycle time approx. 1.7 ms/1 k binary instruction.

07 ZE 61 R302
Central unit for max. 15.7 k user program, for max. 2 module racks with max. 18 I/O slots, cycle time approx. 2.5 ms/1 k binary instruction.

07 ZE 63 R302
Central unit for max. 15.7 k user program, for max. 6 module racks with max. 58 I/O slots, cycle time approx. 2.5 ms/1 k binary instruction.

Program memories

07 PS 60 R2
Program memory (CMOS-RAM) for 3.5 k instructions, data memory for 2 k word flags

07 PS 61 R2/R3
Program memory (CMOS-RAM) for 7.6 k instructions, data memory for 2 k word flags

07 PS 62 R2/R3
Program memory (CMOS-RAM) for 15.7 k instructions, data memory for 16 k word flags

07 PS 63 R2/R3
Program memory (CMOS-RAM) for 48.5 k instructions, data memory for 50 k word flags

07 PR 62 R2
Program memory (EPROM) for 15.7 k instructions, data memory for 16 k word flags

07 PR 63 R2
Program memory (EPROM) for 48.5 k instructions, data memory for 50 k word flags