

THYRISTOR BRAKING UNIT

(Preliminary Issue)

Thyristor Braking Unit (TBU)

(Preliminary Issue)

INDEX

Renewal Parts

TBU Operation

Control Board (SAFT185 TBC)

Power Supply (SAMC11 POW)

Interface Board (SAFT19 INF)

Interface Board (SAFT181 INF)

Pulse Amp Board (SAMT11 PAC)

Measuring Card (SAFT183 VMC)

RENEWAL PARTS

Thyristor Braking Units (TBUs)

| Name | Code | Part No. | Qty. |
|------------------------------|----------|----------|------|
| Control Board SAFT 185 TBC | 58119687 | 505757 | 1 |
| Power Supply SAMC 11 POW | 57171847 | 104750 | 1 |
| Interface Board SAFT 19 INF | 57401389 | 110972 | 1 |
| Interface Board SAFT 181 INF | 58116076 | 505758 | 1 |
| Pulse Amp Board SAMT 11 PAC | 57211369 | 107392 | 1 |
| Measuring Card SAFT 183 VMC | 58115479 | 505760 | 1 |
| Fan | 09857931 | 503211 | 1 |
| Circuit Breaker | 35044931 | 504489 | 1 |
| Capacitor | 09833153 | 505761 | 3 |

TBU Parts Specific to Units

| Name | Code | Part No. | kVA | | | | | |
|---------------|----------|----------|-----|-----|-----|-----|-----|-----|
| | | | 290 | 460 | 730 | 340 | 540 | 870 |
| Choke | 57433124 | 504497 | | 1 | | | | |
| Choke | 57436603 | 504532 | 1 | | | | | |
| Choke | 57423439 | 505840 | | | | | 1 | 1 |
| Choke | 57421355 | 504500 | | | 1 | 1 | | |
| SCR Thyristor | 35077065 | 505841 | 3 | | | | | |
| SCR Thyristor | 09802011 | 505842 | | 3 | 3 | | 3 | 3 |
| SCR Thyristor | 10000556 | 505843 | | | | 3 | | |
| Thermostat | 35046852 | 503459 | 1 | | | 1 | | |
| Thermostat | 35044582 | 104743 | | 1 | 1 | | 1 | 1 |
| Resistor | 09827323 | 505846 | | 1 | 1 | | 1 | 1 |
| Resistor | 09827315 | 505845 | 1 | | | 1 | | |

| Name | Part No. |
|---------------------------|----------|
| 48 volt Power Supply 3.9A | 131091 |
| TBU Rack with Boards | 505411 |

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1. General information

Thyristor braking unit (TBU) is a new module used instead of line converting unit (LCU) or line generating unit (LGU), in applications where regeneration of power is required.

Standard units available are:

| | |
|--------------------|---------------|
| SAFUX 250F380(415) | SAFUX 290F460 |
| 400F380(415) | 460F460 |
| 630F380(415) | 730F460 |
| 1000F380(415) | 1150F460 |
| 1600F380(415) | 1800F460 |

| | |
|---------------|---------------|
| SAFUX 315F500 | SAFUX 340F575 |
| 500F500 | 540F575 |
| 800F500 | 870F575 |
| 1250F500 | 1400F575 |
| 2000F500 | 2100F575 |

| |
|---------------|
| SAFUX 400F660 |
| 630F660 |
| 1000F660 |
| 1600F660 |
| 2500F660 |

2. Design

2.1 Main circuit diagrams

Main circuit diagrams in appendix 11 and 12 and control circuit diagram in appendix 10.

2.2 Main circuit

TBU includes:

- regenerative thyristor bridge:
 - 12 "hockey pock" thyristors (400-2500kVA) or 6 thyristor modules (250-400 kVA)
 - 6 RC-snubbers
 - thermostatic switch
 - cooling elements; 2 bottom elements and 12 topping elements or only 2 bottom elements (with thyristor modules)
- filtering choke (in the small units included to the module, but in bigger units a separate module); in some applications filtering choke is not required (check DIMENSIONING INSTRUCTIONS FOR SAFUX THYRISTOR BRAKING UNITS).
- circuit cards:
 - SAMT 11 ; part of the mechanical construction
 - SAFT 183 VMC ; part of the mechanical construction
 - SAFT 185 TBC ; assembled to a card rack
 - SAFT 19 INF ; assembled to a card rack
 - SAFT 181 INF ; assembled to a card rack
 - SAMC 11 POW ; assembled to a card rack
 - SAFT 182 MOB ; mother card in the rack

2.3 Pulse amplifier card SAMT 11

Main purpose of pulse amplifier card is to construct galvanically isolated gate pulses to the thyristors from the control card logical level signals.

Gate current is continuous as long as logical ON-signal is active; in the beginning of the gate pulse a short spike to turn on the thyristor as fast as possible.

25V 35kHz power from the power supply card SAMC 11 POW.

Scaling of the ac-line voltage and current measurements is done on the pulse amplifier card from where they go to SAMC 19 INF card.

2.4 Power supply card

Operation of the power supply card is based on push-pull principle. Input voltage 30...55VAC. Output voltages +12V, -12V, +5V, 0V to the control electronics and 25V 35kHz to the pulse amplifier.

Rated power of the power supply is 80W (48V 1.7A).

2.5 Control card SAFT 185 TBC

Main purpose of this card is to calculate firing orders to the thyristors based on:

- current discontinuity signal HOLE
- synchronizing information from line voltage USYN
- DC-voltage

Important signals:

- FLT : summary of fault signals
- NETF: netfail signal

Self diagnostics:

- checks all components on the card and turns on the LED on the board if a fault is detected;

Block diagram of the card in figure 1.

2.6 Interface card SAMC 19 INF

Card is connected to the card rack and to the mother card SAFT 182 MOB. Also a connection with ribbon cable to control- and pulse amplifier cards.

Main functions:

- forms synchronizing signals (6*F-signal)
- netfail indication
- forms the current discontinuity signal
- frequency reference selection
- STOP-START signal
- phase locked loop for the synchronizing signal

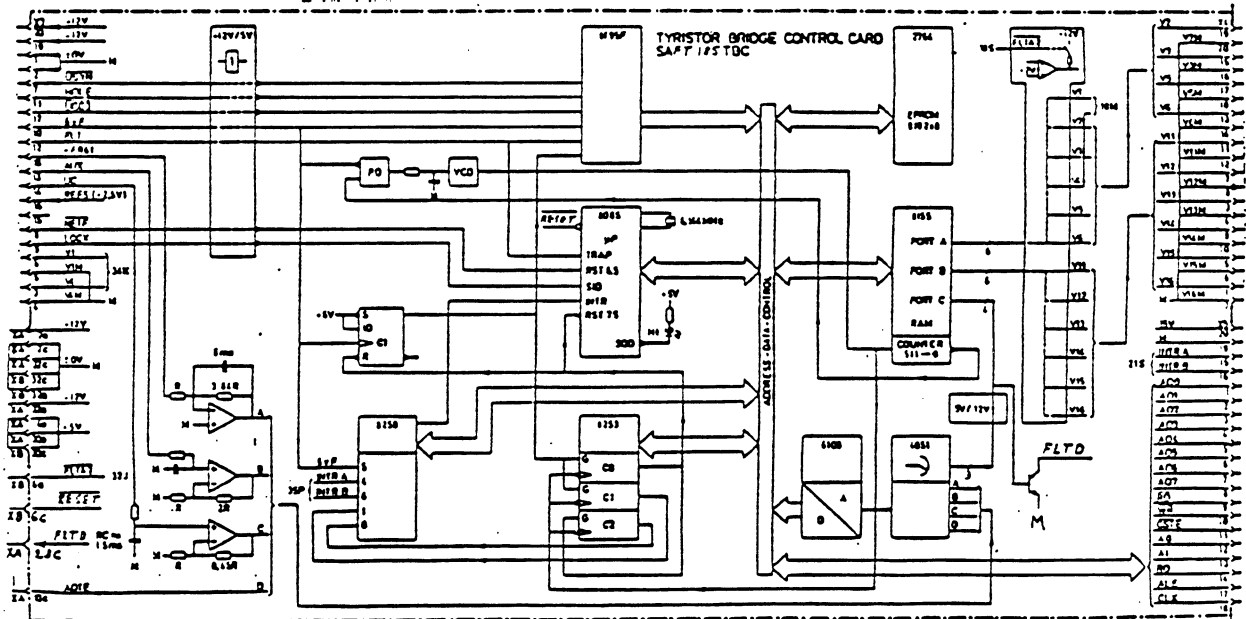


Figure 1. Block diagram

2.7 Interface card SAFT 181 INF

This is the interface card through which control card has connections to different modules. Signals to the card:

- DC-voltage information from SAFT 183 VMC
- inverter RUN signal
- overtemperature or fan motor stopped indication
- +24V from the contactor unit

Possibility to add:

- RESET switch/button for internal faults
- indication light for fault

2.8 Measuring card SAFT 183 VMC

Card measures DC-voltage and scales it (5Vdc output = rated DC-voltage). Output signal is also isolated from the DC-bus potential.

3. Operation

3.1 Main circuit operation

Regenerative bridge is build from 2-way 6-pulse thyristor bridges. Bridge that takes care of power flow from AC-line to the DC-bus is called motoring bridge. Bridge that takes care of power flow from DC-bus to the AC-line is called regenerating bridge.

Control angle of the motoring bridge is approximately 0 when power to the DC-bus is above 50% of rated (operates like a diode bridge).

When load is below 50% and current starts to change from continuous to discontinuous control angle will be changed to 0...65 degrees, which will cause the DC-voltage to decrease to 90% of rated.

If discontinuous current period exceeds the limit (14...32) pulses from the motoring bridge will be removed and two of the regenerating bridge thyristors will be turned on with control angle of 177.6 degrees and after this two thyristors will be turned on with 161 degrees control angle. After this, if current starts to flow, only one thyristor is turned on at a time with control angle of 155 degrees. If current still doesn't flow after this two thyristors will be turned on with control angle of 155...180 degrees depending from the length of "holes" in the current. If length of "hole" in the current still exceeds 10 degrees, pulses from the regenerating bridge will be removed and two motoring bridge thyristors will be turned on with 48 degrees control angle.

Both bridges are line commutated:

- each thyristor has a limited 180 ON-period
- ac-line voltage is used for commutation
- ON-cycle of one thyristor is 120 degrees
- commutation every 60 degrees

Firing table of the motoring bridge thyristors in figure 2.

Firing table of the regenerating bridge thyristors in figure 3.

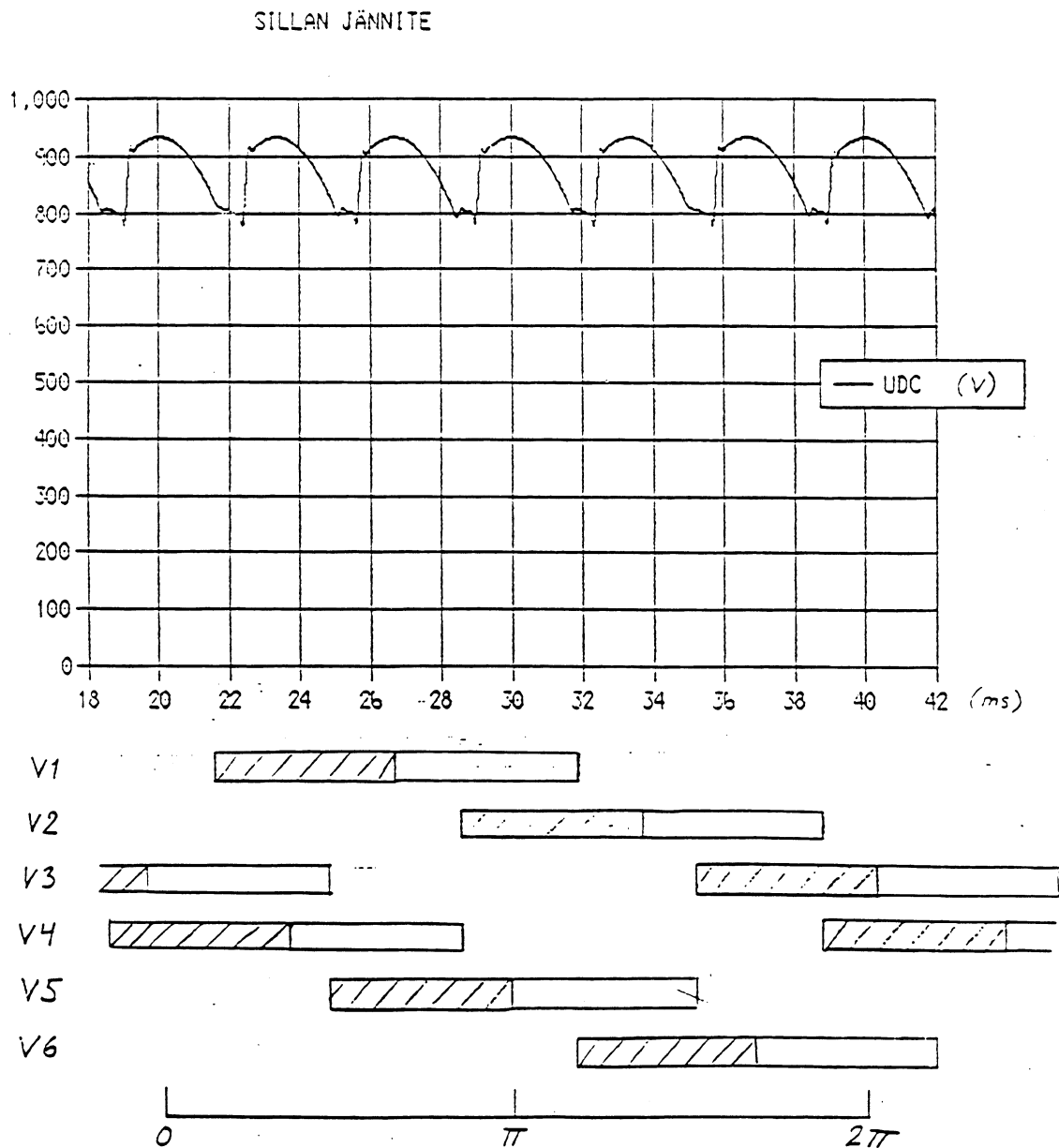


Figure 2. Firing table of the motoring bridge thyristor

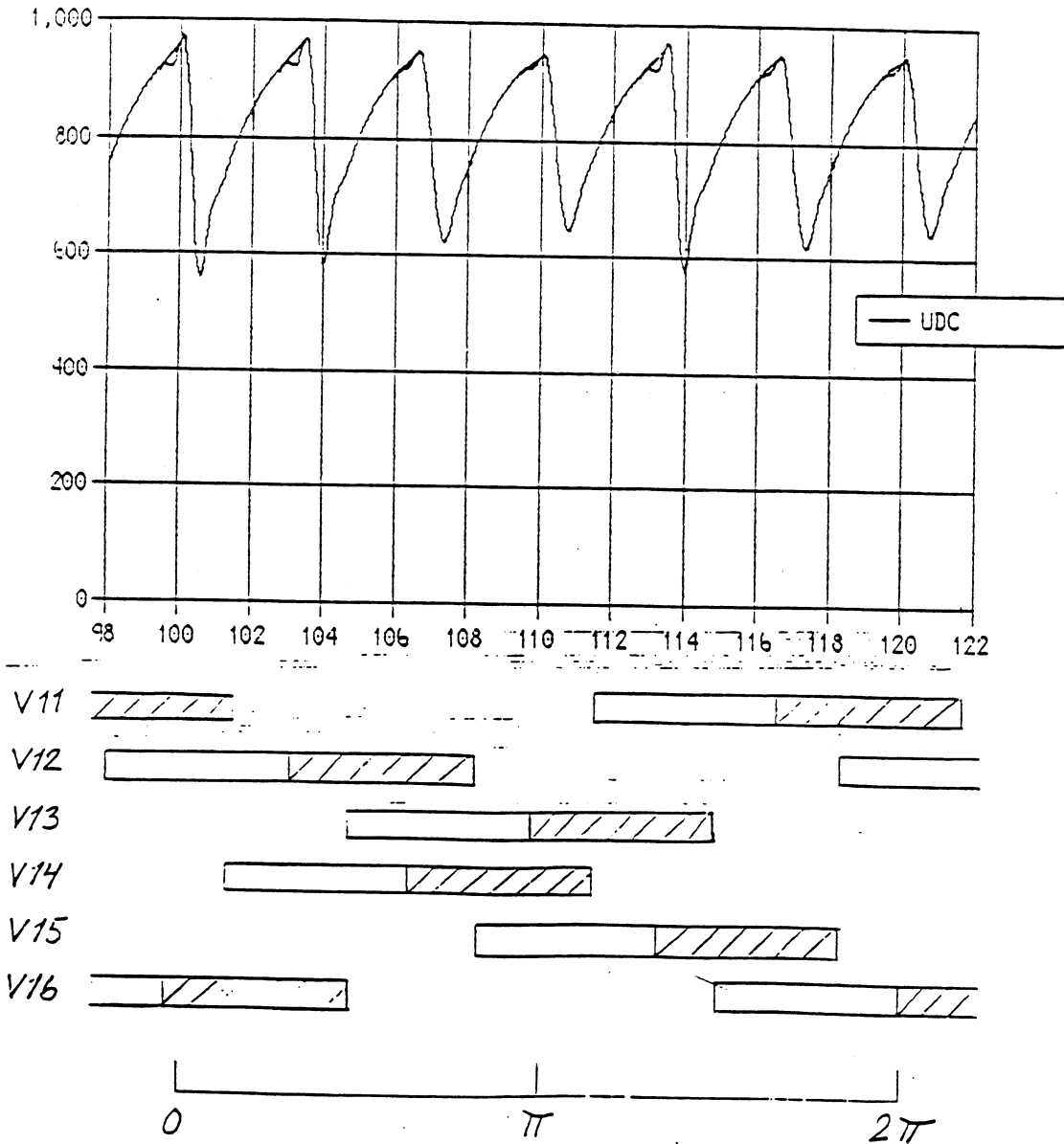


Figure 3. Firing table of the regenerating bridge thyristors

! TO PREVENT DC-BUS FUSE DESTRUCTION: !

! UCC3 START SIGNAL (UCC3 = HIGH) MUST BE FIRST SENT !
! TO THE THYRISTOR BRAKING UNIT AND AFTER MINIMUM OF TWO !
! SECOND DELAY TO THE INVERTER UNIT. !

! UCC3 STOP SIGNAL (UCC3 = LOW) MUST BE FIRST SENT TO !
! THE INVERTER UNIT AND AFTER MINIMUM OF TWO SECOND DELAY !
! TO THYRISTOR BRAKING UNIT. !

3.3 Fault situations

- a. thermostat switch or protection relay of the fan motor opens the current loop connected to the interface card SAFT 181 INF from where information goes to control card SAFT 185 TBC:
 - if fault happens when motoring, gate pulses are turned off and bridge will be closed
 - if fault happens when generating, gate pulses can't be turned off before current becomes discontinuous

In a case of a fault LED on the SAFT 181 INF card will be ON and also the light on the door will be on (optional). This fault doesn't need RESET; when fault is removed TBU will start automatically through the precharge routine.

b. internal faults in TBU:

Table 1. Internal faults in TBU

| fault number | explanation of the fault |
|-----------------|--|
| 1 | timing error in the bridge change |
| 2 | EPROM fault (D2, SAFT 185 TBC) |
| 5 | RAM fault (D5, SAFT 185 TBC) |
| 7 | Error in counter operation (D3, SAFT 185 TBC) |
| 11 | A/D-converter fault (D16, SAFT 185 TBC) |
| 12 | synchronizing error |
| 13 | too big a change in the control angle during two successive 60 degree cycles |
| 14 | program isn't synchronized with 6*F-signal |
| 15 | firing interrupt missing (D3, SAFT 185 TBC) |

Control of the bridge follows the same procedure as during an external fault.

LED on the control card will be blinking during an internal fault; first 2 second interval when LED is OFF and after that LED will turn ON as many times as the fault number in the table 1 indicates (interval = 0.3sec). Also the light on the cabinet door will be blinking (optional).

These faults need an external RESET or control power has to be cycled.

c. Power supply fault.

If error in the operation of the power supply card the LED on the card will turn OFF and operation of both bridges will be inhibited. If error disappears control card program will start with initialization routine.

d. error in phase locked loop

If phase locked loop can't synchronize to the AC-line (phase error above 8 degrees) operation on motoring side will be stopped immediately and on regenerating side after current becomes discontinuous. Program starts with initialization routine.

e. fault in the main AC-voltage

Netfault~~z~~ indication is done on SAFT 19 INF interface card:

- motoring bridge ON: change to generating bridge is inhibited and if fault doesn't disappear motoring bridge will be turned OFF.
- generating bridge ON: control continues in a normal way if current is continuous. If line voltage doesn't recover DC-fuses will be destroyed. If line voltage recovers before fuses are destroyed control will turn the bridge OFF.
- generating bridge ON: if current doesn't flow pulses to the generating bridge will be removed. If line voltage doesn't recover within 100-200 usec bridge will be turned OFF.

After bridge is turned OFF control will monitor the line voltage and after the voltage recovers the motoring bridge will be turned ON either through the precharge routine or directly depending from the DC-voltage level.

3.4 Operation of control system

TBU control measures U- and V- phase currents and forms a HOLE signal from these two; if phase current is below 1.8% of rated current transformer value signal HOLE will be "1" and if above signal will be "0".

Synchronizing signal F is formed on the pulse amplifier card from the secondary voltages of three phase synchronizing transformer. From this signal F interface card SAMC 19 INF forms the 6*F signal, which is used to synchronize thyristor firing pulses to the AC-line frequency.

TBU control measures also DC-voltage; this measurement is used during precharge and to control that voltage doesn't decrease below critical values.

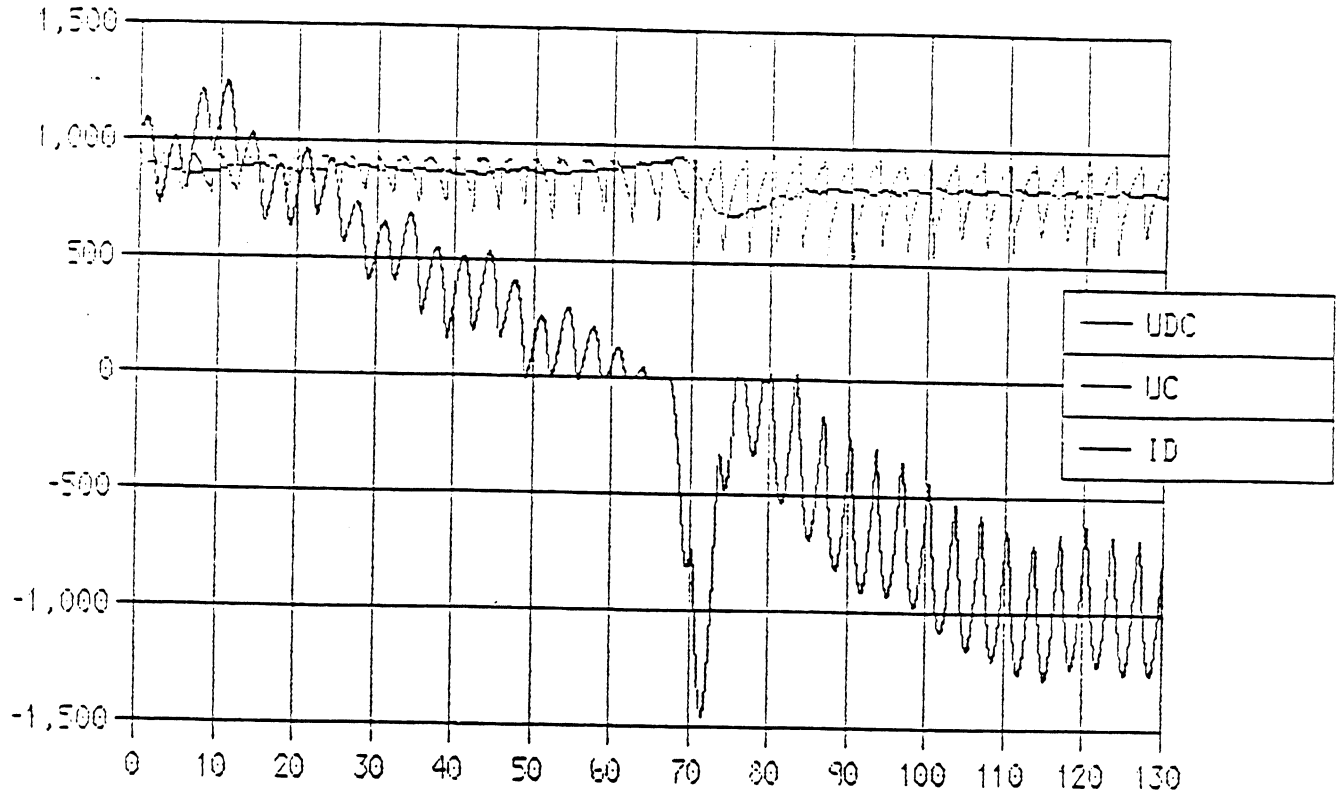
Main control program is cycled once during every 60 conduction cycle; during this conduction cycle HOLE signal is checked 26 times. Based on the number of HOLE signal zero states TBU control will calculate the actual DC-voltage value and also the new reference value. By using old reference and measured values a new control angle is calculated based on digital PI-control algorithm.

On the motoring side DC-voltage reference is approximately 90% of the rated when load current is below 50% of TBU rated current; to achieve this with light load control angle must be between 0 and 60 degrees. When load current exceeds 50% on the motoring side output voltage reference will be changed to 100%.

On the regenerating side voltage reference will be 90% regardless of the load current. Control angle will be between 155 and 180 degrees depending of the load. On the generator mode discontinuous current will cause oscillations to the DC-bus. AC-line inductance value must be checked in each application to be sure that 25 degrees commutation margin is not exceeded.

TBU PI-control values can't be changed. P-gain is set to approximately one; 10% voltage difference will cause a 10% change to the control system. I-gain is set to 0.5 seconds. Fast changes (load steps faster than 300msec) cause vibration to the DC-voltage; if rated motor torque is reversed within less than 200msec it will cause a spike to the motor torque and a compensating pulse to the ac-line. Operation of TBU is ideal when changes in the load don't happen faster than 0.5...1.0 seconds.

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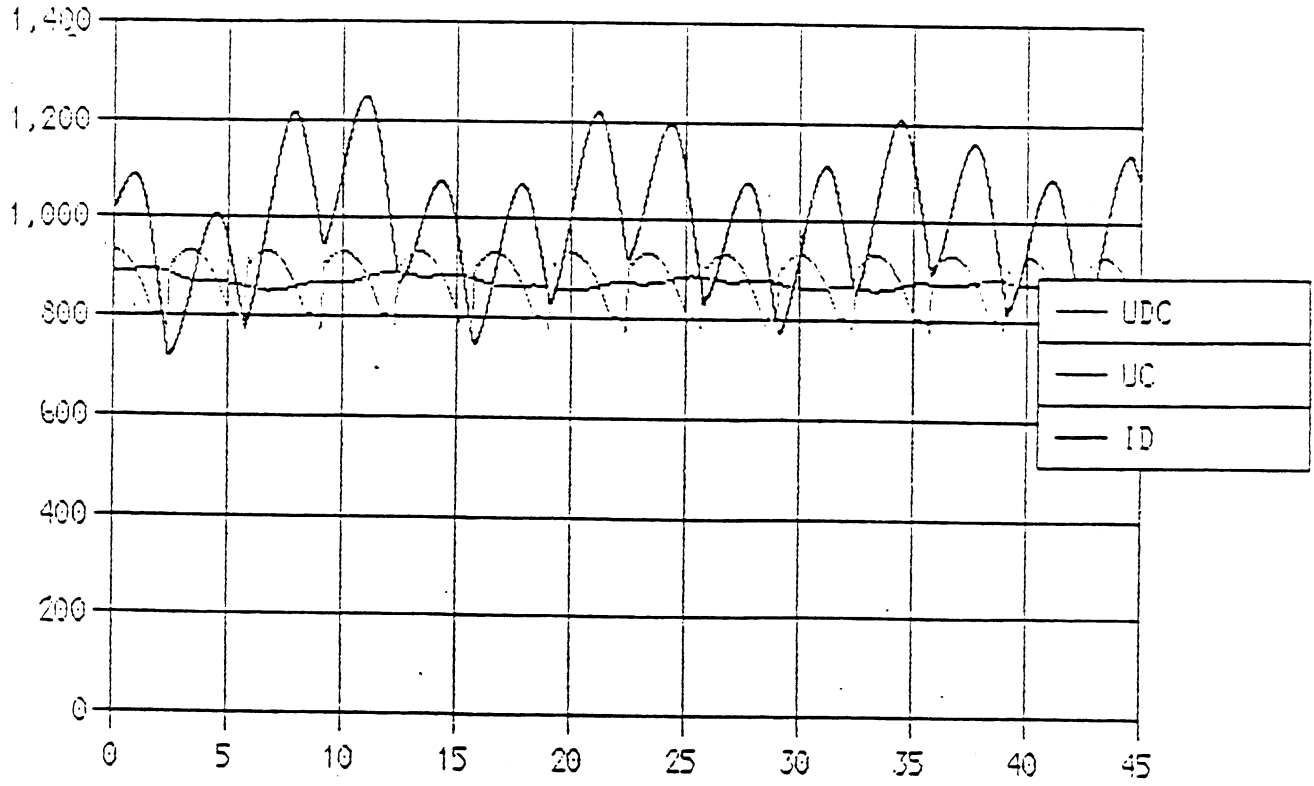


BRIDGE CHANGE FROM MOTORING TO REGENERATING:

- load torque from +100% to -100% within 100msec
- UDC = bridge output voltage [V]
- UC = capacitor bank voltage [V]
- ID = TBU dc-current [A]

03-31-89

SAFUX 1000F660:

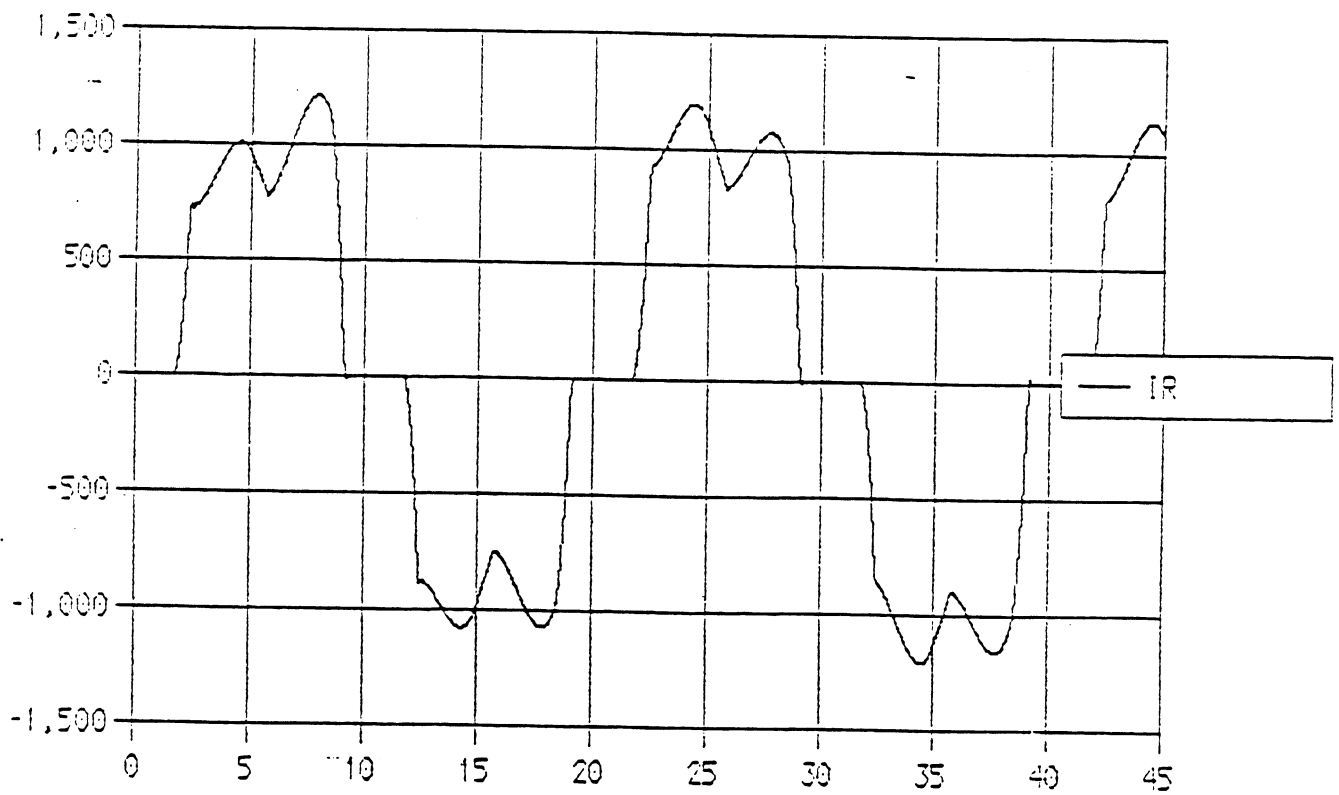


OPERATION ON MOTORING MODE (POWER FLOW FROM AC TO DC; P = 883kW)

03-31-89

SAFUX 1000F660:

AC-LINE CURRENT: IR [A]



OPERATION ON MOTORING MODE (ACTIVE POWER 883 kW)

03-31-89

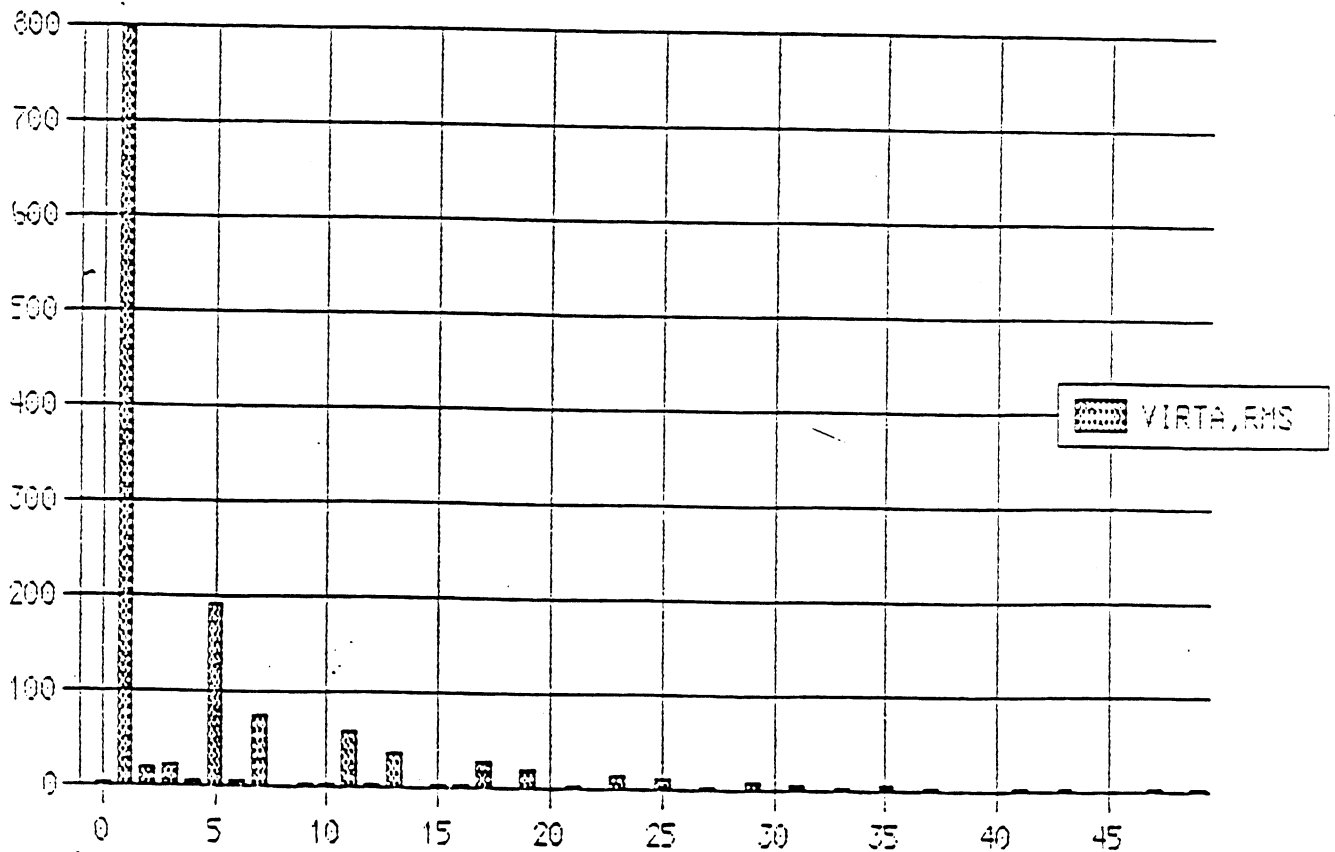
SAFUX 1000F660:

OPERATION ON MOTORING MODE (POWER FLOW FROM AC TO DC):

- CONTROL ANGLE 0
- ACTIVE POWER P = 883kW
- DISTORTION FACTOR OF CURRENT = 30.4%
- POWER FACTOR = 0.9875
- LAMDA = P/S = 0.9461

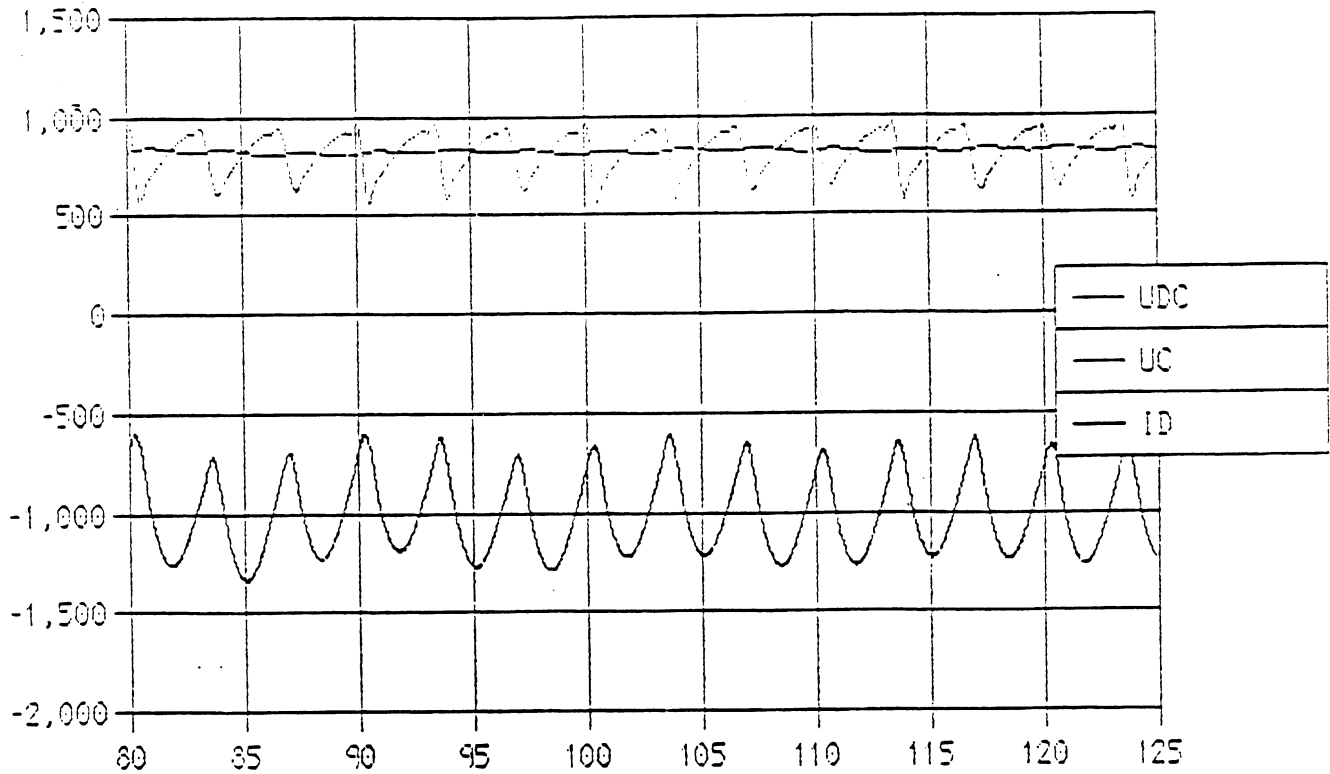
$$- S = 1.732 * U_n * \sqrt{\sum_{n=1}^{\infty} I_n^2}$$

SPECTRUM OF AC-PHASE CURRENT



03-31-89

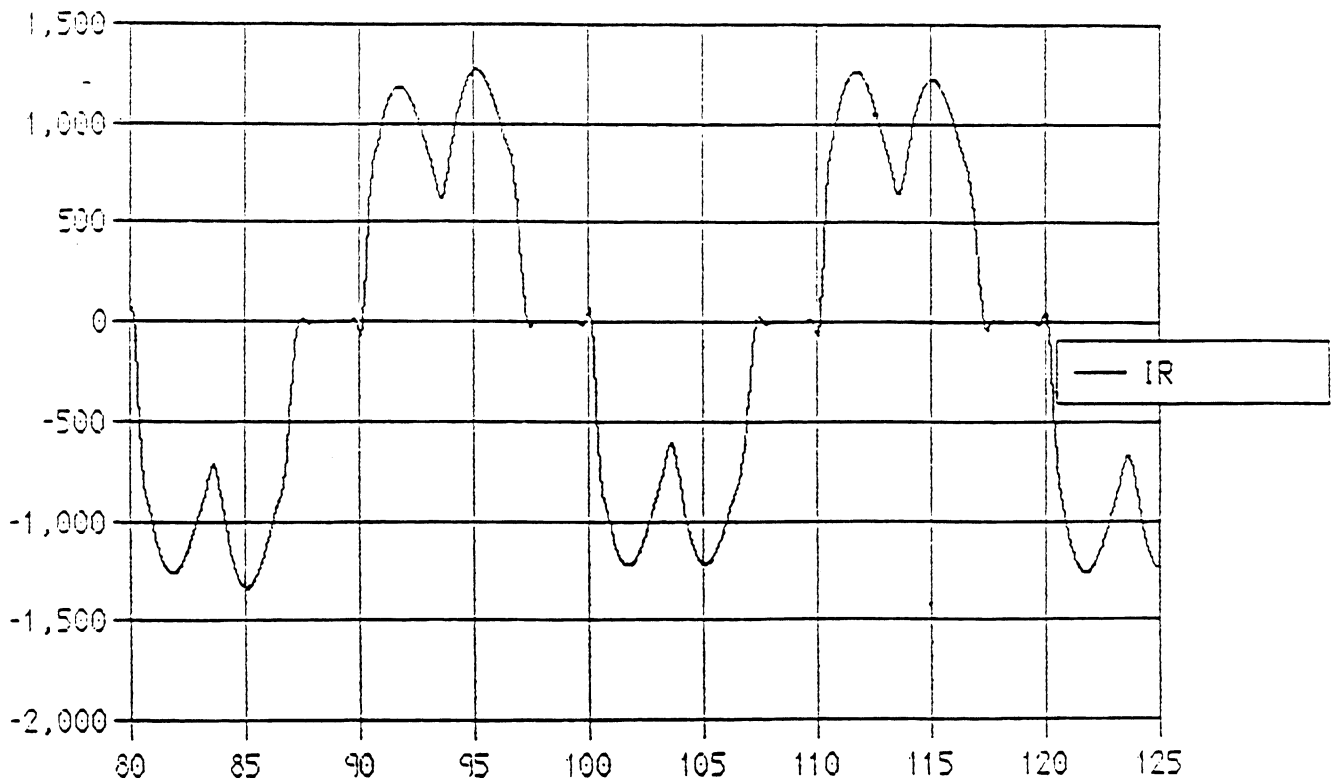
SAFUX 1000F660:



OPERATION ON REGENERATION MODE (POWER FLOW FROM DC TO AC P=816kW)

03-31-89

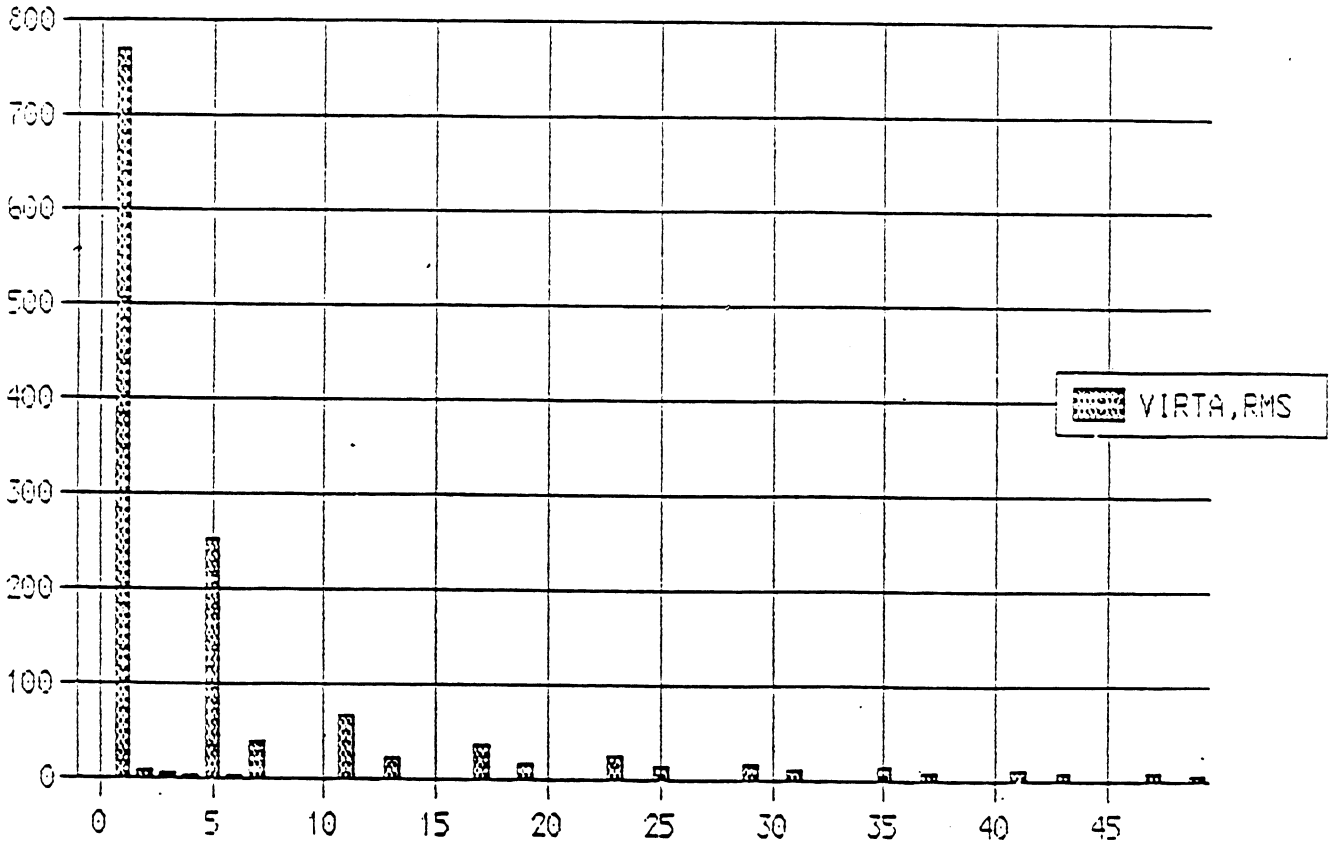
SAFUX 1000F660:
AC-LINE CURRENT: IR [A]



OPERATION IN REGENERATION MODE (POWER FLOW FROM DC TO AC P=816kW)

03-31-89

SAFUX 1000F660: SPECTRUM OF AC-PHASE CURRENT

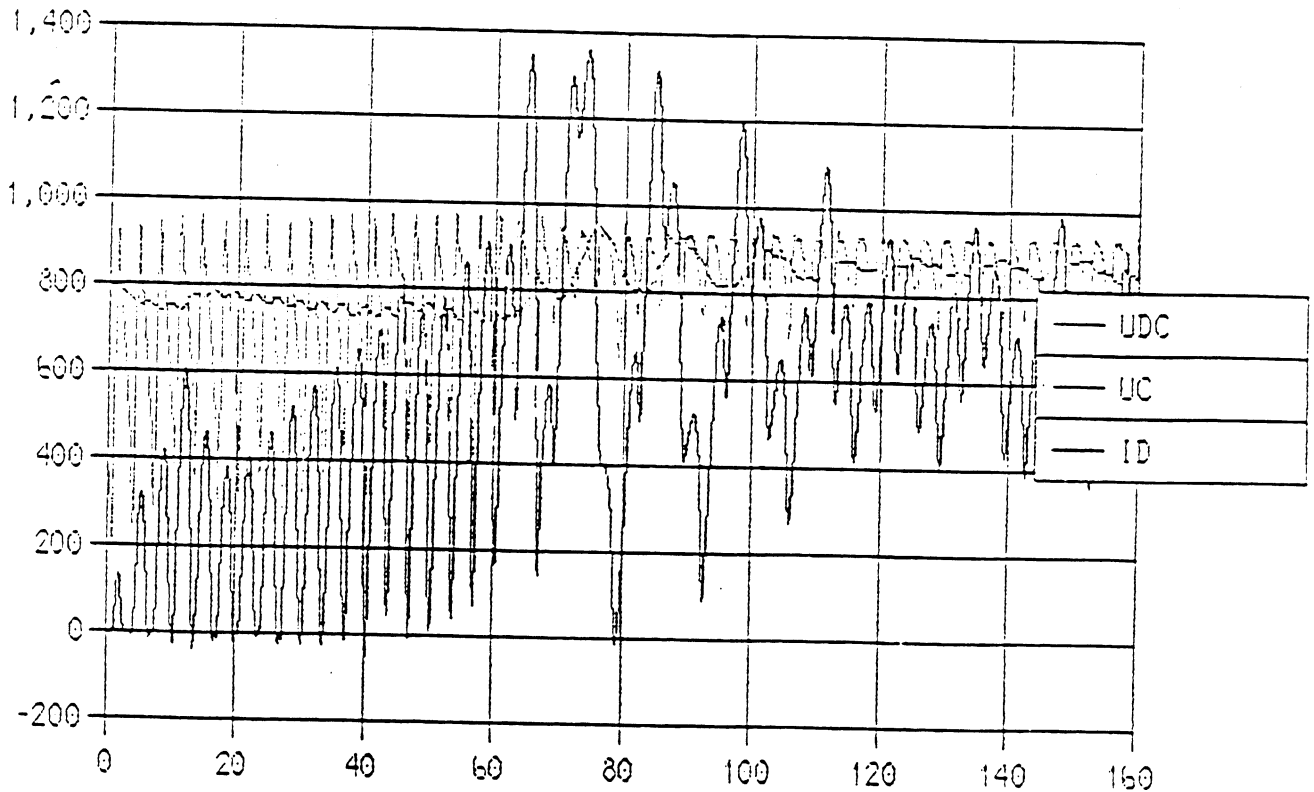


OPERATION IN REGENERATION MODE (POWER FLOW FROM DC TO AC P=816kW)

- CURRENT DISTORTION FACTOR = 35.8%
- POWER FACTOR = -0.9279
- LAMDA = P/S = -0.8665
- CONTROL ANGLE = 155 DEGREES

03-31-89

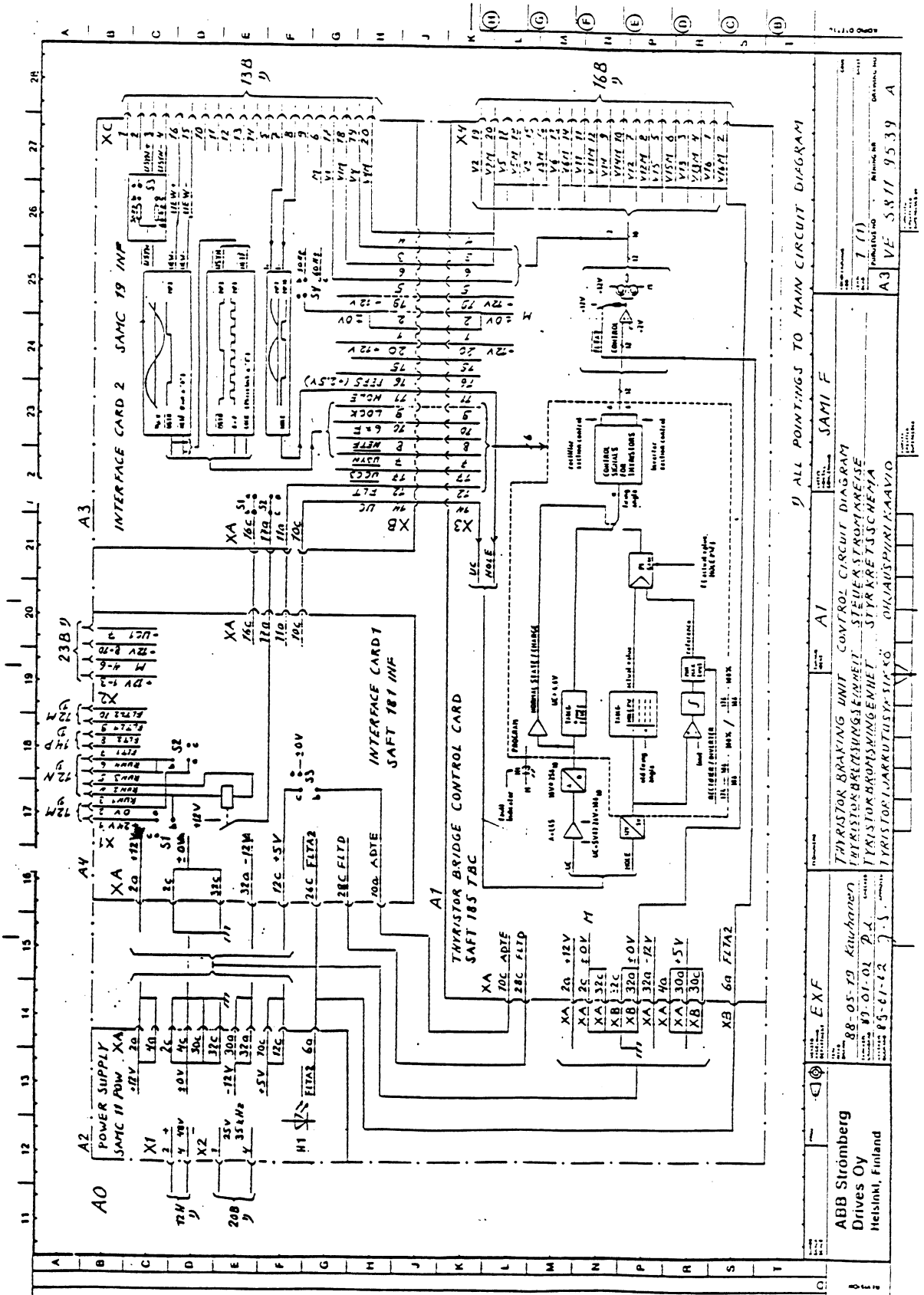
CURRENTS AND VOLTAGE AS A FUNCTION OF TIME



OPERATION ON MOTORING MODE:

- torque step: +50%
- step rise time: 50msec
- integration time: 500msec

03-31-89



ALL POINTINGS TO MAIN CIRCUIT DIAGRAM

ABB STROMBERG
Drives Oy
Helsinki, Finland

88-05-79 Kiukaanen
89-01-01 P.2
85-01-12

THYRISTOR BRAKING UNIT
THYRISTORBRAKINGSÄMMELI
TYRISTORBRAKINGSÄMMELI
TYRISTORJARRUTUSYKÄÄSÖ

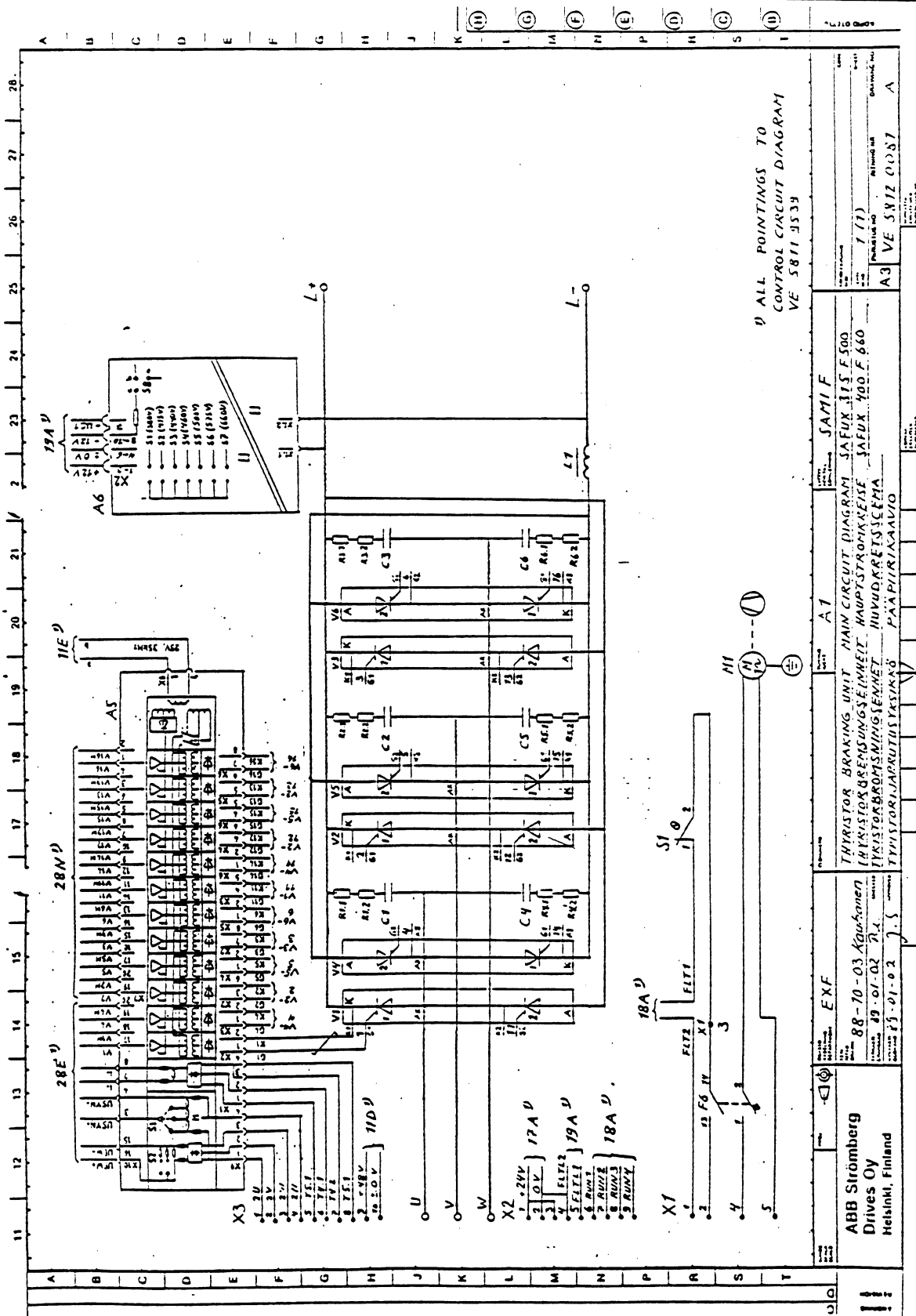
CONTROL CIRCUIT DIAGRAM
STUEVA STROMBERG
STYRKÄRETSICHEMIA
OHJAUSPIIRIKAAVIO

SAMI F

1 (1)

VE 58/1 95.39 A

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6 PULSE DIODE BRIDGE OPERATION:

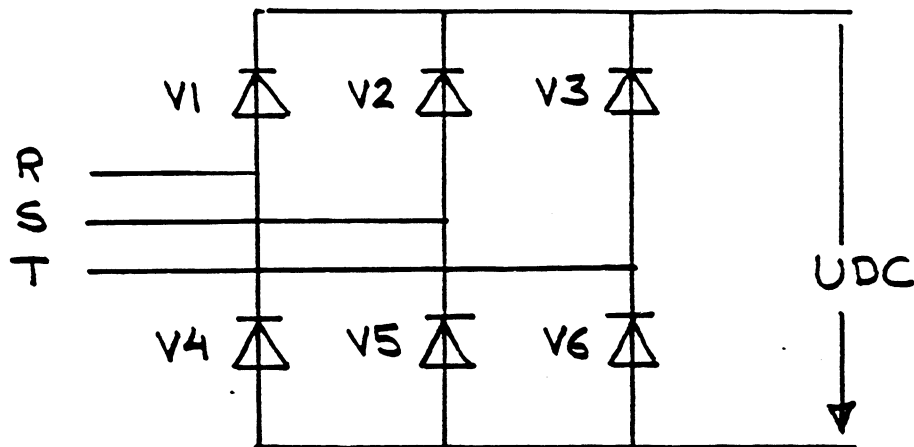


Figure 1. 6-pulse diode bridge

In figure 2 is an example of the 6-pulse diode bridge operation.

$UDC(\text{average}) = 1.35 * UAC(\text{main voltage RMS});$
for example for 460VAC dc-voltage is 621VDC.

Always the diodes with positive cathode to anode voltage are ON.
In figure 2 are also the conduction sequence of diodes.

$$1.35 \times 575 = 776 \text{ VDC}$$

03-31-89

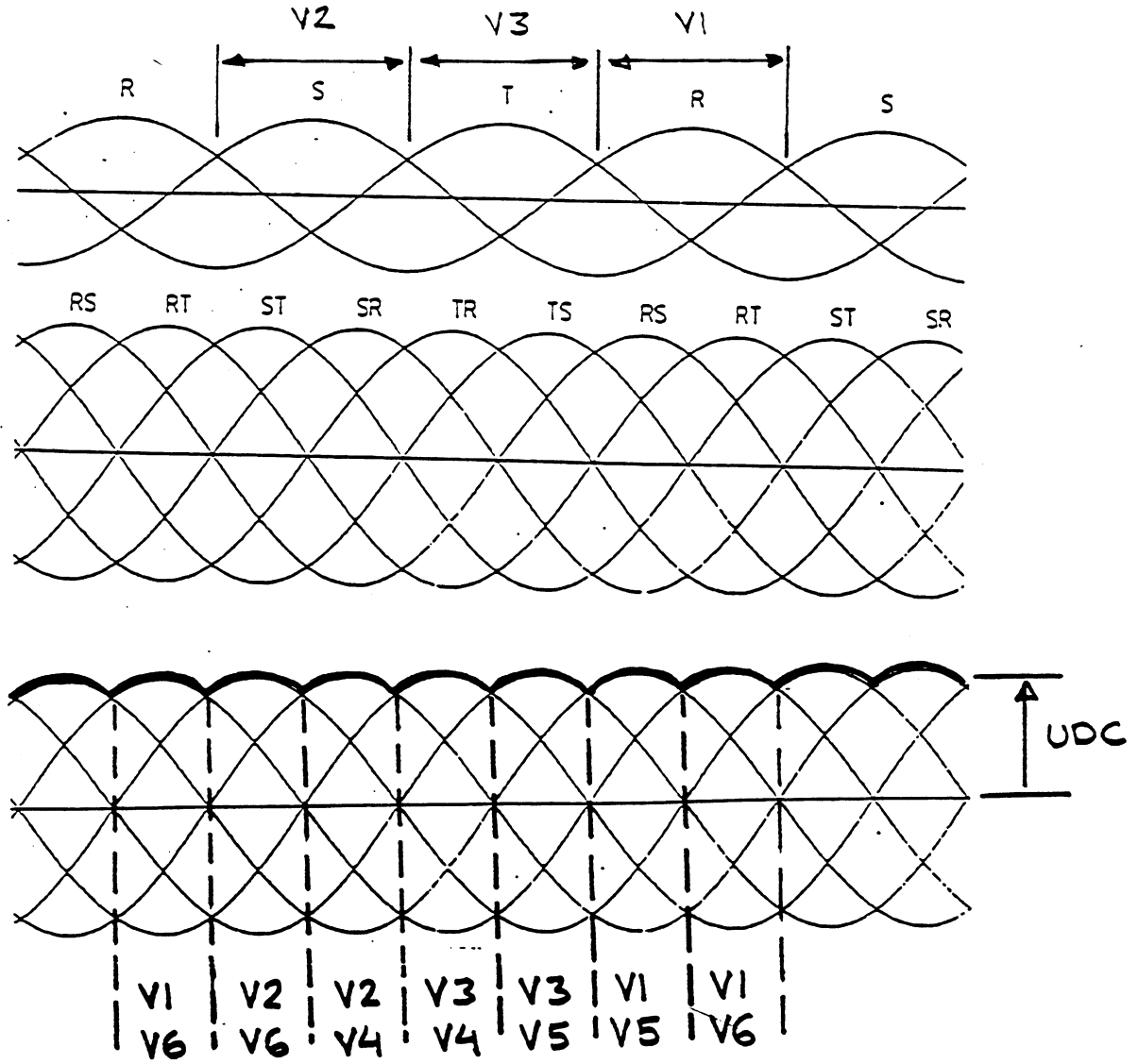


Figure 2. 6-pulse Diode bridge operation

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2-WAY 6-PULSE THYRISTOR BRIDGE OPERATION:

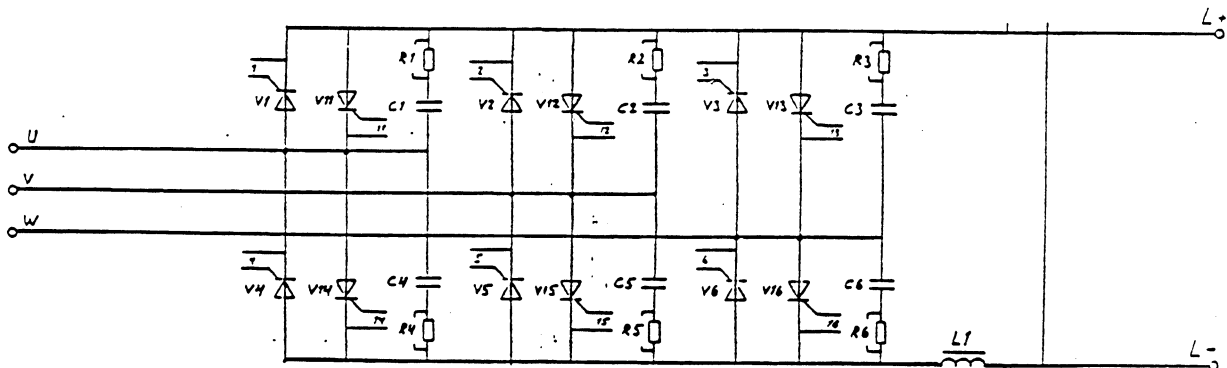


Figure 3. 2-way 6-pulse thyristor bridge

In figure 4 is an example of 2-way 6-pulse thyristor bridge when power flow is from AC-line to the DC-bus. In figure 4 the commutation angles have been ignored to make the operation of the bridge simple. By controlling the control angle of the thyristors it's possible to control the DC-bus level.

In figure 5 is an example of 2-way 6-pulse thyristor bridge when power flow is from DC-bus to the AC-line. Also in this figure commutation angles have been ignored.

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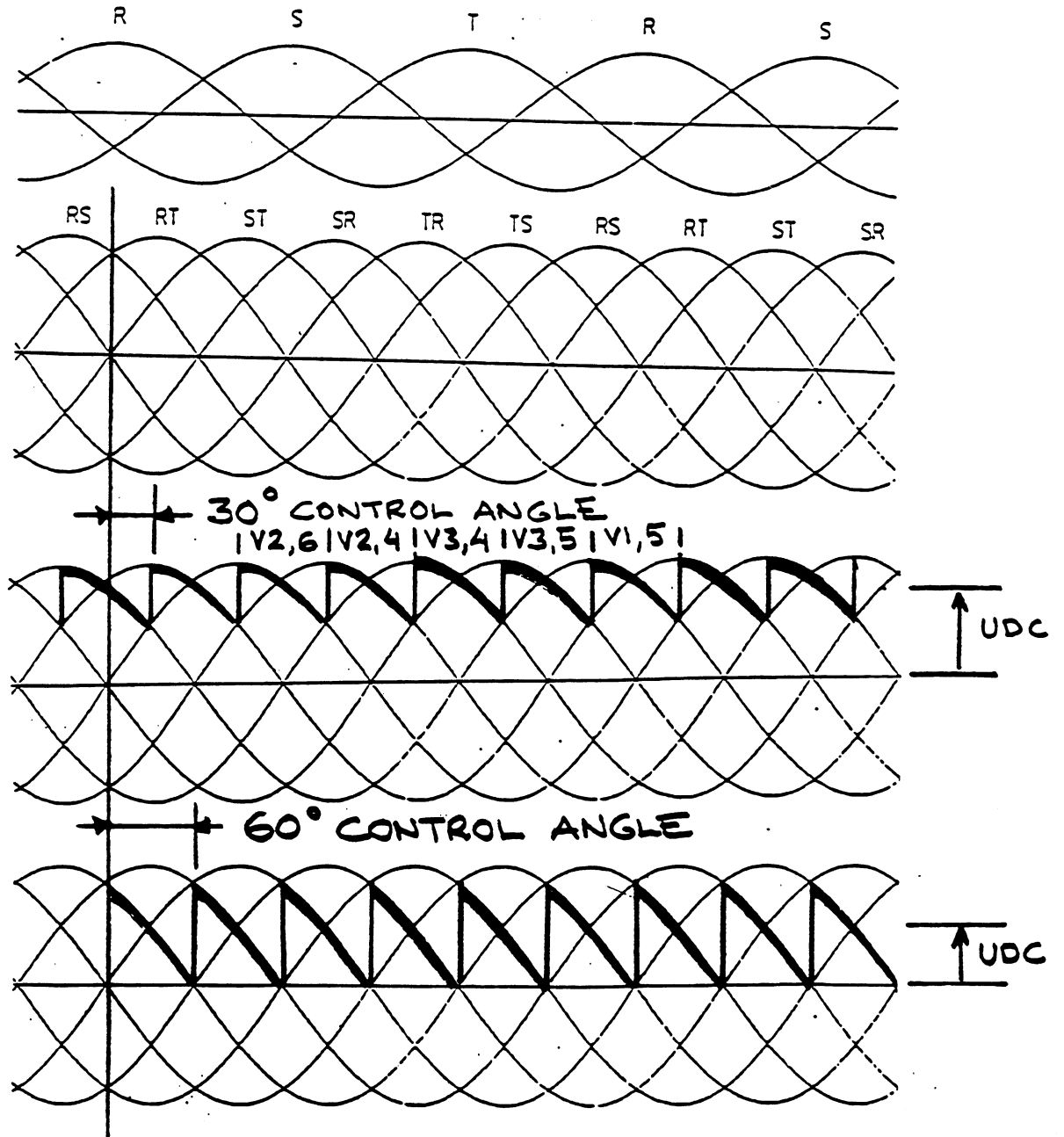


Figure 4. 2-way 6-pulse thyristor bridge operation
(power from AC to DC)

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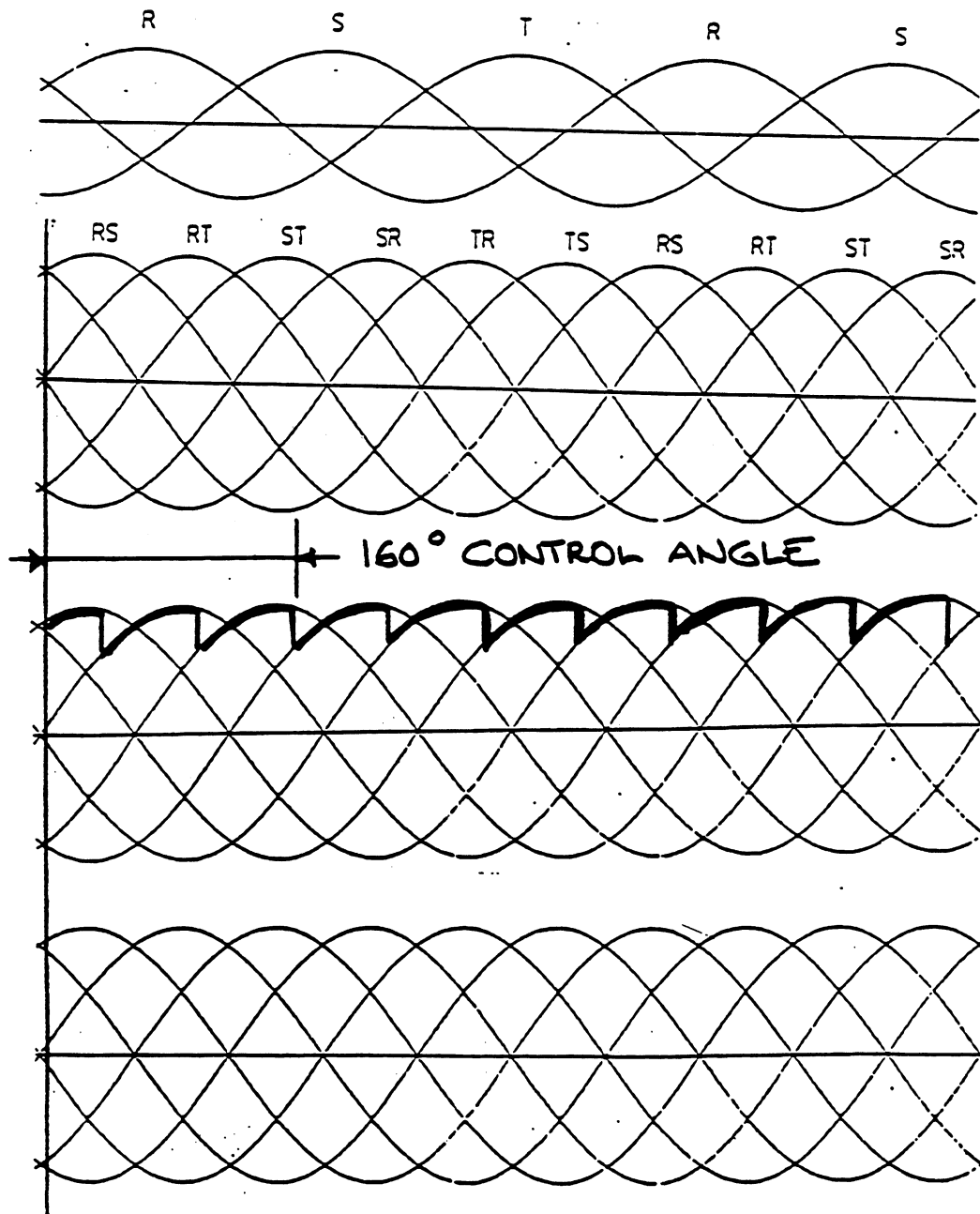
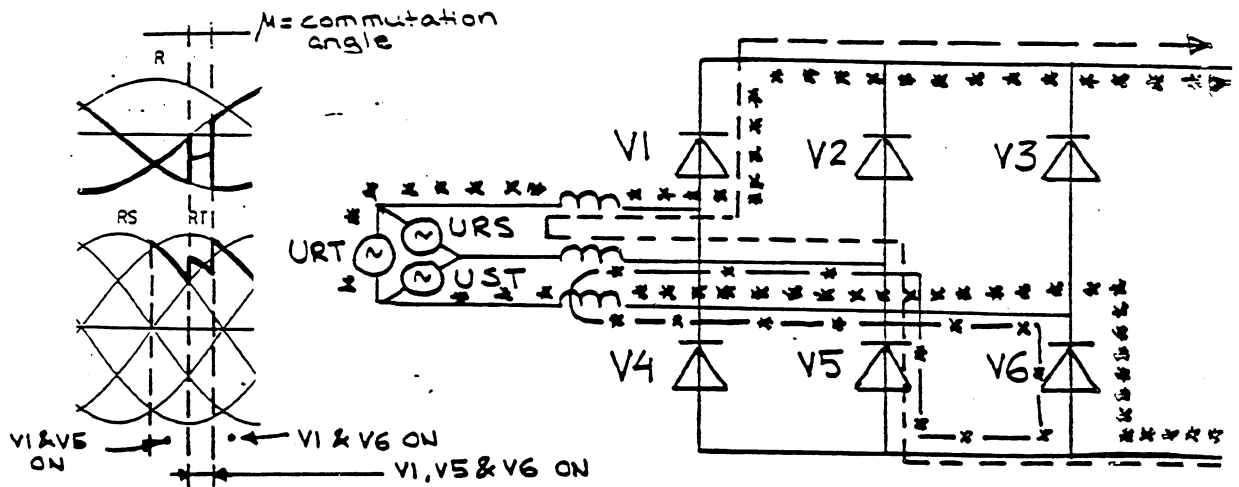


Figure 4. 2-way 6-pulse thyristor bridge operation
(power from DC to AC)

03-31-89

THYRISTOR BRIDGE COMMUTATION:



- during commutation of thyristors V5 and V6 notches in AC-line voltage:

$$U_R = U_T = \frac{U_S + U_T}{2}$$

- during commutation DC-voltage is:

$$U_{DC} = U_R - \frac{U_S + U_T}{2}$$

μ = commutation angle
 ----- = current before commutation
 ***** = current after commutation
 -*- - commutation current/

Figure 6. 2-way 6-pulse thyristor bridge commutation
(power from AC to DC)

03-31-89

Commutation time depends from the line inductance and line voltage:

$$U = L \cdot (di/dt)$$

Voltage for example in figure 6 was UST and inductance is two times one phase inductance of the AC-line (cables and transformers included). Commutation is over when commutation current equals the DC load current ("old" thyristor" will turn OFF).

If AC-line inductance exceeds allowed limits, current doesn't reach load current level before commutation voltage (UST in figure 6) turns negative and the "old" thyristor doesn't turn OFF and as a result DC-fuses will be lost.

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Appendix 1/1 SAFT 185 TBC layout
Appendix 2/4 SAFT 185 TBC circuit diagrams

The AC-current to/from the bridge is measured by two current transformers which are connected to the pulse amplifier board SAMT 11. On the SAMT 11 the current is rectified and transferred to the interface board SAMC 19 INF, where the conducting state information HOLE (SAMC 185 TBC page 2(1-4) X3.11) of the current is formed. The angle of the thyristors is formed according to this HOLE-signal.

In the figure 1 the PI-controller (block A) changes the firing angle according to the transit time of the current. The reference to the PI-block is the relative DC-voltage calculated based on the HOLE-signal. The reference value is formed from the HOLE-signal by the integrator and the level limitation controller (block B).

The internal DC-voltage supervision block takes part to the control during the precharge of the capacitors and when the voltage goes below the normal value.

The motor/generator switch logic operation is based to the transit time of the current:

- full load (continuous nominal current) at the motoring side will cause the firing angle to slide to 155 degrees
- when the current goes below 50% from the nominal the DC-voltage reference of the bridge will be reduced to approximately 90% from the nominal value to make the possible change to the generator side as smooth as possible
- when the bridge is used at the generator side it's controlled so that the DC-voltage is 5...10% below the nominal value so as to be sure that there's enough time for the commutations ; according to this limitation it's obvious that it can't compensate the changes in the AC-line voltage during the generator period

During the precharge of the capacitors the bridge can be controlled with a 50 degrees fixed control angle ; this is used only when the precharge resistors are present. By the switch S3 on the SAFT 181 INF board it's possible to select also a mode where the firing angle is slowly changed from 111 degrees to 50 degrees; in this case the whole precharge circuit is unnecessary.

2. Input- and output signals

2.1 Input signals

Only the signals in the following lists are used.
/ = inverse of the signal

| Signal | 185TBC | Page | Explanation |
|--------|--------|------|--|
| /USYN | X3.7 | 2 | Signal synchronized to the AC-line frequency |
| 6*F | X3.10 | 2 | Six times the synchronizing signal frequency |
| /NETF | X3.8 | 1 | Discontinuity in the AC-voltage |
| /RESET | XB.6c | 1 | Reset to the microprocessor from the power supply |
| LOCK | X3.9 | 1 | Phase difference between the AC-line frequency and the synchronizing signal. The zero pulse width of the LOCK signal is equal to the phase difference. The actual synchronizing signal is formed on the SAMC 19 INF interface board by a phase locked circuit from the /USYN signal ; the purpose of the phase locked circuit is to inhibit the short term notches to have any effect to the final synchronizing signal |
| HOLE | X3.11 | 2 | current informartion, 0 = current flows, 1 = no current |
| FLT | X3.12 | 1 | outside fault indication. Can be used also as a reset to the internal faults |
| /UCC3 | X3.17 | 2 | RUN-information from outside (from the 1352); 0 = STOP, 1 = RUN. The STOP-command locks the firing angle to 50 degrees and inhibits the change to the generator mode. During the STOP-mode it's prohibited to load the capacitor bank not to lower the DC-voltage level. The START-information to the control board must precede the actual start of the drives (2...3 sec delay from the START-information to the actual start is a must). If the delay is too short the collapse of the DC-level can cause the fuses to blow .. |

Signal 185TBC Page Explanation

| | | | |
|--------|--------|---|---|
| ADTE | XA.10c | 3 | The selection between the two precharge types; + 5V = constant 50 degrees angle. Precharge circuit needed. 0V = the control angle is reduced starting from 111 degrees according to the level of the DC-bus. Precharge circuit not needed |
| UC | X3.14 | 3 | DC-voltage measurement; +5V = nominal voltage |
| /FLTA2 | XB.6a | 4 | Control power fault from the power supply (same as /RESET) |

2.2 Output signals

Signal 185TBC Page Explanation

| | | | |
|---------|--------|---|---|
| REF5 | X3.16 | 3 | +2.5 V reference voltage to the SAMC 19 INF board |
| V1... | | 4 | Control signals to the motoring bridge |
| V6 | | | |
| V1M... | | 4 | Control signals to the motoring bridge |
| V6M | | | |
| V11... | | 4 | Control signals to the generating bridge |
| V16 | | | |
| V11M... | | 4 | Control signals to the generating bridge |
| V16M | | | |
| FLTD | XA.28c | 4 | Internal fault signal to the board SAFT 181 INF. This signal will blink the light at the door as well as the led V ₁ on the control board (SAFT 185 TBC page 1(1-4) |

3. SAFT 185 TBC operational block diagram/components

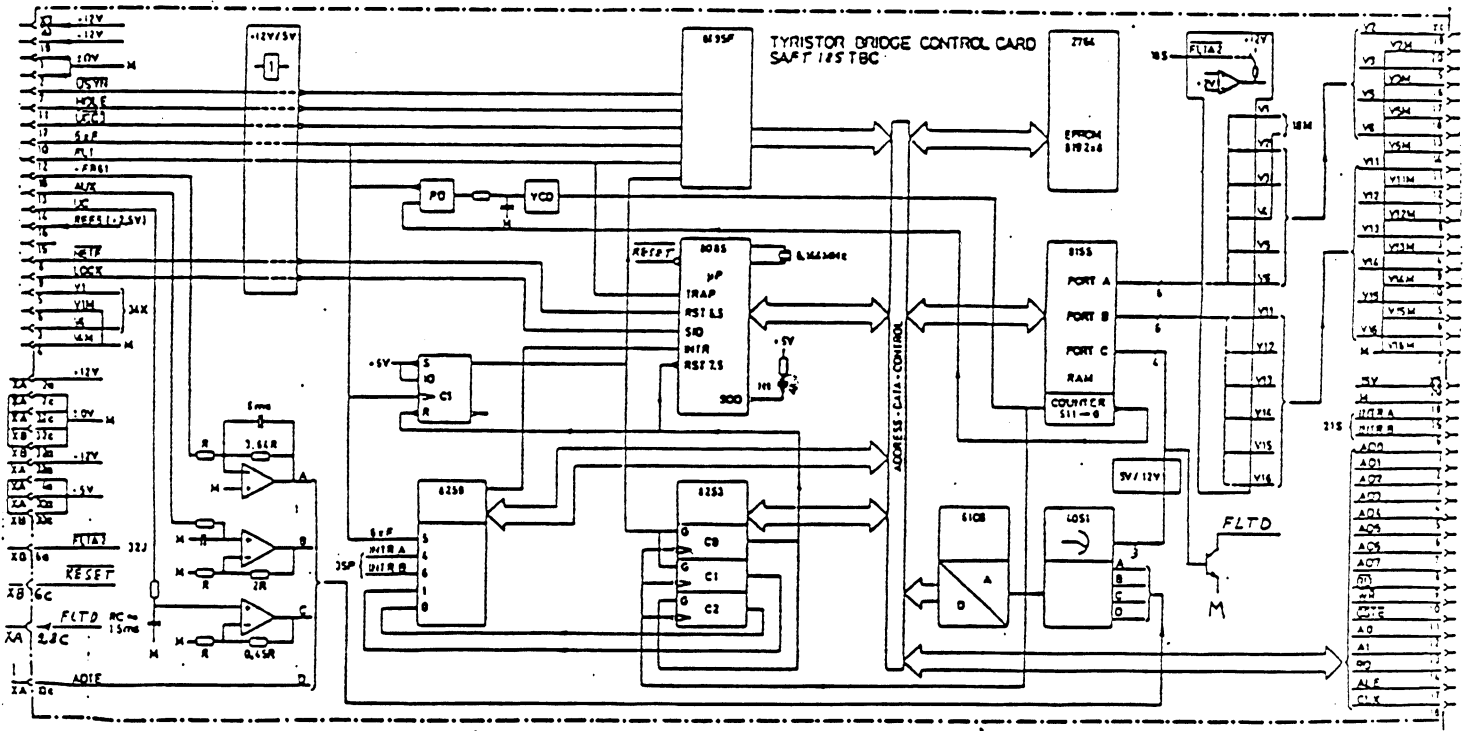


Figure 2. The block diagram of the control board

| Component | Explanation |
|-----------|--|
| D1 | 8-bit INTEL-uP 8085 with a 3,072MHz internal clock frequency (6,144MHz crystal) |
| D2 | 8 kByte EPROM-memory |
| D3 | Programmable counter; three 16-bit counters with individual clock-input, enable-input and output. Each down-counter can be programmed to six different modes. A pulse can be detected at the output when the counter reaches zero |
| D4 | Programmable interruption-circuit; 8 possible interrupt signals to the microprocessor. Interrupt signal wired to the INTR-input of the uP the priority of which is lower than the others |
| D5 | Programmable interface circuit with two 8 bit and one 6 bit input/output port. 256 byte RAM and a 14 bit programmable counter |
| D16 | 8 bit A/D-converter; input range 0...+10 VDC. Conversion time 3 us |
| D21 | Analog multiplekser for the A/D-coverter input |
| D17 | Phase locked loop; forms a clock frequency OSC that is 512 times the 6*F frequency. This signal is synchronized to the AC-line frequency; 50Hz AC-line frequency equals 153.6kHz. This clock frequency is used by the D3 to the timing of the thyristor firing |

4. The clock signal in the firing counter

The firing angle is loaded to the firing counter D3 (L0). Timing is selected so that 512 equals 60 degrees. Clock signal is formed by a phase locked loop D17 and the counter D5 according to the figure 3.

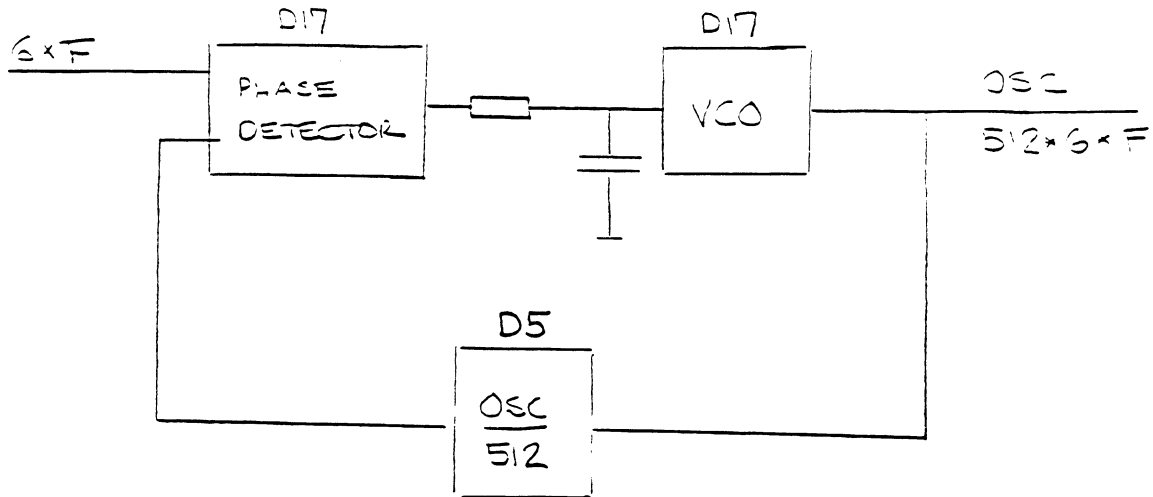


Figure 3. The clock signal OSC

The inputs to the phase detector D17 are $6 \cdot F$ ($6 \cdot \text{AC-line frequency}$) and the output of the voltage controlled oscillator VCO divided by 512.

The output of the phase detector depends from the phase difference of the input signals. If there is a change in the AC-line frequency also the phase difference and the output of the phase detector will change. Because of the change also the output of the VCO will change and as a result the phase difference will move towards zero.

The output from the VCO is 3072 times the AC-line frequency and because of the phase detection the firing time is always accurate even with a changing AC-line frequency.

5. Firing logic

D3 (L0) is used as the firing counter (at the generator side also L1). D3 counts the OSC clock pulses. The enable input of D3 is controlled by the D-flip-flop D11 output CNTEN. CNTEN changes to high state on the rising edge of $6 \cdot F$ -signal. When CNTEN goes up the counter L0 starts to count towards zero. When L0 reaches zero its output /FIRP goes to zero for a period of one clock cycle and at the same time CNTEN is reseted. /FIRP is connected to the uP input RST7.5, the priority of which is the highest from the maskable interrupt inputs. This RST7.5 normally causes the firing of the thyristors and the calculation of new firing pulses.

In the interrupt service program the uP writes the new firing orders to the D5 output ports A and B (A is for motor- and B for generator side). Comparators A4...A6 accomplish the level transformation from +5V to +12V. Finallu the hybride circuits A8 and A9 transfer the firing orders to the pulse amplifier board.

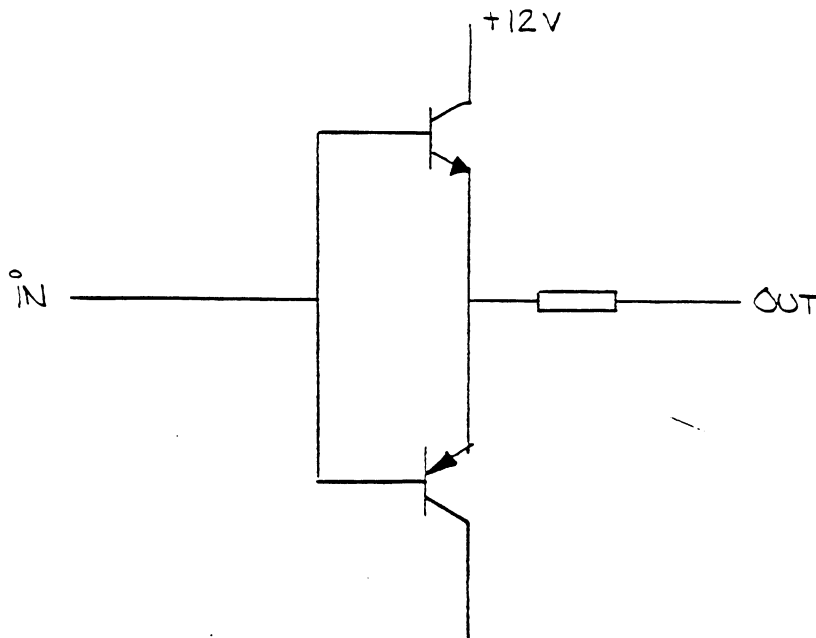


Figure 4. Hybride circuit A8-9 one phase diagram

Signal /FLTA2 inhibits the firing of the thyristors if the control power voltage levels aren't within the normal limits.

The motoring side firings are normally timed by the D3 counter L0, but after the change of the control side the next firing will be done on the rising edge of the signal 6*F.

The number corresponding the firing angle will be between 10...502 (1,2...58.8 degrees). By changing the starting number the angle can be changed by approximately 60 degrees. Additional to this also different control areas are used:

| | | |
|-----------|-------------|-------------------|
| - area 0: | -30...+30 | degrees motor |
| | +150...+210 | degrees generator |
| - area 1: | +30...+90 | degrees motor |
| - area 2: | +90...+150 | degrees motor |

Dividing the areas is a result of timing of the 6*F signal with the AC-line frequency. Changing the area means that in the table of conducting thyristors a jump up or down has to be done.

In the motor side two thyristors are always fired; in the generator side things aren't so simple. In the generator side the first firing interruption of each conduction cycle is given by the D3 counter L1, which is connected to the interrupt controller D4. In the first generator side firing after the change of the bridge two thristors are fired with the angle of 180 degrees. In the second firing only one thyristor is fired with the angle of 161 degrees. In the third firing and the firings after that one thyristor is fired with the angle of 155 degrees.

If the current stops flowing during the conduction cycle all the thyristors are turned off. In this case turning on one thyristor doesn't turn the bridge to the conductive state. Angle 155...180 degrees is loaded to the counter L0 depending on the load. Two thyristors are turned on with this angle (one of the thyristors was the one turned on before). If the current is continuous commutation will happen when one thyristor is turned on with 155 degrees angle; commutation margin will be 25 degrees. If the current is uncontinuous the current will start flowing only after two thyristors are turned on.

When the load current decreases it's important to increase the firing angle towards 180 degrees to inhibit the voltage to go lower. By turning on one thyristor before insures the commutation margin to be 25 degrees independent from the load.

The counter L2 of the circuit D3 is programmed to give a interruption 20 times during 60 degrees. After each interruption the signal HOLE is checked and according to the value in this counter either the current- or hole counter is increased by one. By this way it's easy to define the transit time of the uncontinuous current. According to this transit time the relative voltage of the bridge can be calculated and the control angle can be adjusted to the right value.

6. The principles of analog signal connections

A/D-converter D16 is a 8 bit converter, the conversion time of which is 3 usec. Input range is 0...+10 V corresponding to values 0...255 in the program. The converter is triggered by a write pulse /WR. /CSAD is the selection signal for the converter and must be zero when writing to or reading from the converter. When reading the converter the address lines A0 and A1 are used the following way:

- A0: 0 = the status of the converter
1 = the conversion result
- A1: 0 = the conversion result as a two's complement
(not used)
1 = the conversion result as a binary number (used)

The signal to be measured is selected by the analog multiplexer D21. The channel is selected by the addresses AMUX0...AMUX2 from the circuit D5. Only the signals ADTE and UC are measured in the program. The signal ADTE is measured only once after the control powers are turned on. If ADTE is +5V, the constant angle precharge is selected (50 degrees). If ADTE is 0V, the control angle is reduced according to the UC value starting from 111 degrees.

In the DC-voltage measurement +5V equals the nominal voltage. The gain of the operational amplifier A2 is adjusted so that +5V gives 186 as the conversion result from the A/D-converter. The DC-voltage information is used only during the precharge and during line interruptions. During a normal situation the voltage measurement is not used and the bridge won't compensate the shift of the DC-voltage caused by the changes in the AC-line.

7. Operation in fault situations

Two kind of faults are possible, internal- and external faults.

External faults:

- FLT; external fault input
- /NETF; interruption in the AC-supply voltage
- too large phase difference in the LOCK signal

Internal faults:

- damage of components
- internal loss of synchronism

7.1 Internal faults

Internal faults cause the led V1 to blink; also the signal FLTD will change the state with the same frequency as led V1. FLTD will turn the light on the TBU door on and off.

Fault codes are 1...15. In the case of a fault V1 will blink with 0.3 second intervals as many times as the fault code requires and after that it will have a 2 second pause and repeat the same cycle again.

| Fault code | Explanation |
|------------|---|
| 1 | <p>Timing error in the change of the bridge. In the motor mode this occurs if during the starting period of the bridge the first firing interruption doesn't take place within a certain time after the program has synchronized to the AC-line. This same thing can happen during the change of bridges from generator- to the motor bridge. After changing to the generator bridge the program checks if the synchronism is still correct compared to the 6*F signal; if the synchronism is lost the indication will be a timing error.</p> |
| 5 | <p>The RAM-memory of the circuit D5 has failed. The operation will be checked only once during the connection of the control powers.</p> |
| 7 | <p>Counter D3 doesn't work correctly. The operation will be checked only once during the connection of the control powers (all the three counters will be checked)</p> |
| 11 | <p>A/D-converter doesn't work correctly</p> |
| 12 | <p>Fault in the synchronizing procedure. When the signals /USYN and 6*F are high at the same time the program will calculate the right thyristors according to the control angle. If the thyristors calculated don't match with the ones picked up from the conduction order table a fault will turn on.</p> |
| 13 | <p>Too large change of the control angle between two successive 60 degrees cycles</p> |
| 14 | <p>Fault in in synchronizing procedure. After changing the control area the thyristors will be turned on on the rising edge of 6*F. The "area change inquiry" flag will be reseted immediately. If the flag is for some reason on after a normal firing interruption /FIRP and 6*F is at the same time one a fault will happen.</p> |

-
- 15 Watch dog in the back ground program indicates that the delay between two firing interruptions /FIRP is too long.

Internal faults can be reseted by cycling the control power or by causing an outside fault. In the case of a internal fault all the interruptions are inhibited. FLT is connected to an unmaskable interrupt signal TRAP, so the program will start running when the fault is turned off. If this FLT signal is used as a reset the possible operation of this FLT must be inhibited during a normal operation by hardware.

7.2 External faults

When ever the phase difference of the phase locked loop on the interphase board SAMC 19 INF compared to the AC-line exceeds 8 degrees it will cause an immediate stop in the motoring mode and in the generating mode a stop will be done as soon as the current changes to uncontinuous state. After the stop the program starts from the initialization routine.

External fault FLT will cause an immediate stop when motoring and a stop when generating only after the current is uncontinuous. After the stop the program returns to the initialization program.

Control power fault /FLTA2 will inhibit the firing of the thyristors. Processor will be reseted by the signal /RESET.

Network failure indication is performed on the board SAMC 19 INF. As an input to this board will be a six pulse rectified AC-line voltage. This unfiltered information and the same input filtered and damped by 15% will be compared by a comparator, so the signal works as a reference to itself. If the AC-linevoltage changes suddently below 85% from the nominal average value the comparator will supply a /NETF signal, which is connected to the reset input RST6.5 of the uP. There can be several /NETF indications before actual fault is detected; the detection depens from the following circumstances:

a. Network failure when motoring:

An internal flag NETFLG is set to one. When NETFLG is one the change to the generating side is inhibited. The program will go through the following routine:

- if NETFLG = 0, normal control mode
- if NETFLG > 0, one will be added to NETFLG
- if /NETF indication disappears NETFLG will be reseted to zero
- if NETFLG > 3, it will cause a internal stop

b. Network failure when generating and no current flowing during the indication:

Thyristors are turned off. Delay of 100...200 us after which current is checked once again and if it's still zero the control will be turned off. If the current is flowing after the thyristors are turned off a procedure explained in part c will follow.

c. Network failure when generating and the current is flowing:

The network failure flag is set to one and the control of the thyristors continues the normal way. If the AC-line doesn't come up any more the fuses will blow up. After the firing interruption following routine will be followed:

- if /NETF signal has disappeared, NETFLG will be set to zero and normal control will be continued
- if /NETF is on and the current is flowing normal control will be continued
- if /NETF is on and the current is zero the drive will be stopped

After the stop the program will wait for the /NETF signal to dissapeare. After the AC-line comes back up and the LOCK signal has indicated that the system is locked to the line the program will measure the value of the UDC during a period of approximately 500ms:

- If the UDC indicates that the voltage is/was below 60% from the nominal value the drive will be started through the precharge routine. The same precharge routine will be followed also if the UDC didn't exceed 90% value from the nominal DC voltage during the measurement period.
- If the UDC exceeds the value 90% form the nominal all the thyristors in the motoring bridge will be turned on several times during this 500ms period. If UDC doesn't go below 90% value the motoring brige will start to get normal firing pulses starting with the control angle 0 degrees. If this doesn't happen the precharge will precede the start.

8. Operation of the angle controller

The angle controller will follow the following formula:

$$\pm d(\text{alfa}) = GPROP*(USREFP-USREF+USVAL-USVALP) + GINTEG*(USVAL-USREF)$$

$\pm d(\text{alfa})$ = the change of the control angle (+ used on the motoring side and - on the generating side)

GPROP = the gain of the P term

GINTEG = the gain of the I term

USREF = new reference output voltage of the bridge

USREFP = old reference output voltage of the bridge

USVAL = new actual value of the output voltage

USVALP = old actual value of the output voltage

On the motoring side with a light load the output voltage will be $90\% \cdot U_{dcn}$ and with a heavy load $100\% \cdot U_{dcn}$. The reference will be changed smoothly according to the transit time of the current.

On the generating side the reference remains at the constant $90\% \cdot U_{dcn}$ value. The actual voltage value of the bridge is calculated according to the control angle and the load determined by the transit time of the current. Checking/control of the angle is done once during every 60 degrees period.

All the reference- and actual values are relative to the AC-line voltage, which means that the controller doesn't compensate any changes in the AC-line voltage.

9. Precharge

Two possible precharge modes are available depending on the state of the signal ADTE:

- ADTE = +5V:
Constant control angle 50 degrees used (precharge circuitry must be added).
- ADTE = 0V:
The control angle is reduced from the starting value of 111 degrees to 50 degrees.
The control angle will not be reduced if the UC doesn't start to increase.

In both cases the precharge will last as long as the DC voltage exceeds 85% from the nominal. With the constant angle precharge (50 degrees) it means that the precharge can be done with 90% AC-line voltage but not with a lower value.

If the signal /UCC3 goes to zero, the program returns to the precharge routine and will stay there with the angle of 50 degrees as long as the signal /UCC3 goes back to one.

10. Change of the bridge

The change of the bridge will happen always, when the current during the present 60 degrees period stops flowing and the transit period of the current is below the reference value. After the current stops flowing all the thyristors will be turned off.

The change from motoring to generating:

- if the last control angle on the motoring side is below 28 degrees the first control angle used on generating side will be approximately 180 degrees:

If the motoring control angle was 15 degrees the first generator side firing will happen after 45 degrees from the last motor side firing pulse.

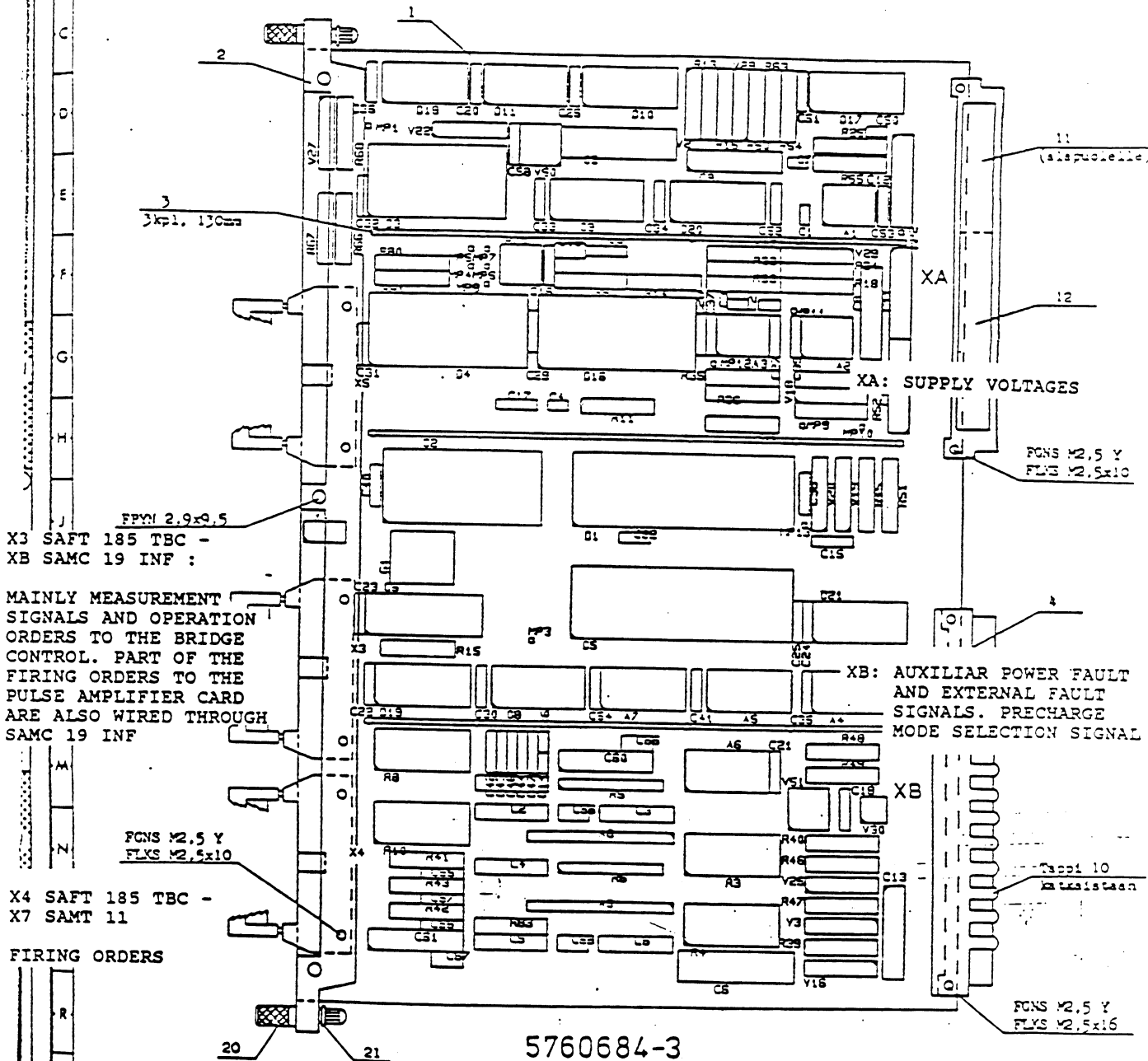
If the motoring control angle is above 28 degrees the first generator side firing will happen in the second possible point with the angle of approximately 180 degrees. So for example if the motoring control angle is 30 degrees the first generator side firing will happen 90 degrees after the last motor side firing pulse and with a 60 degrees angle the first generator side firing will happen 60 degrees after the last motor side firing pulse.

The change from generating to motoring:

After the decision of the bridge change following will happen:

The motor bridge thyristor that is parallel with the generator bridge thyristor that was the last one turned on will be turned on. This will insure that the generator bridge really turns off. The first real motor bridge firing will happen in the first possible point with an angle of 50 degrees; if the angle of the generator bridge was 170 degrees the first motor bridge firing will happen 60 degrees after the last generator firing

APPENDIX 1/1



X3 SAFT 185 TBC -
XB SAMC 19 INF :
MAINLY MEASUREMENT
SIGNALS AND OPERATION
ORDERS TO THE BRIDGE
CONTROL. PART OF THE
FIRING ORDERS TO THE
PULSE AMPLIFIER CARD
ARE ALSO WIRED THROUGH
SAMC 19 INF

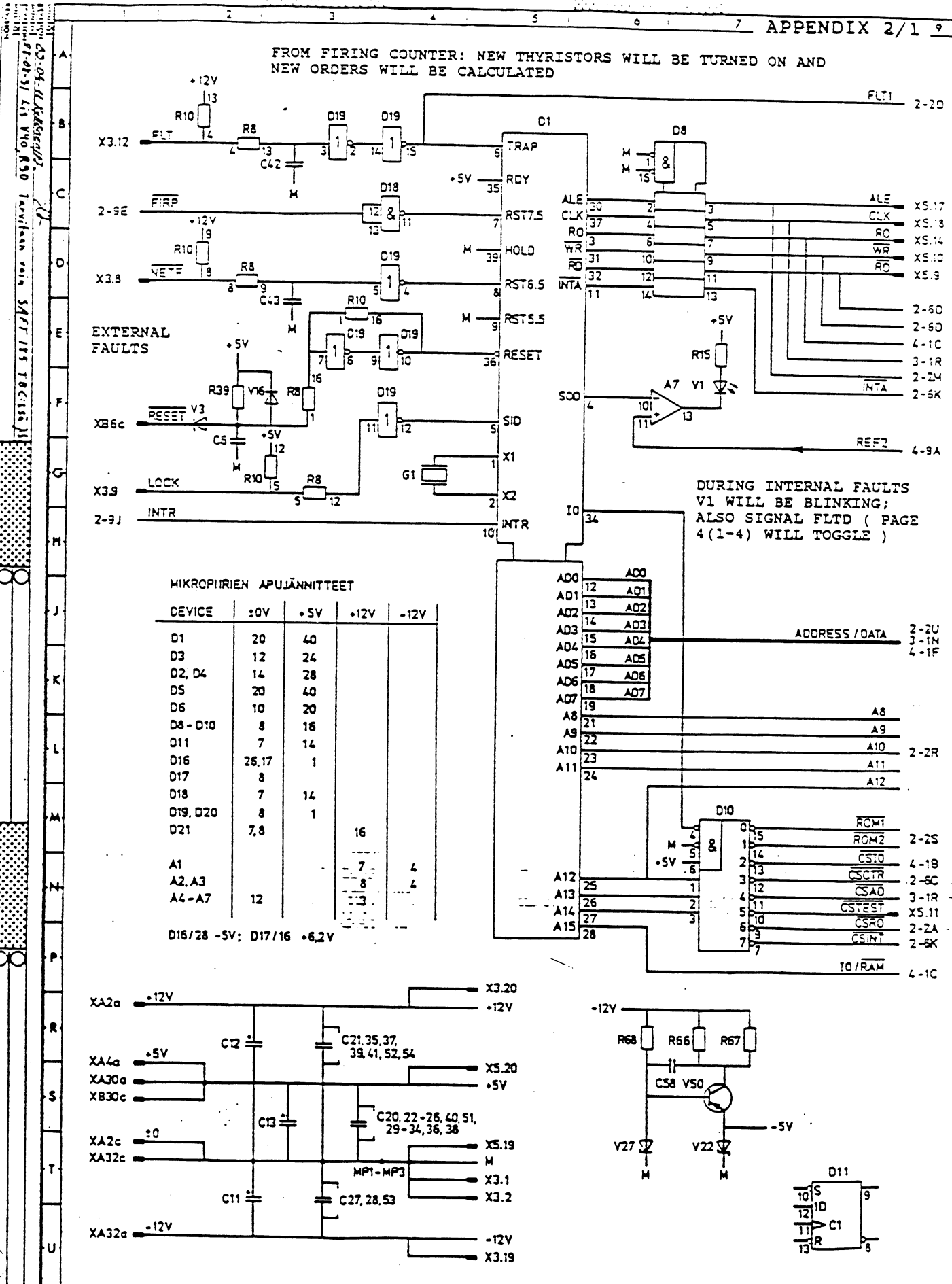
X4 SAFT 185 TBC -
X7 SAMT 11
FIRING ORDERS

5760684-3

Huom. Etutuen alle ei saa asentaa 9mm korkeampia komponentteja.

| | | | |
|--|--|--|--------------------------------|
| <p>1:1</p> <p>Og Strömberg Ab FINLAND</p> | <p>EXH</p> <p>82-08-23 Pannila</p> | <p>VRS-ohjaukskortti</p> <p>SAFT 185 TBC SAFT 185 TBC</p> | <p>SAMI B</p> <p>5719539-3</p> |
|--|--|--|--------------------------------|

FROM FIRING COUNTER: NEW THYRISTORS WILL BE TURNED ON AND
NEW ORDERS WILL BE CALCULATED

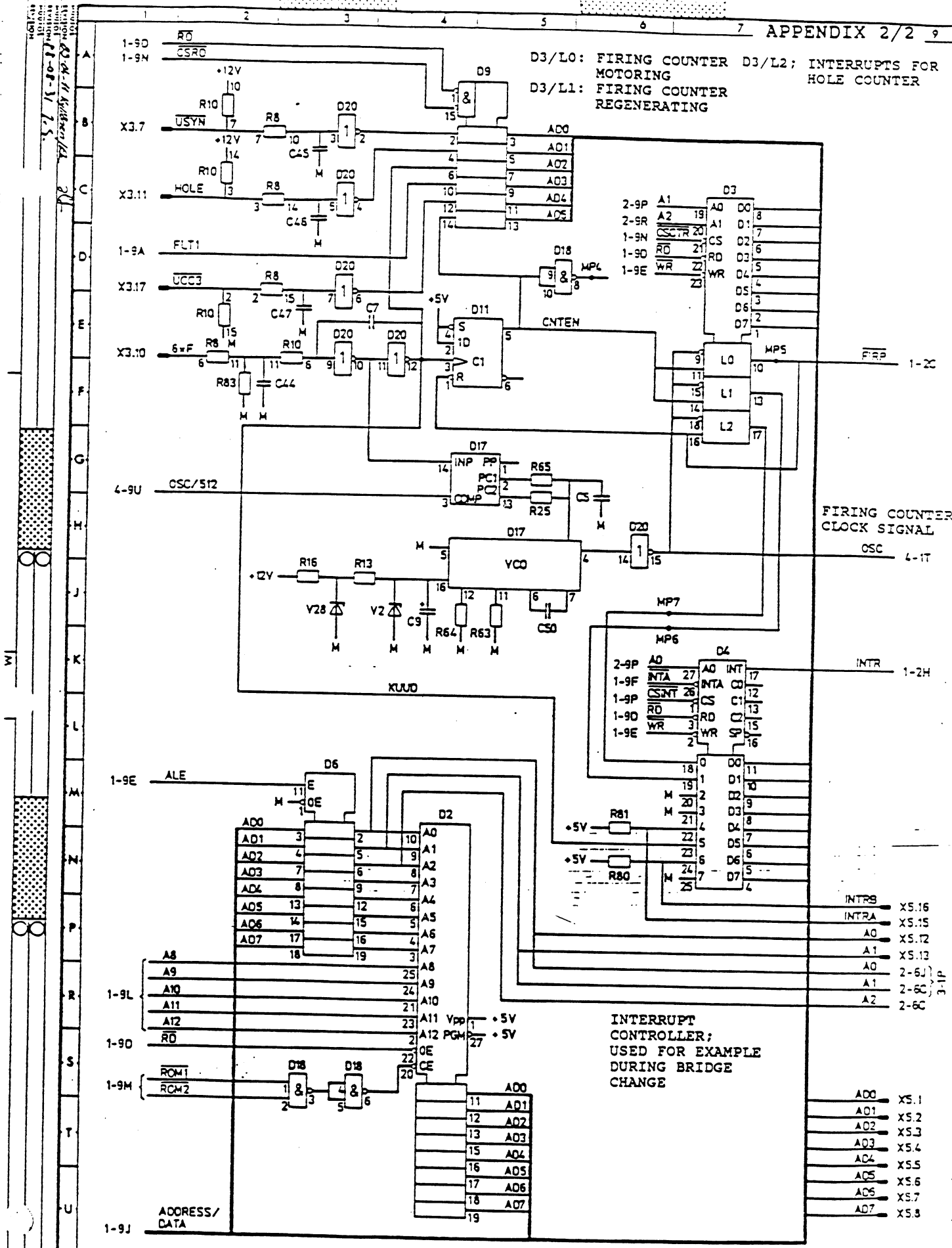


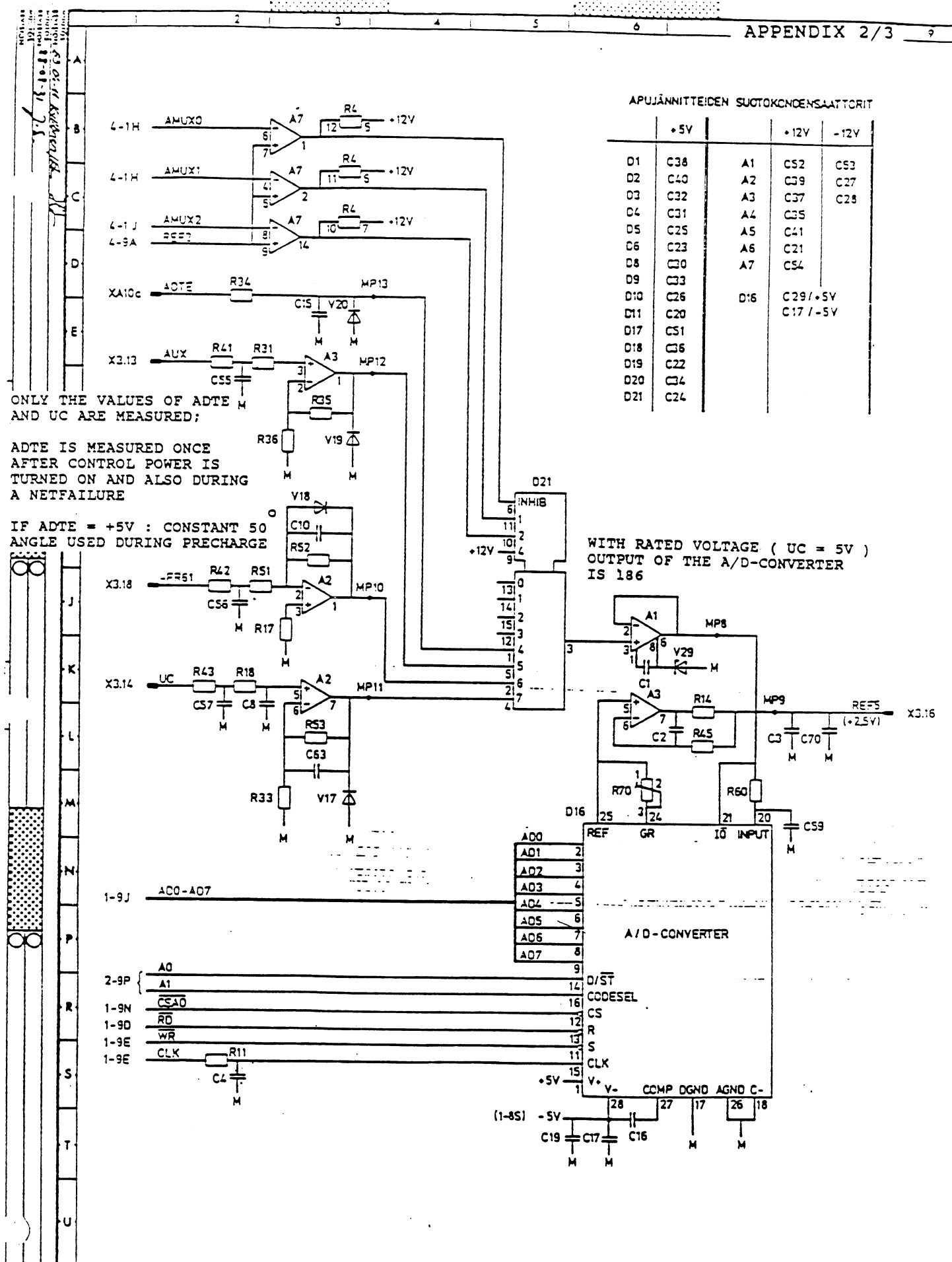
EXTERNAL FAULTS

MIKROPIIRIEN APUJÄNNITTEET

| DEVICE | ±0V | +5V | +12V | -12V |
|----------|--------|-----|------|------|
| D1 | 20 | 40 | | |
| D3 | 12 | 24 | | |
| D2, D4 | 14 | 28 | | |
| D5 | 20 | 40 | | |
| D6 | 10 | 20 | | |
| D8 - D10 | 8 | 16 | | |
| D11 | 7 | 14 | | |
| D16 | 26, 17 | 1 | | |
| D17 | 8 | | | |
| D18 | 7 | 14 | | |
| D19, D20 | 8 | 1 | | |
| D21 | 7, 8 | | 16 | |
| A1 | | | 7 | 4 |
| A2, A3 | | | 8 | 4 |
| A4 - A7 | 12 | | 13 | 4 |

D16/28 -5V; D17/16 +6.2V





APUJÄNNITTEIDEN SUOTOKKOENSAAJOTIT

| | +5V | | +12V | -12V |
|-----|-----|-----|-----------|------|
| D1 | C38 | A1 | C52 | C53 |
| D2 | C40 | A2 | C39 | C27 |
| D3 | C32 | A3 | C37 | C28 |
| D4 | C31 | A4 | C35 | |
| D5 | C25 | A5 | C41 | |
| D6 | C23 | A6 | C21 | |
| D8 | C30 | A7 | C54 | |
| D9 | C33 | | | |
| D10 | C26 | D16 | C29 / +5V | |
| D11 | C20 | | C17 / -5V | |
| D17 | C51 | | | |
| D18 | C36 | | | |
| D19 | C22 | | | |
| D20 | C34 | | | |
| D21 | C24 | | | |

ONLY THE VALUES OF ADTE AND UC ARE MEASURED;

ADTE IS MEASURED ONCE AFTER CONTROL POWER IS TURNED ON AND ALSO DURING A NETFAILURE

IF ADTE = +5V : CONSTANT 50 ANGLE USED DURING PRECHARGE

WITH RATED VOLTAGE (UC = 5V) OUTPUT OF THE A/D-CONVERTER IS 186

| MIRIVI | TUNNUS | MAARA | YKS:DI | KOODI | OSAN NIMITYS | TYYPFI | TEKNISET ARVOT | VALMI. | MAA:HUOH: |
|--------|-------------------|---------|----------|----------|------------------|--------|----------------|--------|-----------|
| 101 | | | | 5719 | 5398:KOKONFAHD | | | | |
| 201 | | | | 5719 | 5371:PIIRIKAAVIO | | | | |
| 301 | | | | | | | | | |
| 401 | | | | | | | | | |
| 501 | | | | | | | | | |
| 601 | | | | | | | | | |
| 651 | | | | | | | | | |
| 661 | | | | | | | | | |
| 671 | | | | | | | | | |
| 701 | | | | | | | | | |
| 801 | | | | | | | | | |
| 901 | | | | | | | | | |
| 1001 | | | | | | | | | |
| 1101 | | | | | | | | | |
| 1201 | | | | | | | | | |
| 1301 | | | | | | | | | |
| 1401 | | | | | | | | | |
| 1501 | | | | | | | | | |
| 1601 | | | | | | | | | |
| 1701 | | | | | | | | | |
| 1801 | | | | | | | | | |
| 1901 | | | | | | | | | |
| 2001 | | | | | | | | | |
| 2101 | | | | | | | | | |
| 2201 | | | | | | | | | |
| 2301 | | | | | | | | | |
| 2401 | | | | | | | | | |
| 2501 | | | | | | | | | |
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| 2701 | | | | | | | | | |
| 2801 | | | | | | | | | |
| 2901 | | | | | | | | | |
| 3001 | | | | | | | | | |
| 3101 | | | | | | | | | |
| 3201 | | | | | | | | | |
| 3301 | | | | | | | | | |
| 3401 | | | | | | | | | |
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| 3601 | | | | | | | | | |
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| 3801 | | | | | | | | | |
| 3901 | | | | | | | | | |
| 4001 | | | | | | | | | |
| 4101 | | | | | | | | | |
| 4201 | | | | | | | | | |
| 4301 | | | | | | | | | |
| 4401 | | | | | | | | | |
| 4501 | | | | | | | | | |
| 4601 | | | | | | | | | |
| 4701 | | | | | | | | | |
| ABB | SIRBERG DRIVES OY | FINLAND | TUNNUS | | | | | | |
| ALKUF. | A | TSTO | EXF | | | | | | |
| FVM | 880901 | SUOHN. | JUUIA | SUOHINEN | | | | | |
| VIIH. | A | TSTO | EXF | | | | | | |
| FVM | 880901 | SUOHN. | SUOHINEN | | | | | | |
| FVM | | TANK. | | | | | | | |
| FVM | 880901 | HYV. | SUOHINEN | | | | | | |

NIMITYS
 OHJAUSKORTTI
 SAFT 185 18C
 SAFT 185 18C
 SAHI F VASTARINNAKSILLAN OHJAUSKORTTI
 KÄYTTÖKOHDE SAMI F
 LISÄTIEDOT
 SAHI F VASTARINNAKSILLAN OHJAUSKORTTI
 FLKA 85610
 LEHTI
 KITI
 KODI
 VE 5811 9607

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| | |
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| Appendix 1/1 | SAMC 11 POW layout |
| Appendix 2/1 | SAMC 11 POW circuit diagram |
| Appendix 3/1 | SAMC 11 POW parts list |
| 3/2 | |

1. Block diagram

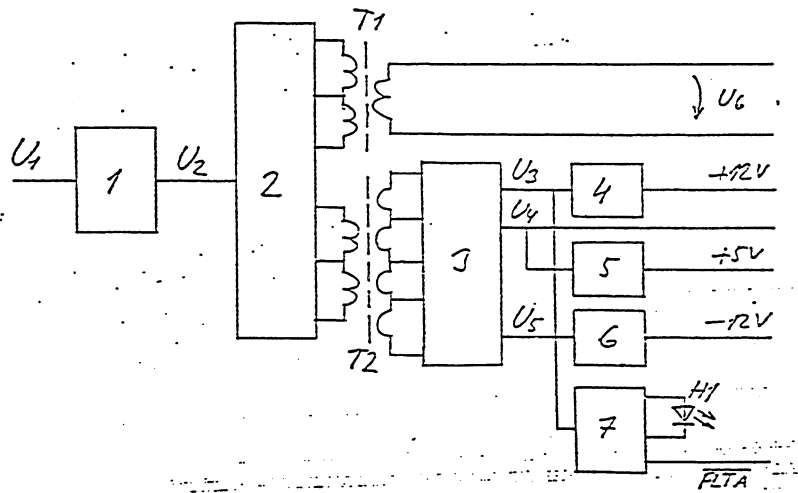


Figure 1. SAMC 11 POW block diagram

- block 1: Stabilized transformer. Input voltage $U_1 = 30 \dots 55V$. Output voltage $U_2 = 55V$.
- block 2: Push-pull inverter to the transformers T1 and T2.
- block 3: Rectifies and filters the secondary voltages from transformers T1 and T2. Output voltages $U_3 = + 15 V$, $U_4 = + 8.5 V$ and $U_5 = - 15 V$
- blocks 4, 5 and 6: Integrated voltage regulators. Output voltages:
 - block 4 = + 12 V
 - block 5 = + 5 V
 - block 6 = - 12 V
- block 7: Control voltage supervision; if level of the voltage U_3 is too low this block will sen a fault signal FLTA to the control unit and it will turn off the LED H1
- from the transformer T1 a 25V 35kHz square wave voltage for the pulse amplifier cards

2. Stabilized power supply

2.1 Operation principle

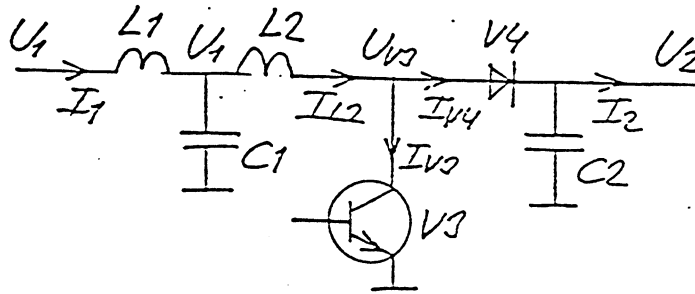


Figure 2.

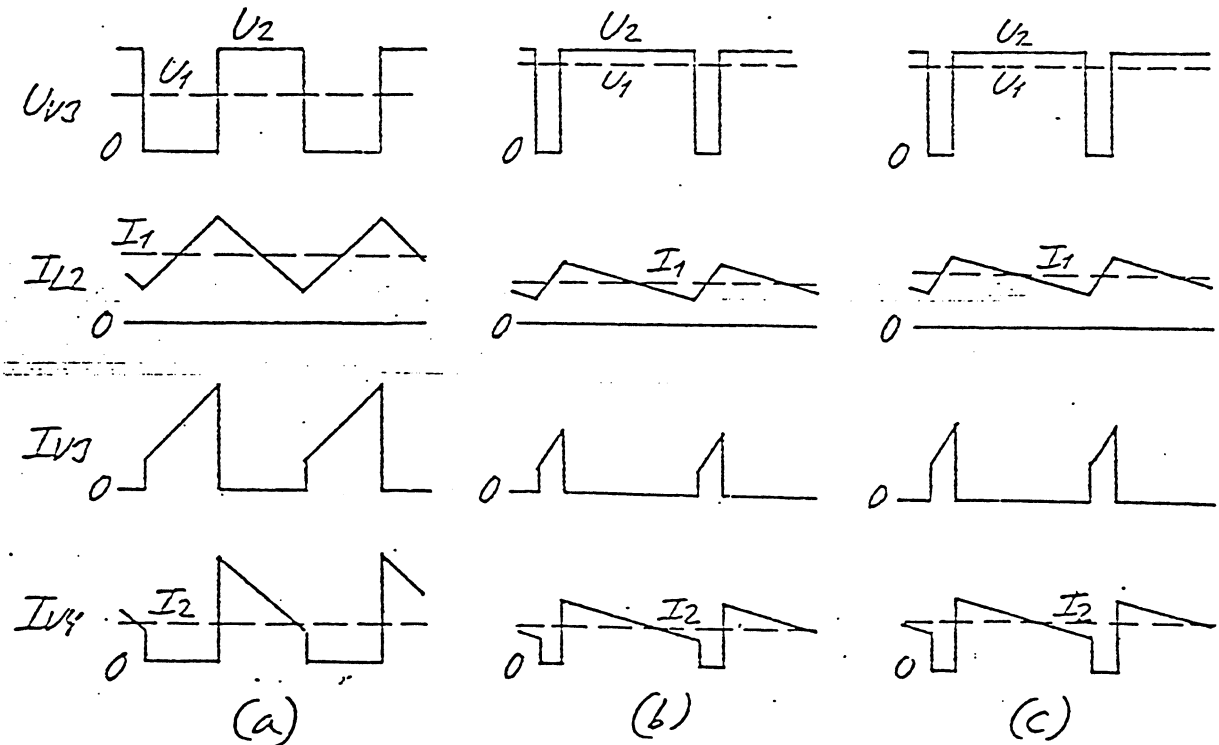


Figure 3.

Stabilized power supply main circuit diagram is in figure 1 and current and voltage wave shapes related to it in figure 2:

- choke L1 and capacitor C1 are used to reduce the noise level reflected back to the supply side and produced by the switching of V3
- stabilized power supply L2, V3, V4 and C2 operation:
 - when V3 is turned on the current through it increases based approximatel to the time constant of L2 and the voltage level of U1
 - when V3 is turned off current starts to flow through the diode V4 and current decreases based to the voltage difference U2-U1. Capacitor C2 is charged during this period; C2 is large enough to keep the voltage level constant
 - by controlling the operating cycle of V3 so that average value of current IV4 equals the load current the voltage of C2 will remain constant
- figure 3a; operation with low input voltage
- figure 3b; same load current as in figure 3a, but with a higher input voltage
- figure 3c; same input voltage as in figure 3b, but with a higher load current
- automatic control system will produce a constant 55Vdc voltage (= UC2)

2.2 Control circuitry operation

Control circuit diagram in figure 4 and wave shapes related to it in figure 5.

- actual voltage U52 (= output voltage U2 scaled by resistors R17 and R19) is compared to a constant reference 2.86 and these to voltages are used as inputs to the differential amplifier in the integrated regulator circuit A5

- voltage U45 (= output from the integrated regulator circuit A5 modified by zener diode V18) and voltage U44 (= function of the input voltage U1 and the current IV3 of the transistor V3) are used as inputs to the comparator A4
- every other rising edge of the clock oscillator (oscillator frequency = 70kHz) switches the output of the flip-flop D2 to "1"-state:
 - transistors V2 and V3 are turned on and voltage U44 value will increase
 - when voltage U44 exceeds the value of U45 the output of flip-flop D2 will change from "1"- to "0"-state and transistors V2 and V3 will be turned off
- if voltage U2 starts to decrease differential amplifier will increase the level of U45 which allows U44 to reach higher values before V2 is turned off. This means that the input power increases and voltage U2 remains constant regardless of the higher loading. Similar operation when load decreases.

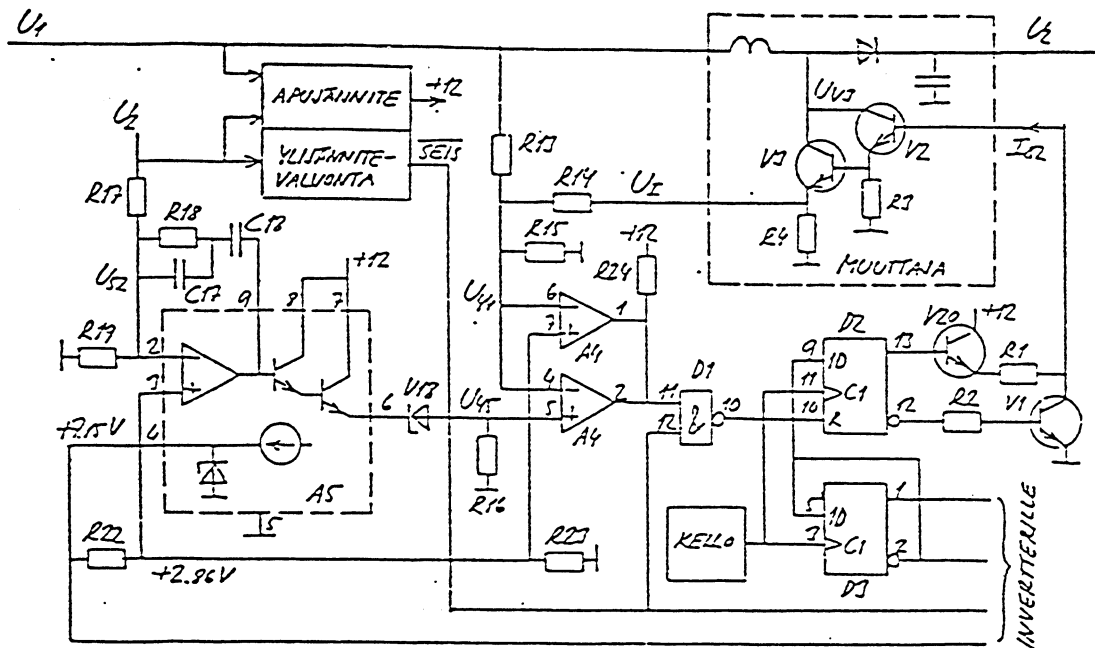
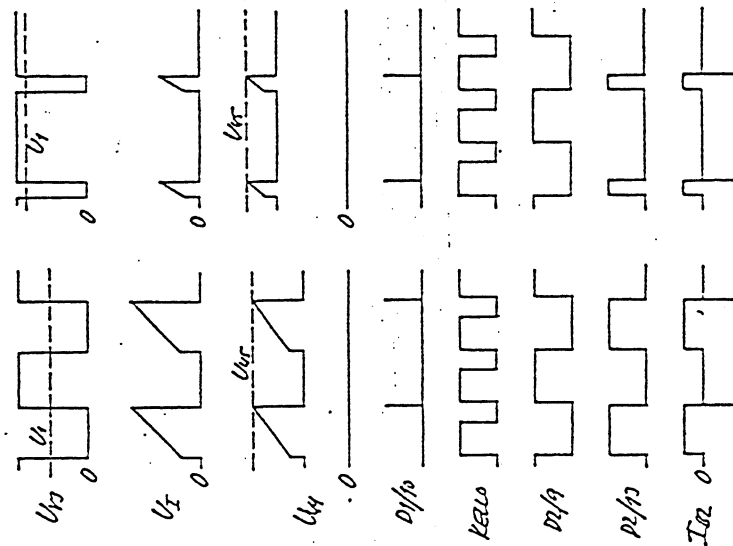


Figure 4. SAMC 11 POW control circuitry



- figure 5a: wave shapes with low input voltage (30 V)
- figure 5b: wave shapes with high input voltage (48 V)

Figure 5. SAMC 11 POW control circuit signals

Current is limited based to the voltage U44; if U44 value exceeds 2.86 volts (equals 2A output current) an additional comparator will force the output of D2 to zero. Current limiting will operate only when $U2 \geq U1$ and because of this U2 isn't short circuit protected by this controller but with current limiting in the inverter section (check chapter 3.2).

Fault in the control circuitry can cause output voltage to increase to a high value, which could cause damage to the semi-conductors in the main circuitry. To inhibit possibility of circuit failures an additional overvoltage limit has been added which will turn off pulses and control from the inverter if safe operation area of U2 is exceeded.

3. Push-pull power supply

3.1 Operation principle

In figure 6 is the main circuitry of the inverter and in figure 7 the signal wave shapes related to it. Signals UV21 and UV22 control power fets V5 (IV5 in figure 7) and V6 (IV6 in figure 7) to form a square wave input voltage to the transformers T1 and T2.

Secondary voltages of T1 and T2 are rectified and filtered:

| T1 output | T2 output |
|--------------|-----------------------------------|
| U3 = + 15 V | U6 = 25V 35kHz (I6 in figure 7) |
| U4 = + 8.5 V | |
| U5 = - 15 V | |

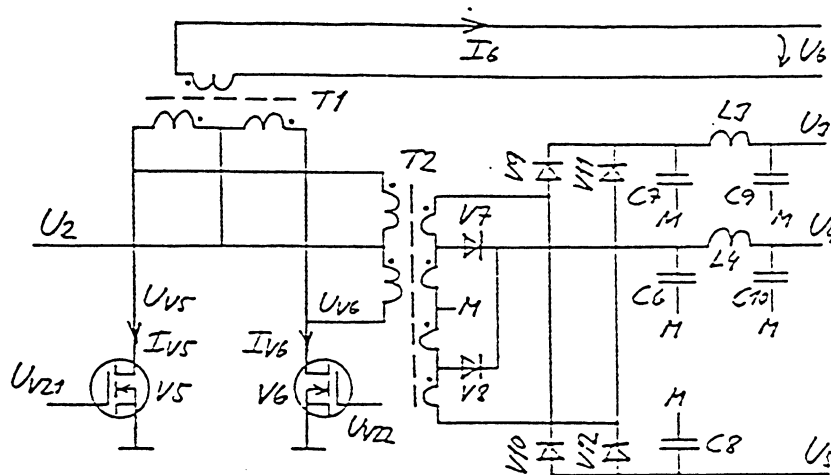


Figure 6. Push-pull inverter

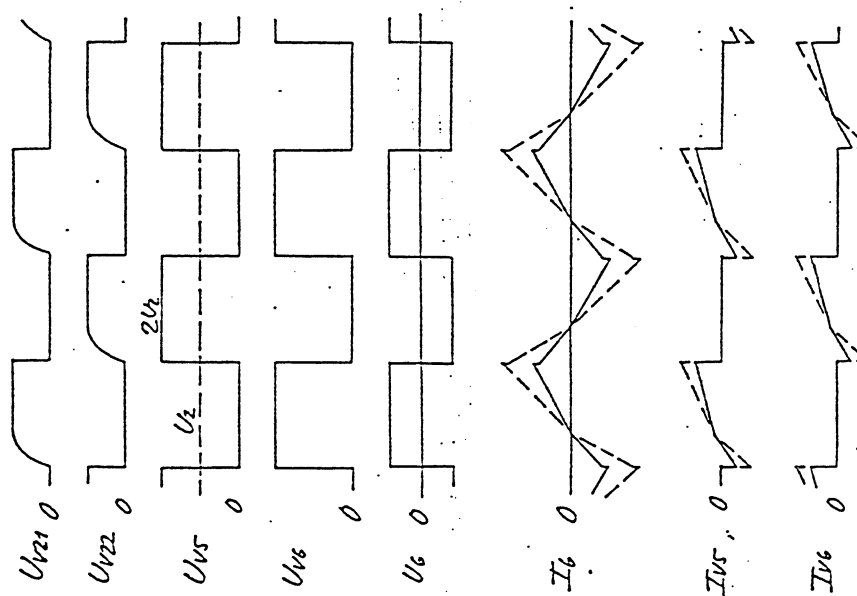


Figure 7. Signal wave shapes in the push-pull inverter

3.2 Control circuitry operation

Inverter control circuit diagram in figure 8 and signal wave shapes related to it in figure 9a.

Components A4, C20, R34...R36 form the 70kHz oscillator circuit (output = A4/13). With the flip-flop D3 this 70kHz is used to produce two opposite phase 35kHz signals, which via the NAND circuits D1 control transistors V21 and V22. Collector voltages of these transistors control the power FETs V5 and V6 in the main circuit.

Oscillator circuit includes also compensation to take care of different resistance values in the power FETs.

Voltage U49 is formed by the resistors R7...R10 from currents IV5 and IV6; if this voltage exceeds reference level U48 the output of flip-flop D3 (D3/12) will remove the ON-signal from corresponding FET for the rest of the cycle. This signal has also an effect to the reference level U48 via resistor R54; earlier the D3/12 goes to zero the lower the level of U48. In figure 9b wave shapes during current limiting period.

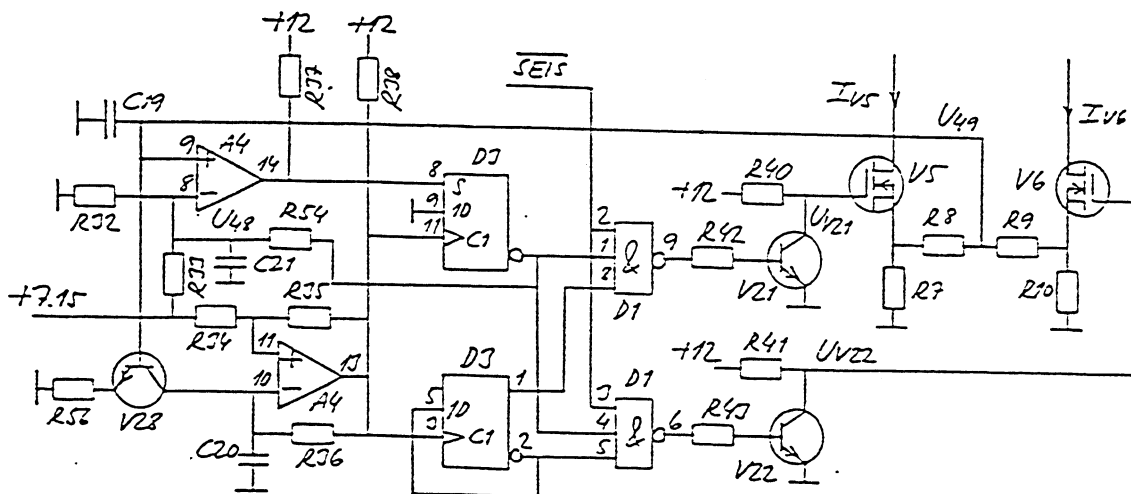


Figure 8. Control circuit of the push-pull inverter

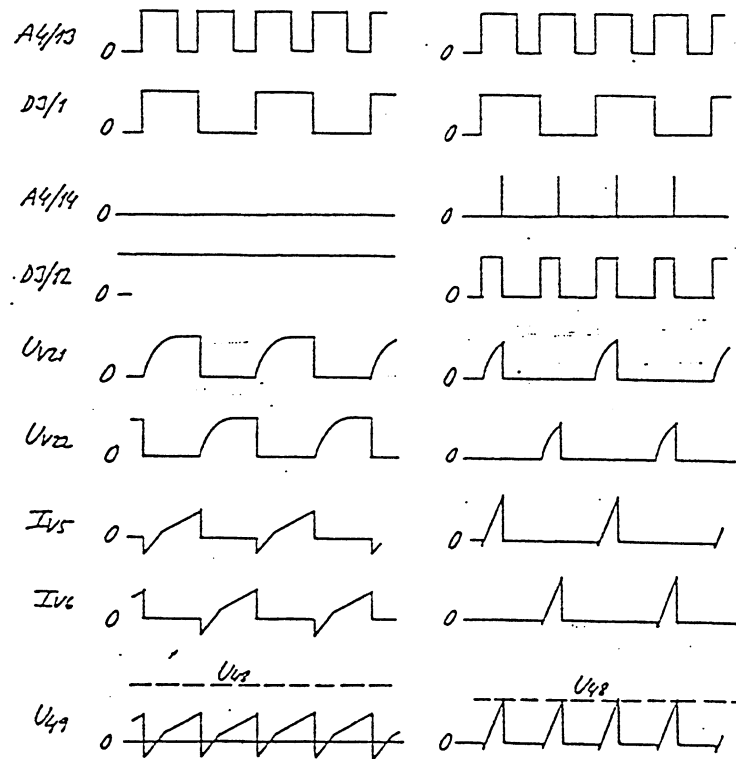


Figure 9. Signal wave shapes in the push-pull inverters control circuitry:
a: normal operation b: current limiting operation

4. Output circuits

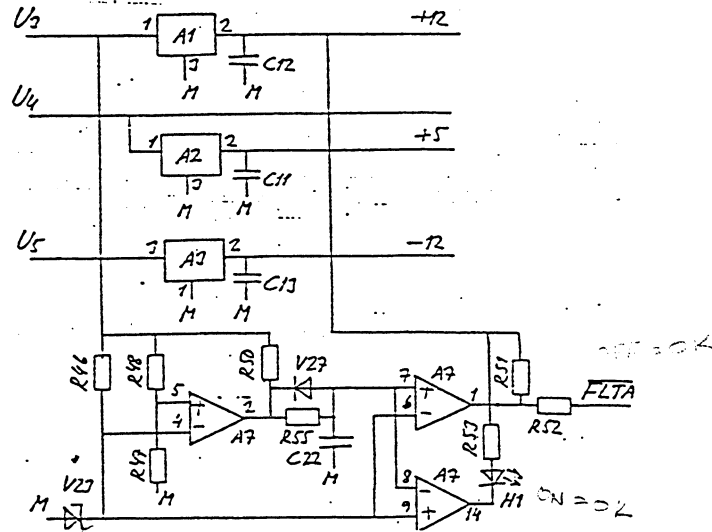


Figure 10. Output circuits

Secondary voltages of transformers T1 and T2 are rectified and filtered and then stabilized by integrated regulators; output voltages: $U_3 = +15\text{ V}$, $U_4 = +8.5\text{ V}$ and $U_5 = -15\text{ V}$.

If voltage level of U_3 goes too low fault signal \overline{FLTA} will be sent to the control unit and LED H1 will be turned off. If voltage level returns above the threshold level the fault signal will be removed after 0.15 seconds and LED H1 will be turned on again.

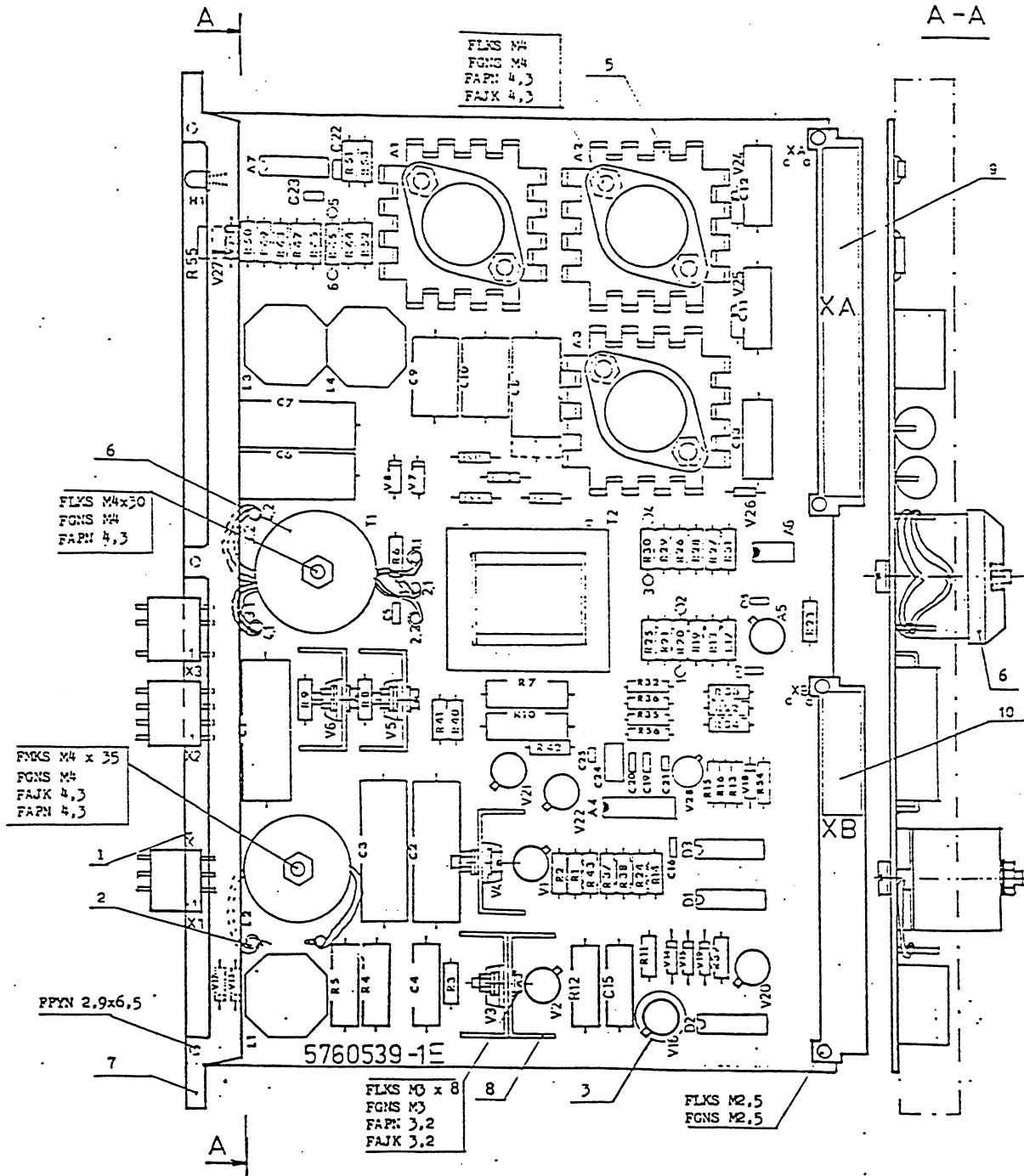
5. Characteristics

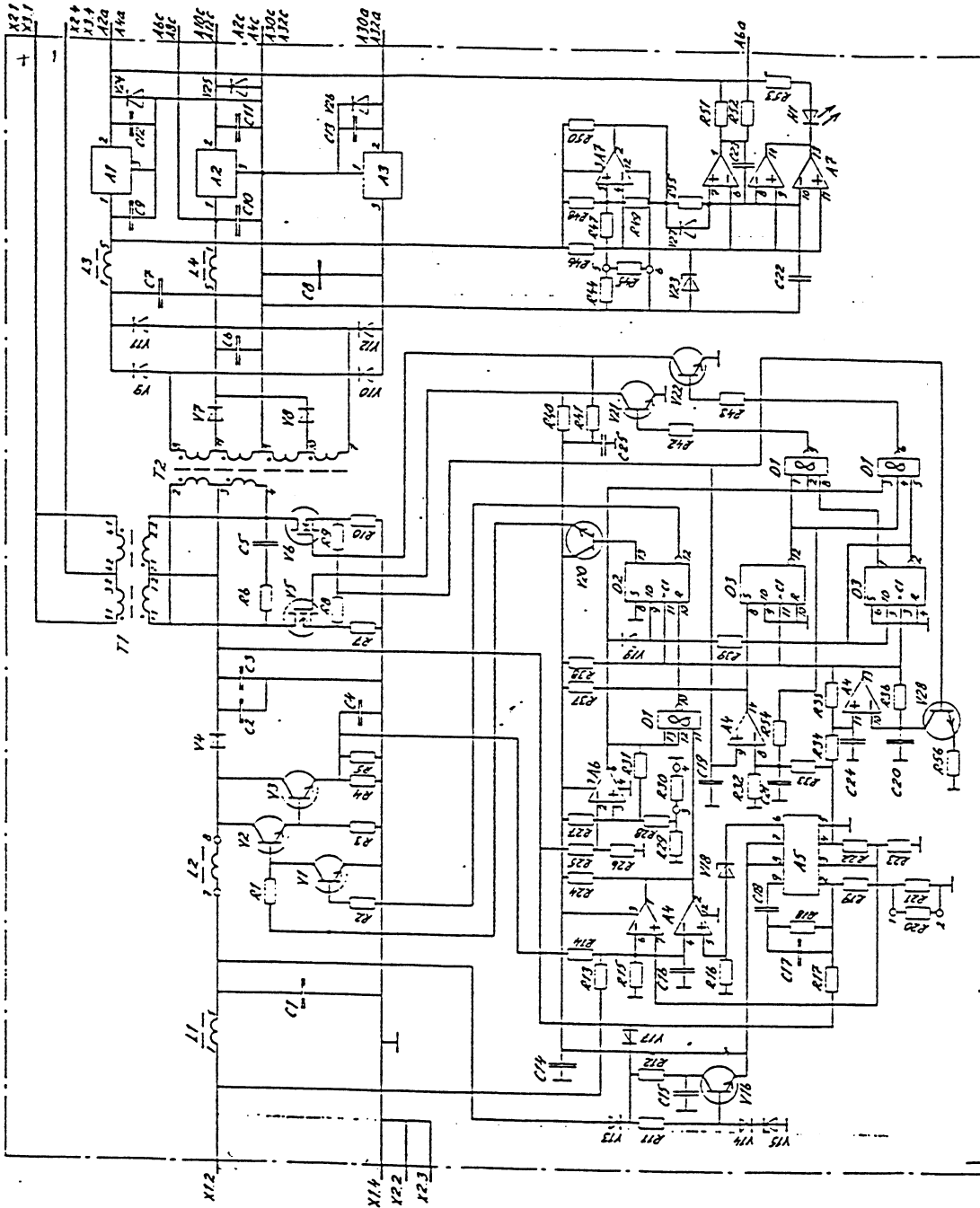
| | | |
|---------------------------|------------|----------------------|
| Input voltage | 30...55V | |
| Output voltages | +12V | 1.0A |
| | +5V | 1.5A |
| | -12V | 0.5A |
| | +25V | 30W (50W instant.) |
| | | 35kHz square wave |
| Rated input power | 80W | o |
| Rated ambient temperature | +5...+55 C | |

Allen-Bradley/Stromberg
03-16-89
R.Ahola

SAMC 11 POW
block diagram

APPENDIX1/1





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16

21

| M: RIVI | TUNNUS | MAKRX YKS: KOODI | OSAN NIMITYS | LAJMERKKI | TEKNISET ARVOT | VALMIST. MAAP. IIR. :SIU. |
|---------|--------------|------------------|--------------------|----------------|-----------------------|---------------------------|
| B: 510: | IC15 | 1 KPL:09831568 | KONDENSAATTORI | MKC 1. 51 | 0, 22UF 63V 10% | FARCO |
| B: 520: | IC5 | 1 KPL:09830154 | KONDENSAATTORI | 2222 563 02471 | 1470 PF 500V 20% | FARCO |
| B: 530: | IC6, 7, 8 | 3 KPL:09831657 | KONDENSAATTORI | MKC 1. 51 | 4, 7 UF 63V 10% | FARCO |
| B: 540: | IC9, 10, 13 | 3 KPL:09831631 | KONDENSAATTORI | MKC 1. 51 | 2, 2UF 63V 10% | FARCO |
| B: 550: | XC14 | 3 KPL:35030892 | KONDENSAATTORI | 3418 100A 101K | 1100PF 100V 10% | AVX |
| B: 560: | IC16, 17, 19 | 3 KPL:09830219 | KONDENSAATTORI | CM20 C 473 MC | 47 NF 50V 20% | USCC |
| B: 570: | IC18, 21, 22 | 1 KPL:09830219 | KONDENSAATTORI | CM20 C 473 MC | 47 NF 50V 20% | USCC |
| B: 580: | IC25 | 1 KPL:35030906 | KONDENSAATTORI | 3429 100A 102K | 11000PF 100V 10% | AVX |
| B: 600: | IC23 | 1 KPL:35030922 | KONDENSAATTORI | 3429 100A 471K | 1470PF 100V 10% | AVX |
| B: 610: | IC24 | 1 KPL:09831029 | KONDENSAATTORI | 2222 630 04221 | 220PF 63V +-20% | PHILIPS |
| B: 619: | | | | | | |
| B: 629: | | | | | | |
| E: 630: | IH1 | 1 KPL:09809354 | LIQSTEDIODI+KAULUS | MV5253+MP52 | IF=20MA 1, 5MCD TYP | MONSANTO |
| B: 640: | IV4 | 1 KPL:35063102 | DIODI | BYU80-150 | 150V AV7A 50NS | SECOSEM |
| C: 650: | IV7, 8 | 2 KPL:35064485 | DIODI | BYU9B-100 | 100V AVIA 50NS | SECOSEM |
| B: 660: | IV9-12 | 3 KPL:09803238 | DIODI | IN4936 | 400V AVIA 200NS | FAIRCHILD |
| D: 670: | IV13, 17, 24 | 3 KPL:09803718 | DIODI | IN4004 | 400V AVIA | |
| D: 671: | IV25, 26 | 2 KPL:09803718 | DIODI | IN4004 | 400V AVIA | |
| D: 680: | IV14, 19, 27 | 3 KPL:09803190 | DIODI | IN4148 | 75V AV0, 15A 4NS | |
| B: 690: | IV15 | 1 KPL:09808264 | ZENERDIODI | IN 759 A | 12 V 0, 4W | PHILIPS |
| B: 700: | IV18 | 1 KPL:09808213 | ZENERDIODI | IN 750 A | 4, 7V 0, 4W | PHILIPS |
| B: 710: | IV23 | 1 KPL:09808230 | ZENERDIODI | IN 754 A | 6, 8V 0, 4W | PHILIPS |
| B: 720: | IV1, 20-22 | 4 KPL:09806601 | TRANSISTORI | 2N 2219 A | NPN 0, 5 A 40 V | |
| B: 730: | IV2, 16 | 2 KPL:09806482 | TRANSISTORI | 185W 68 | NPN 1 A 150 V | |
| B: 740: | IV3 | 1 KPL:35064460 | TRANSISTORI | 18U 406 | NPN 5A 200V | SGS |
| H: 745: | IV28 | 1 KPL:09806458 | TRANSISTORI | 2N 2369 | NPN 0, 1 A 15 V | MOTOROLA |
| B: 750: | IV5, 6 | 2 KPL:35064478 | MOS-TRANSISTORI | IRF 632 | 5A 200V 0, 6 OHM | IR |
| B: 760: | IA1 | 1 KPL:09813616 | REGULAATTORI | 5G7B12CK | POSIT 12V 1A | SIL GEN |
| B: 770: | IA2 | 1 KPL:09813420 | JANN. REGULAATTORI | 1309 | 5 V 1A | |
| B: 780: | IA3 | 1 KPL:09813624 | REGULAATTORI | 5G7912CK | NEGAT 12V 1A | SIL GEN |
| B: 790: | IA4, 7 | 2 KPL:09813365 | 4 X KOMPANAATTORI | CA 139 G | 10S 25 NA | RCA |
| B: 800: | IA5 | 1 KPL:09813403 | JANN. REGULAATTORI | 723 | 2...37V 0, 15A | |
| B: 810: | IA6 | 1 KPL:09813489 | OPER. VAHVISTIN | 101 A | 10S 10 NA | |
| B: 820: | ID1 | 1 KPL:09816372 | MIKROPIIRI | MC 14023 BAL | CHDS 3X3-INPUT NAND | |
| B: 830: | ID2, 3 | 2 KPL:09816275 | MIKROPIIRI | MC 14013 BAL | CHDS DUAL D FLIP-FLOP | |
| B: 849: | | | | | | |
| B: 850: | | 51521901 | TARKASTUSOHJE | | | |
| G: 860: | 9 | 1 KPL:57162465 | ARVOKILPI | * | B2X10 | MAIN+ETIK:FI |
| G: 870: | 10 | 1 KPL:35055398 | SARJANUMEROKILPI | * | 10 X 30 MM TARRAKILPI | FI |
| B: 899: | | | | | | |
| J: 900: | 20 | 2 KPL:57168901 | LUKITUSRUUVI | * | M4 | NOMET |
| J: 910: | 21 | 2 KPL:35064982 | PIDATINLAATTA | FSTJ/B 4 | 6, 5/3, 2X0, 3 | FI |

OSALUETTELO

TEKN. ARVOT

KÄYTTÖOHJE

LAJMERKKI

SAMI B

NIMITYS

JANNITELÄNIDE

LAJMERKKI

SAMC 11 POW

JAKELU

LEHTI

2 (2)

KOKO A3

ALKUP. VERSIO A OSASTO

PVM B10305 SUUNN. LOUNILA

VIH. VERSIO J OSASTO EXP

PVM B11214 SUUNN. LASBILA

| H: RIVI | TUNNUS | MÄÄRÄ YKS: | KOODI | OSAN NIMITYS | LAJIMERKKI | TEKNISET ARVOT | VALMIST. | MAA: | PIIR. | ISIJ. |
|---------|-------------|------------|---------------|---------------------------|------------------------|-------------------|----------|------|-------|-------|
| B: 10: | | | 37171833 | PIIRIKAAVIO | | | | | | |
| B: 20: | | | 57171863 | KOKOONPANO | | | | | | |
| B: 30: | | 1 | KPL: 57603391 | PIIRILEVY | 5760339-1 | REV. E | | | | |
| B: 40: | X1, 2, 3 | 3 | KPL: 09883231 | KORTTILIITIN | 2420-4A | 32-NAP. | | | | |
| B: 50: | XA, XD | 2 | KPL: 09882111 | KORTTILIITIN KOIRAS | 32-NAP. | | | | | |
| B: 60: | | 13 | KPL: 09878858 | JUOSTOSTAPPI | RM 1, 3/3, 5/9, 7, 002 | 12, 5/BXB | | | | |
| B: 70: | | 1 | KPL: 09880160 | JÄKÄHD. ELEM. TOS:LLE | SKK 38 | 25, 4X30X12, 7 | | | | |
| B: 80: | | 5 | KPL: 09880178 | JÄKÄHD. ELEM. TOS:20:ILLE | IFK 216/SA-CB | | | | | |
| B: 90: | | 3 | KPL: 09880143 | JÄKÄHDYTYSELEMENTTI | IK 42-3 | | | | | |
| B: 100: | L1, 3, 4 | 3 | KPL: 57580445 | SAMI-KURISTIN | | 50UH 3, 5A | | | | |
| B: 110: | L2 | 1 | KPL: 57171812 | SAMI-KURISTIN | | 150UH 5A | | | | |
| B: 120: | T1 | 1 | KPL: 57171910 | PULSSIMUUNTAJA | | | | | | |
| B: 130: | T2 | 1 | KPL: 57171839 | SAMI-MUUNTAJA | | | | | | |
| H: 140: | | 1 | KPL: 57163445 | ETUTUKI | CXD | | | | | |
| B: 159: | | 1 | KPL: 09911154 | IVASTUS | CR 37 | 150 OHM 0, 5 W 5% | | | | |
| B: 160: | R1 | 3 | KPL: 09912711 | IVASTUS | MR 25 | 6, 8K 0, 3W 2% | | | | |
| B: 170: | R2, 42, 43 | 3 | KPL: 09911057 | IVASTUS | CR 37 | 22 OHM 0, 5 W 5% | | | | |
| B: 180: | R3 | 2 | KPL: 09914471 | IVASTUS | B10 | 1, 0 OHM 4 W 5% | | | | |
| B: 190: | R4, 5 | 1 | KPL: 09911138 | IVASTUS | CR 37 | 100 OHM 0, 5 W 5% | | | | |
| B: 200: | R6 | 2 | KPL: 09914421 | IVASTUS | B10 | 0, 39 OHM 4 W 5% | | | | |
| B: 210: | R7, 10 | 2 | KPL: 09912657 | IVASTUS | MR 25 | 2, 2K 0, 3W 2% | | | | |
| B: 220: | R8, 9 | 3 | KPL: 09912657 | IVASTUS | MR 25 | 6, 8 K 0, 5 W 2% | | | | |
| B: 230: | R17, 40, 41 | 1 | KPL: 09911359 | IVASTUS | MR 30 | 560 OHM 4 W 5% | | | | |
| B: 240: | R11 | 3 | KPL: 09915435 | IVASTUS | B10 | 22 K 0, 3W 2% | | | | |
| B: 250: | R12 | 3 | KPL: 09912771 | IVASTUS | MR 25 | 1, 2K 0, 3W 2% | | | | |
| B: 260: | R13, 33, 35 | 2 | KPL: 09912682 | IVASTUS | MR 25 | 3, 7K 0, 3W 2% | | | | |
| B: 270: | R14, 53 | 2 | KPL: 09912681 | IVASTUS | MR 25 | 1, 0K 0, 3W 2% | | | | |
| B: 280: | R15, 47 | 2 | KPL: 09912614 | IVASTUS | MR 25 | 47 K 0, 3W 2% | | | | |
| B: 290: | R16, 23, 52 | 3 | KPL: 09912819 | IVASTUS | MR 25 | VALITTAAN | | | | |
| B: 300: | R17, 25 | 1 | KPL: | IVASTUS | | | | | | |
| B: 310: | X, R20 | 1 | KPL: | IVASTUS | | | | | | |
| B: 311: | | 1 | KPL: 09912592 | IVASTUS | MR 25 | 680 OHM 0, 3W 2% | | | | |
| B: 320: | R21 | 3 | KPL: 09912631 | IVASTUS | MR 25 | 1, 5K 0, 3W 2% | | | | |
| H: 330: | R22, 44, 56 | 3 | KPL: 09912738 | IVASTUS | MR 25 | 10 K 0, 3W 2% | | | | |
| B: 340: | R24, 37, 39 | 1 | KPL: 09912738 | IVASTUS | MR 25 | 10 K 0, 3W 2% | | | | |
| B: 350: | R50 | 3 | KPL: 09912650 | IVASTUS | MR 25 | 4, 7K 0, 3W 2% | | | | |
| B: 360: | R26, 27, 46 | 1 | KPL: 09912665 | IVASTUS | MR 25 | 4, 7K 0, 3W 2% | | | | |
| B: 370: | R48 | 1 | KPL: 09912649 | IVASTUS | MR 25 | 2, 7K 0, 3W 2% | | | | |
| B: 380: | R28 | 1 | KPL: 09912649 | IVASTUS | MR 25 | 1, 8K 0, 3W 2% | | | | |
| B: 390: | R29 | 1 | KPL: | IVASTUS | | VALITTAAN | | | | |
| B: 400: | X, R30 | 1 | KPL: | IVASTUS | | | | | | |
| B: 401: | | 1 | KPL: 09912651 | IVASTUS | MR 25 | 100 K 0, 3W 2% | | | | |
| B: 410: | R31 | 3 | KPL: 09912673 | IVASTUS | MR 25 | 3, 3K 0, 3W 2% | | | | |
| B: 420: | R32, 38, 51 | 2 | KPL: 09912703 | IVASTUS | MR 25 | 5, 5K 0, 3W 2% | | | | |
| B: 430: | R34, 18 | 1 | KPL: 09912754 | IVASTUS | MR 25 | 15 K 0, 3W 2% | | | | |
| B: 440: | R36 | 1 | KPL: | IVASTUS | | VALITTAAN | | | | |
| B: 450: | X, R45 | 1 | KPL: | IVASTUS | | | | | | |
| B: 451: | | 1 | KPL: 09911553 | IVASTUS | CR 37 | 330 K 0, 5 W 5% | | | | |
| B: 460: | R49 | 1 | KPL: 09912835 | IVASTUS | MR 25 | 68 K 0, 3W 2% | | | | |
| B: 470: | R54 | 1 | KPL: 09912983 | IVASTUS | 2322 241 | 4, 7M 0, 25W 5% | | | | |
| F: 480: | R55 | 3 | KPL: 09831601 | KONDENSAATTORI | MKC 1, 51 | 10 UF 63V 10% | | | | |
| B: 489: | | 3 | KPL: 09831568 | KONDENSAATTORI | | 0, 22UF 63V 10% | | | | |
| B: 490: | C1, 2, 3 | | | | | | | | | |
| B: 500: | C4, 11, 12 | | | | | | | | | |

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-2-11-81

OSALUETTELO
TEKN. ARVOT
KÄYTTÖKOINTE
LISÄTIEDOT
GAMI B
NIMITYS JÄNNITELÄINDE
LAJIMERKKI SAMC 11 POW
JAKELU
LEHTI 1 (2) KOKO A3

ALKUP. VERSIO A OSASTO
PVH B10305 SUUNN. LDUNILA
VIIM. VERSIO J OSASTO EXP
PVH B11214 SUUNN. LASSILA

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1. Block diagram

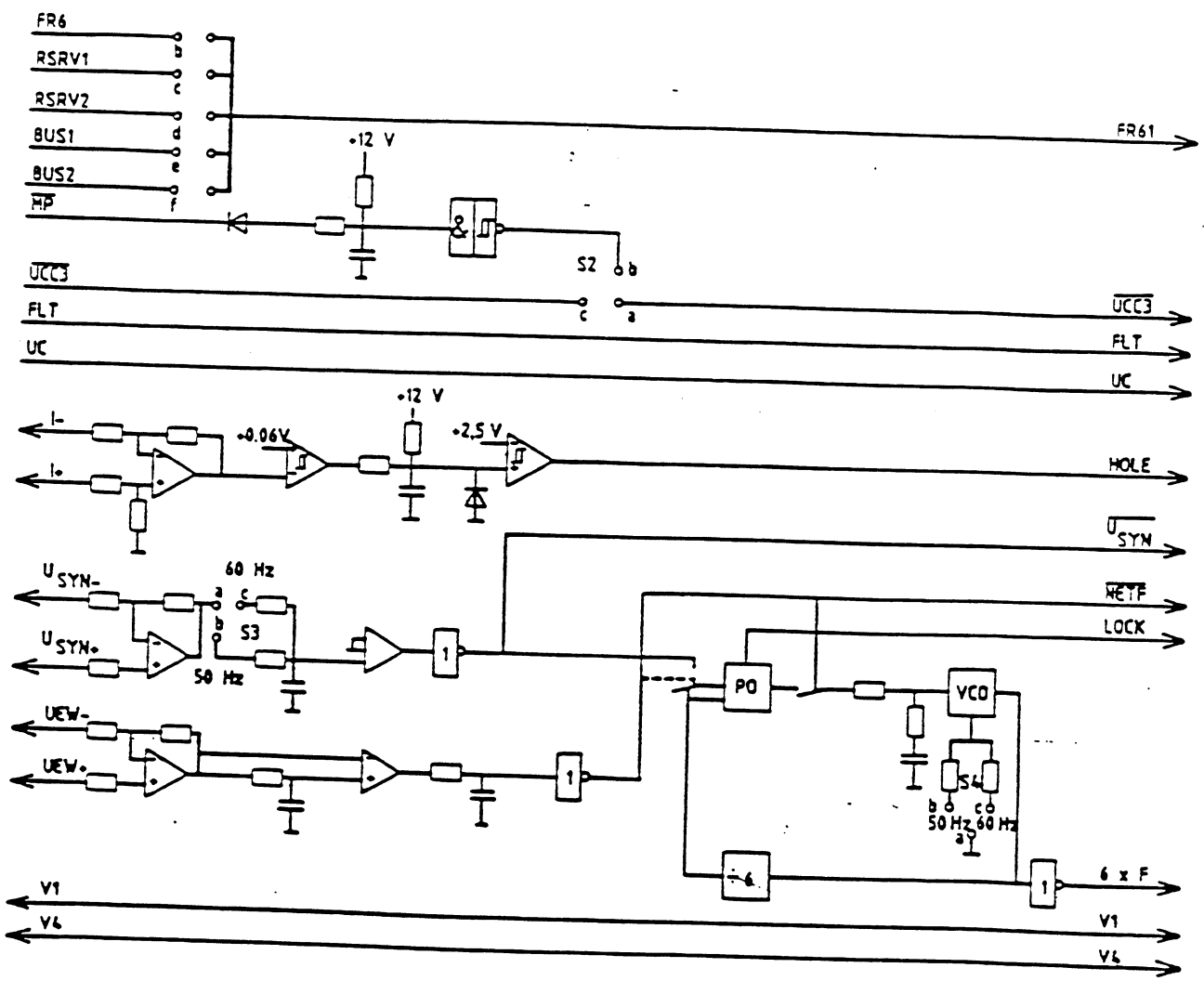


Figure 1. Block diagram of the interface card SAMC 19 INF

2. General

Interface card SAMC 19 INF is located in the control rack (card position A04) and is connected through the mother board to the standard control unit and through two "front" connectors to the pulse amplifier and to the control board SAFT 185 TBC. Card takes care of five main operations:

- forms synchronizing signals
- netfail indication
- forms information about the current continuity/
uncontinuity (= signal HOLE)
- selection of frequency reference

Synchronizing:

Synchronizing signal times the operation of the bridge to the AC-line frequency. UV-main voltage measured by the synchronizing transformer and scaled by the pulse amplifier card is used as an input to the synchronizing circuits; outputs from this circuitry are:

- 50 (60) Hz digital synchronizing signal
- 300 (360) Hz digital synchronizing signal
- netfail signal
- phase failure in synchronizing

Current measurement:

Phase currents U and V are measured by the current transformers and rectified and changed to voltage signal by burden resistors on the pulseamplifier card. From this voltage signal interface card forms the signal HOLE (= current discontinuity signal).

Frequency reference selection:

Start/Stop signal is formed from the signals $\overline{UCC3}$ and $\overline{RUN2}$; control card SAFT 185 TBC will get the information via the interface card.

Firing orders to the thyristors V1 and V4 are wired from the control card through the interface card to the pulse amplifier.

3. Operation of the card

3.1 Synchronizing

Synchronizing signals time the operation of the bridge to the AC-line frequency. U-phase voltage measurement from (pulse amplifier circuit diagram 1(4)) the synchronizing transformer is used as an input to the synchronizing circuits.

Synchronizing transformer (660V / 25V) output is scaled on the pulse amplifier card (figure 2).

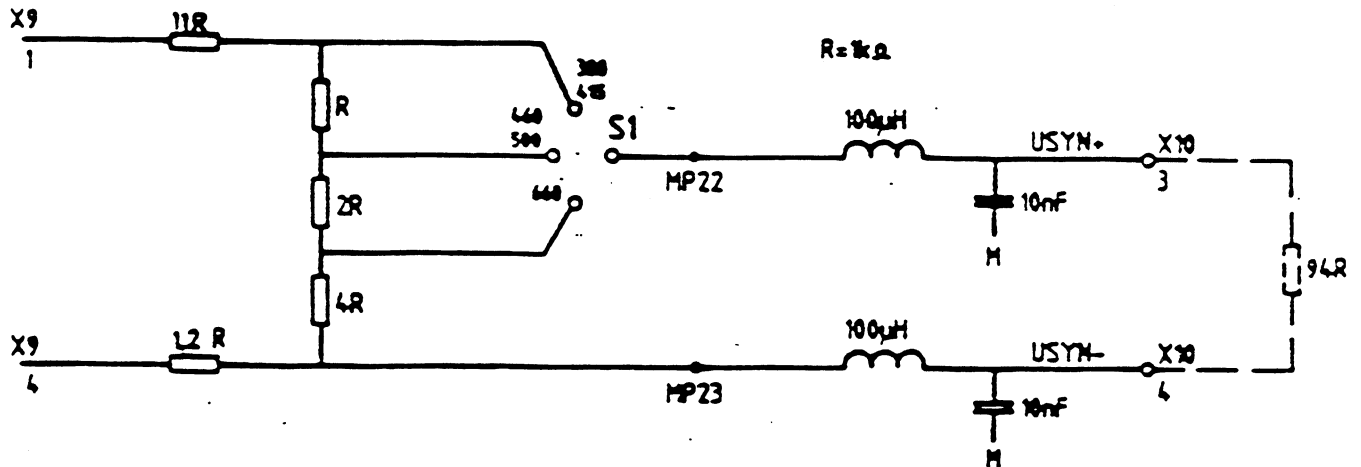


Figure 2. Scaling of synchronizing signal

S1 jumper is used to select the correct scaling for the AC-line voltage used. Scaled line voltage can be measured from between the measuring points MP22 and MP23; LC-circuits are used for filtering of signals USYN+ and USYN-.

On the interface card (Appendix 2/1):

- filtered signal goes to differential amplifier A2; gain is one and -3dB frequency is 34Hz
- jumper S3 is used to select either 50Hz (a-b) or 60Hz (a-c) operation:
 - S3 = a-b: synchronizing is done by RC-circuit R60, C11
 - S3 = a-c: synchronizing is done by RC-circuit R62, C11
 - during manufacturing testing of the card synchronizing is tuned to be accurate by the potentiometers R60 and R62

3.1.1 50Hz (60Hz) synchronizing signal (Appendix 2/1)

Delayed sinusoidal synchronizing voltage goes to comparator A5:

- threshold level is $\pm 0V$
- hysteresis is done by the resistors R40 and R23; real threshold levels are $-0.57V$ and $+0.62V$
- buffer D3 is used to form digital synch. signal \overline{USYN}

In figure 3 is an example of synchronizing signal forming.

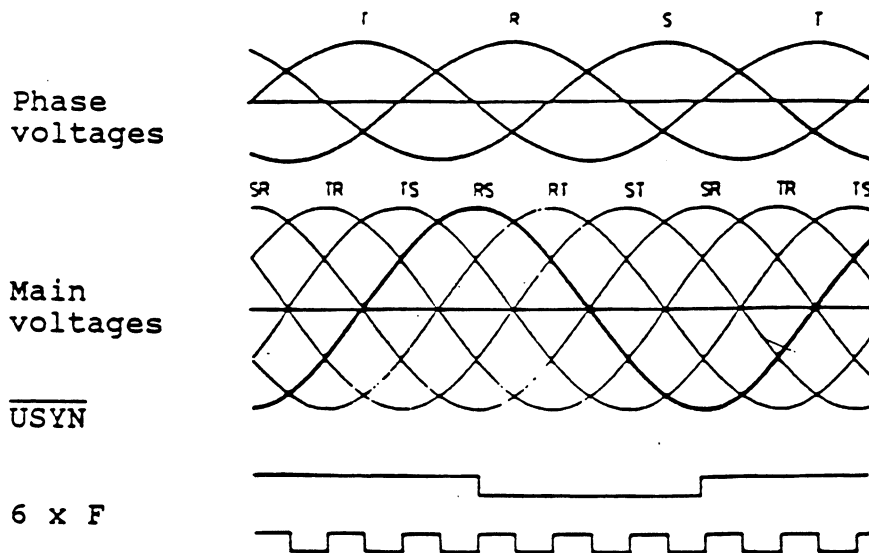


Figure 3. Formation of synchronizing signal

3.1.2 300Hz (360Hz) synchronizing signal (Appendix 2/1)

Thyristor bridge needs six firing pulses during one AC-line cycle and because of this a $[6 \times F(\text{AC-line})]$ synchronizing signal is formed. Phase difference of this signal compared to AC-line phase voltage must be zero; 300(360)Hz signal is done by a phase locked loop (figure 4).

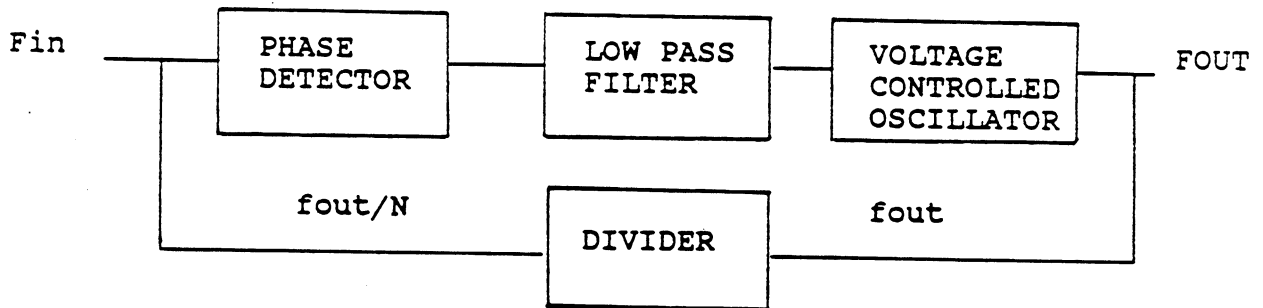


Figure 4. Phase locked loop

Phase locked loop is formed from:

- phase detector
- low pass filter
- voltage controlled oscillator
- divider

Output of the phase detector depends from the phase difference of the input signals:

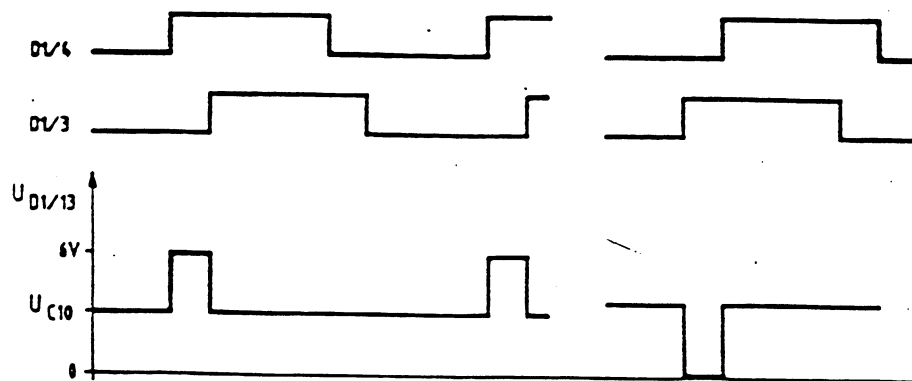
- If input signal phase precedes the signal from the divider (rising edges are compared) the output of the phase detector will increase the control voltage to the voltage controlled oscillator and vice versa
- low pass filter determines the control features of the system, for example response time

Phase detector and voltage controlled oscillator (VCO) are in one circuit D1 (in the circuit diagram Appendix 2/1 detector and VCO are drawn separately).

Operation:

- the input of the phase detector is wired through the selection circuits D4 and D5; selection circuits are controlled by the NETF signal (D4/6):
 - when $\overline{\text{NETF}}$ is one $\overline{\text{USYN}}$ is selected
 - when NETF is zero (failure in AC-line) output from the "divide by 6" is selected; so during a netfail two identical signals are compared. At the same time low pass filter is isolated from the phase detector by V1
 - "comparator" D1/14(input 1) & D1/3(input 2):
 - input 2 is always the 50(60)Hz signal from the divider D2

Phase detector D1(1,2,3,13 and 14) controls the low pass filter (R58,C2,R51 and C10) only when phase difference is detected, otherwise the output of the detector is in high impedance state. Example of the operation in figure 5.



a: AC-phase phase voltage leads b: AC-phase voltage lags

Figure 5. Operation of the phase detector

DC-voltage output of the lowpass filter controls the output frequency of the VCO (D1). Frequency range of the VCO is determined by components C9, R41, R63 and R65 or R64:

- R65 is used with 50Hz ac-line (S4 = a-b) to set the operation point of VCO so that when there's no phase difference between the two frequencies compared the voltage across C10 will be approximately 3.5V
- R64 is used with 60Hz ac-line (S4 = a-c) to set the operation point of VCO so that when there's no phase difference between the two frequencies compared the voltage across C10 will be approximately 3.5V

This output of the VCO is used as an input to the divide by 6 circuit D2 the output of which is used as the other frequency input to the comparator D1.

3.1.3 Synchronizing information LOCK (Appendix 2/1)

This signal is normally in "1"-state. When in "0"-state a phase difference in the frequency is detected.

3.2 Indication of network failures (Appendix 2/2)

Network failure must be detected as fast as possible to save time for the control system to operate, but still it ought to be immune for the glitches and short time disturbances in the AC-phase voltage.

The voltage from the synchronizing transformer is rectified with a 6-pulse bridge on the pulse amplifier board; this rectified voltage UEW- UEW+ is wired to differential amplifier on the SAMC 19 INF interface card (amplifier gain = 1); this signal goes directly to the netfail detection comparator A5/4 (negative input). Same input signal is wired also to the same comparator A5/5 (positive input), but this signal is dampened (A=0.85) and filtered (T=68msec).

Operation of the detection circuit (Appendix 2/2):

- A5/4 value changes fast from "1" to "0" and the output of the comparator A5 goes from "1" to "0".
A5/5 input will follow the change, but with a delay of approximately 68msec.
- signal NETF follows the change in A5 output with a 1msec delay (filtering of glitches is approximately 1 msec)
- diode V24 takes care of correct operation when AC-line is disconnected

3.3 Current uncontinuity information

Control system of the thyristor bridge requires a feedback from the output voltage of the bridge (DC-voltage). Voltage is calculated from the firing angle and from the current transition time; current transition time can be calculated by measuring the uncontinuity of the current.

Current is measured by to current transformers and rectified on the pulse amplifier board.

Operation of the measurement circuit (Appendix 2/2):

- signal to the differential amplifier A1 (amplifier gain = 1)
- output of the amplifier A1 is filtered by R9 and C7
- filtered signal to comparator A6/8 (negative side)
- reference voltage to the comparator tuned by potentiometer R61 to 60 mV in SAMI BQ and 80mV in SAMI BG.
- resistors R1 and R49 are used for hysteresis
- output of the comparator controls the voltage across capacitor C8; when current feedback is above the reference value voltage across capacitor C8 will decrease from +4.7V to -0.6V
- when voltage across C8 goes below 2.4V HOLE-signal will go to "0"
- when voltage across C8 goes above 2.6V HOLE-signal will go back to "1"
- in figure 6 is an example of the operation

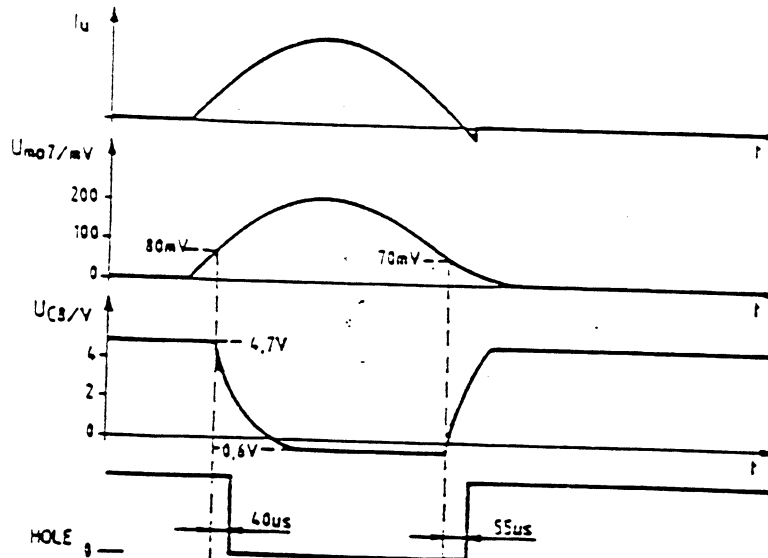


Figure 6. Operation of the current uncontinuity measurement

3.4 Selection of the frequency reference

In the thyristor braking bridge the DC-voltage is constant 100 % when load current is above 50%. When load current decreases below 50% also DC-voltage will decrease towards 90% value, which is also used when operating on the regenerative mode.

$$- S1 = a-b (-FR6 = -FR61).$$

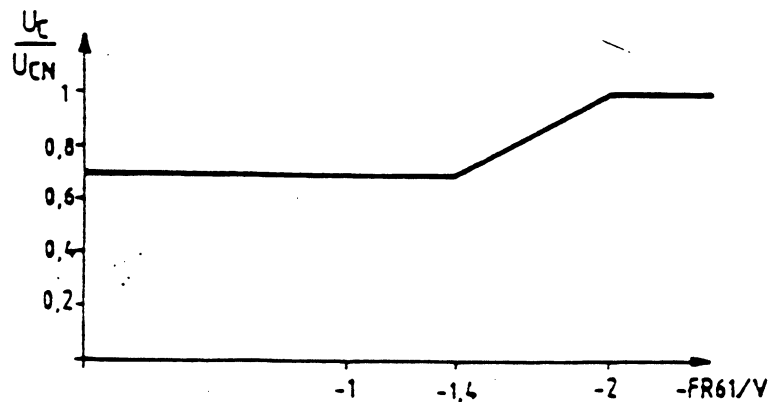


Figure 7. DC-voltage as a function of $-FR61$

3.5 Start command

In the thyristor braking bridge S2 = a-c, so START command is controlled only by UCC3-signal.

| <u>RUN3</u> | <u>UCC3</u> | <u>UCC3 out</u> |
|-------------|-------------|-----------------|
| 0 | 0 | 0 |
| 0 | 1 | 1 |
| 1 | 0 | 0 |
| 1 | 1 | 1 |

4. Jumper settings

S1 = a-b
S2 = a-c
S3 = a-c (with 60Hz AC-line)
S4 = a-c (with 60Hz AC-line)

5.1 Tuning of the synchronization (60Hz AC-line)

If synchronization of the bridge doesn't work properly it can be tuned:

Equipments needed:

- oscilloscope
- isolation transformer to isolate the scope ground from the supply line ground

With 60Hz AC-line S4 = a-c:

Measurement:

- channel 1 y-gain 2V/DIV (zero in the middle)
- channel 2 y-gain 5V/DIV (zero in the middle)
- sweep time 0.83msec/DIV (check with pulsegenerator)
- triggering LINE, HFREJ rising or falling edge
- channel 1 connected to pulse amplifier card SAMT11:
 - channel 1 ground lead to MP23 and channel 1 to MP22
- channel 2 to the control card SAFT 185 TBC MP4
- tune either with TRIGGERING LEVEL or X-SHIFT the half cycle of the synchronizing voltage (positive or negative) to the middle of the screen in the x-direction; after doing this the zero crossings of the synchronizing voltage are exactly in the corners of the screen.
- R62 must be tuned so that the falling edge of the channel 2 signal is 150usec before the center of the screen
- check, that USYN signals (SAMC 19 INF MP2) falling edge is in the middle of the screen (positive half cycle of the synchronizing voltage)
- lock the potentiometers R60 and R62

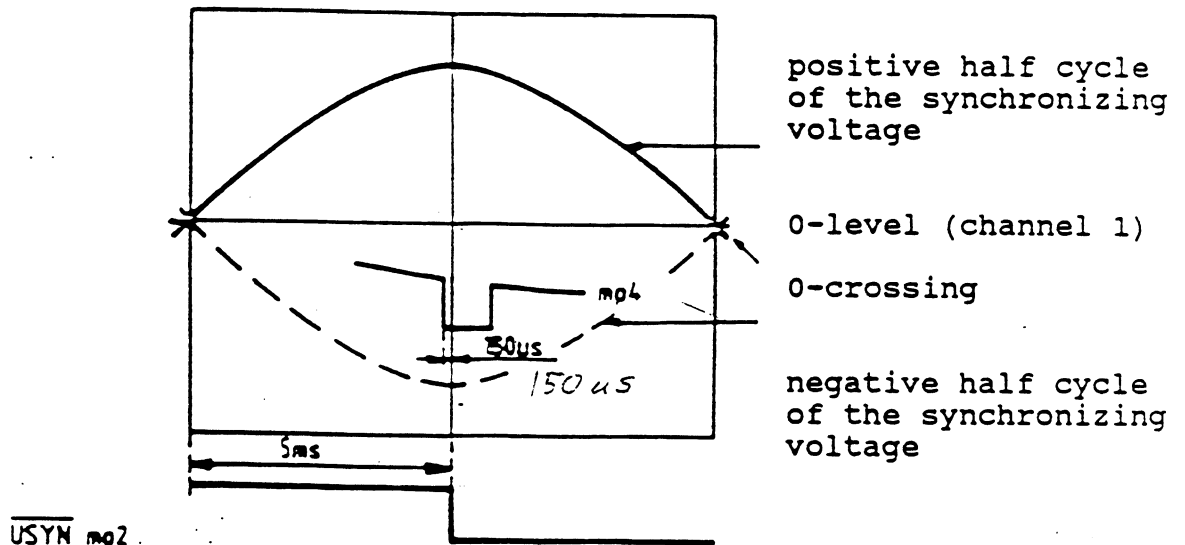


Figure 8. Tuning of the synchronizing

5.2 Tuning of the current limit

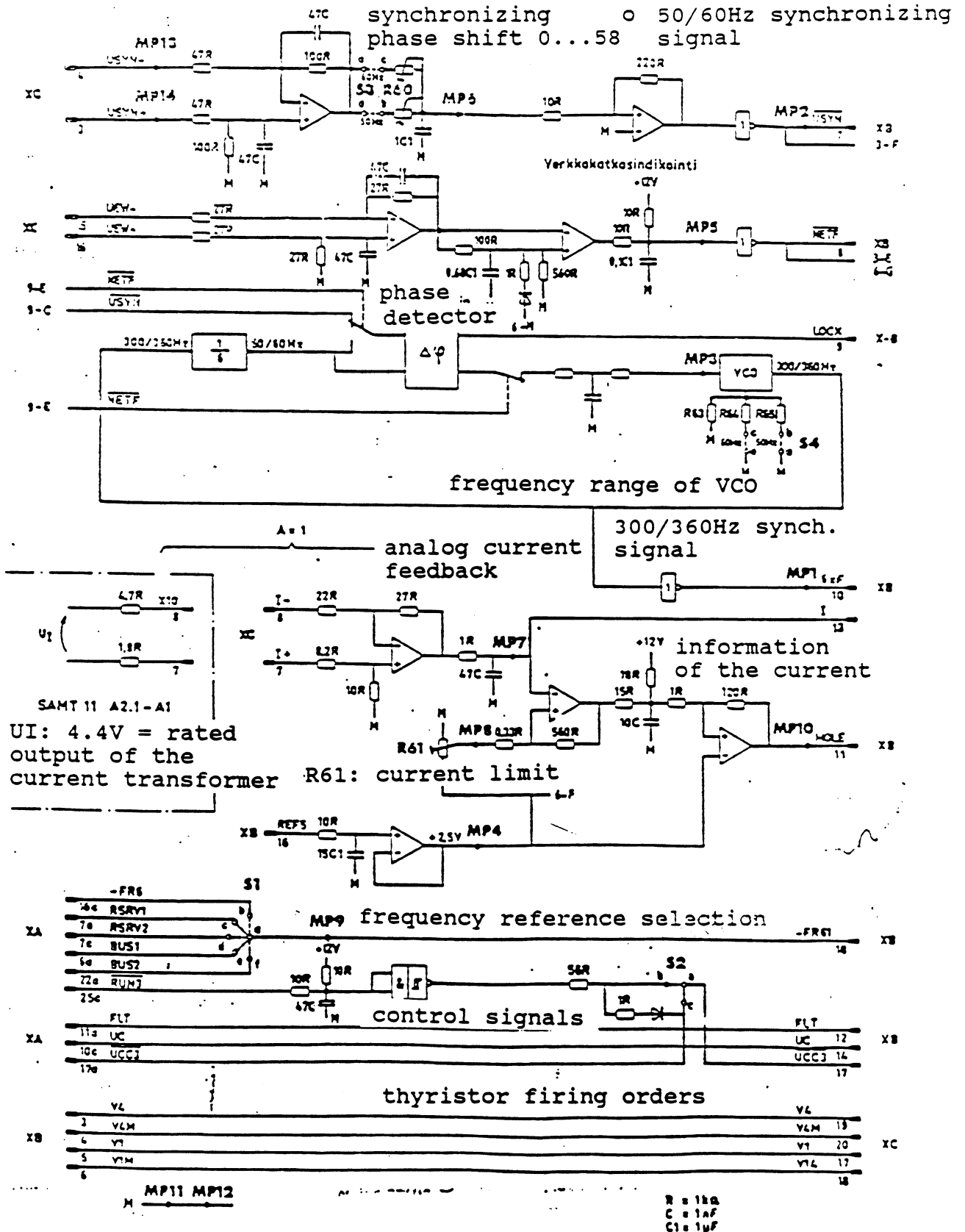
Current limit is related to the uncontinuity information signal HOLE. Current value lower than the current limit equals zero current to the control system. Tuning is done by the trimmer R61; initial setting 60mV (???) between MP8 and MP11.

6. Internal values / delays

| | |
|-------------------------------|------------------------------------|
| - temperature range | -25 C... +70 C |
| - synchronizing signal | 6 * AC-line frequency |
| - sync. signal error | + - 1 (+ - 50 usec) |
| - current uncontinuity signal | HOLE |
| - delay in signal HOLE | + - 100 usec |
| - nominal ref. voltage -FR61 | + 2 V |
| - control voltage | + 12 V (25mA) - 12 V (22mA) |

SAMC 19 INF BLOCK DIAGRAM

Appendix 1/1



SAMT 11 A2.1-A1
 UI: 4.4V = rated output of the current transformer

R61: current limit

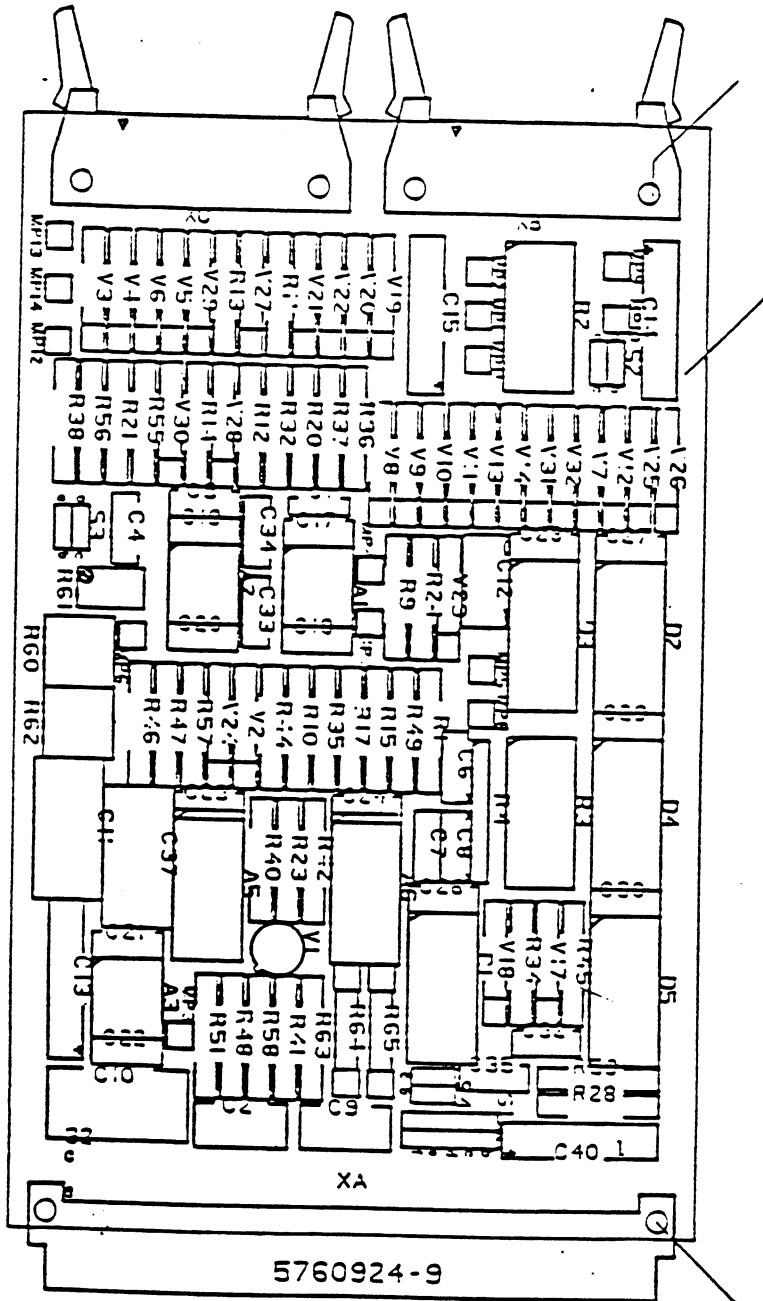
MP9 frequency reference selection

control signals

thyristor firing orders

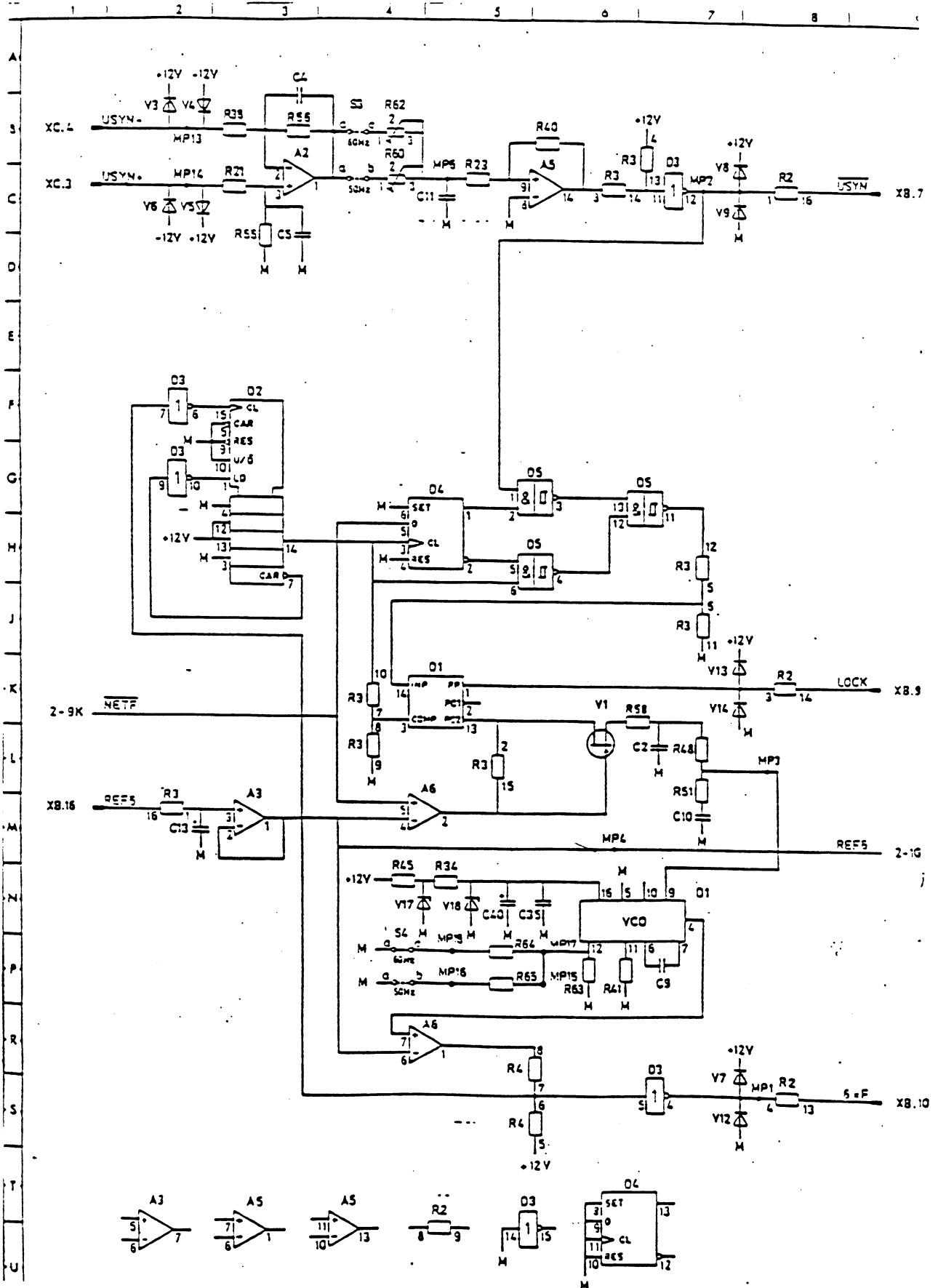
SAMC 19 INF LAYOUT

Appendix 2/1



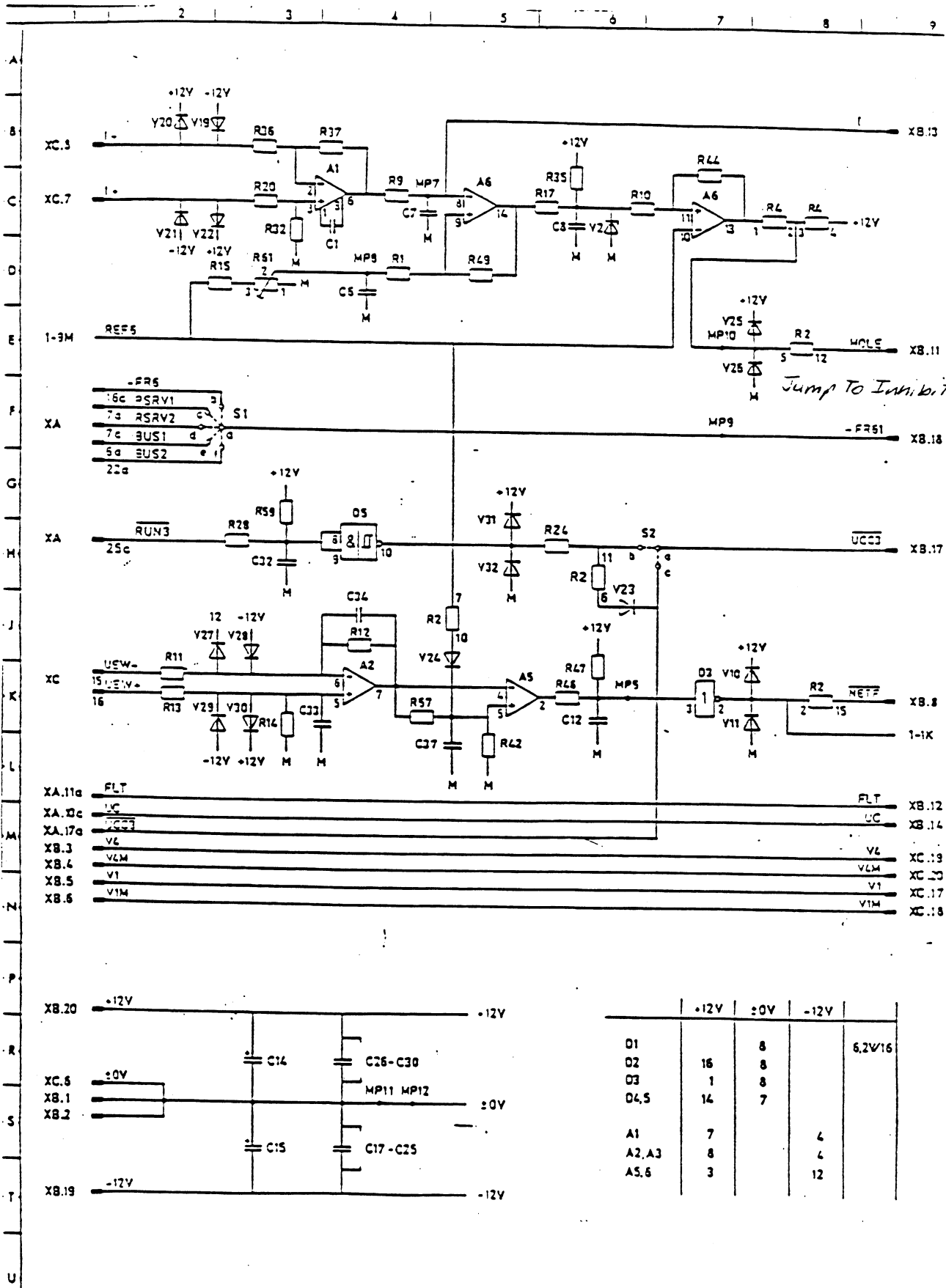
SAMC 19 INF CIRCUIT DIAGRAM

Appendix 3/1



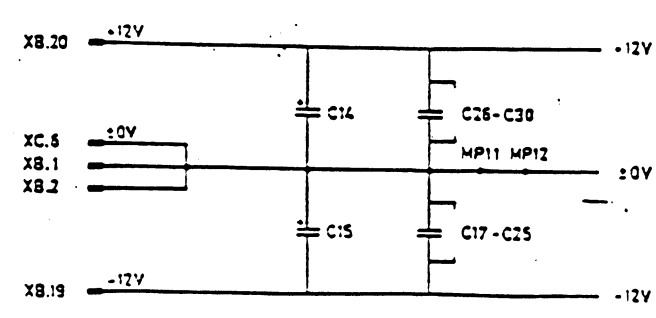
SAMC 19 INF CIRCUIT DIAGRAM

Appendix 3/2



Jump To Inhibit Re...

- XA.11a FLT
- XA.11c UC
- XA.17a UC
- XB.3 VL
- XB.4 VLM
- XB.5 V1
- XB.6 V1M
- XB.12 FLT
- XB.14 UC
- XB.13 VL
- XB.15 VLM
- XB.17 V1
- XB.18 V1M



| | +12V | ±0V | -12V | |
|-------|------|-----|------|---------|
| D1 | | 8 | | 6.2V/16 |
| D2 | 16 | 8 | | |
| D3 | 1 | 8 | | |
| D4,5 | 14 | 7 | | |
| A1 | 7 | | 4 | |
| A2,A3 | 8 | | 4 | |
| AS,6 | 3 | | 12 | |

SAMC 19 INF PARTS LIST

Appendix 4/1

| MAARÄ | YKSIKÖ | KOODI | OSAN NIMITYS | TYYPPI | TEKNISET ARVOT | VALMI. MAALINUM: |
|---|-----------|--------|-----------------------------------|--|---------------------------|--------------------|
| 101 | KPL | 5740 | 1419:KOKOONFANO | | | |
| 201 | | 5740 | 1401:PIIRIKAAVIO | | | 535097/ |
| 22X | | | TOIMINTASELOSTUS | | | 535097/ |
| 24X | | | KOESTUSOHJE | | | |
| 251 | | | | | | |
| 301B | | 1.000 | KFL:VE 5760 9249:PIIRILEVY | 5760924-9 | REV. D 1E 2-LAYER | FI |
| 321 | | 1.000 | KFL:VE 5716 2645:TARNA PIIRILEVY | PDI111 GAY LOGOLIA | 50,8X9,5MM (ID-3233) | PANDUI:US |
| 331 | | 1.000 | KFL:VE 3505 5398:SARJANUMEKILFI | SARJANUMERO | 10X30MM | MATROUS:FI |
| 351 | | 1.000 | KFL:VE 0988 2154:EUR.LIITIN | C064NS-CIA-1,25/5 | 64-NAP. KOIRAS JUOT. | FERLOS:FI |
| 401 | XA | 2.000 | KFL:VE 0988 2286:NAHIAKAAFELIITIN | FAP-2001-1202-0AS | 20-NAP. KOIRAS 90AST | YAHATIC:IF |
| 891 | | | | KTM1,3/3,5/9,7,002 | CuZn, Sn | STOCKO:DE |
| 901 | MF1-HF1B | 18,00 | KFL:VE 0987 8058:JUOTOSTAPPI | FL-10-0,8-1 | 2-NAP. | FEROS:FI |
| C 1101 | SI,4 | 4,000 | KFL:VE 0988 2031:DIKOSULKUFALA | FHI 6-2X36-0,8/5 | 172-NAP. 2-RIV. SUORA | FEROS:FI |
| C 1201 | SI,4 | 0,350 | KFL:VE 0988 2103:PIIKKIRAMA | | | |
| 1591 | | 1.000 | KFL:VE 0981 6488:MIKROPIIRI | CD4046HE | CMOS PHASE LOCKED LOOP | RCA :US |
| 1601 | D1 | 1.000 | KFL:VE 0981 6704:MIKROPIIRI | CD4510HE | CMOS IC6-UP/DOWN COUNTER | RCA :US |
| 1701 | D2 | 1.000 | KFL:VE 0981 6494:MIKROPIIRI | CD4049UBE | CMOS HEX INVERTER/HUFFER | RCA :US |
| 1801 | D3 | 1.000 | KFL:VE 0981 6275:MIKROPIIRI | CD 4013BE | CMOS DUAL D FLIP-FLOP | RCA :US |
| 1901 | D4 | 1.000 | KFL:VE 0981 6623:MIKROPIIRI | CD4093BE | CMOS QUAD SCHMITT TRIGGER | RCA :US |
| 1951 | D5 | 1.000 | KFL:VE 0981 3489:OPER. VANIVISTIN | LM101AJ | 10S 10 NA EOS 2 MV | NS :US |
| 2091 | A1 | 2.000 | KFL:VE 0981 3497:2X OP AMP | CA155BE | EOS 5 HV 10S 200 NA | RCA :US |
| 2201 | A2-A3 | 2.000 | KFL:VE 0981 3365:4XCOMPARATOR | LM139J | 10S 30 NA EOS 6 HV | NS :US |
| 2301 | 05,06 | 1.000 | KFL:VE 0980 7802:FET | 2N4393 | 5HA 40V 1000HM. | PHILIP:NL |
| 2791 | V1 | 1.000 | KFL:VE 0980 8213:ZENERDIODI | BZX79C4V7 | 4,7V 0,4W | PHILIP:NL |
| 3001 | V2 | 12,00 | KFL:VE 0980 3190:DIODI | 1N4148 | 175V AVO.15A 4MS | PHILIP:NL |
| 3301 | V3-14 | 14,00 | KFL:VE 0980 3198:DIODI | 1N4148 | 175V AVO.15A 4MS | PHILIP:NL |
| 3401 | V19-V32 | 1.000 | KFL:VE 0980 8248:ZENERDIODI | BZX79C8V2 | 8,2V 0,4W | PHILIP:NL |
| 3501 | V17 | 1.000 | KFL:VE 0980 8906:ZENERDIODI | 1N 021 | 6,2V 5 X | PHILIP:NL |
| 3501 | V18 | 1.000 | KFL:VE 0991 0085:VASTUSVERKKO DIL | 098-3-R1K | 0X1K FR=0,2 M FT=1,6W | BECKMA:US |
| 3691 | R2 | 1.000 | KFL:VE 0991 0Y15:VASTUSVERKKO DIL | 098-3-R10K | 0X10K FR=0,2M FT=1,6W | BECKMA:US |
| 3701 | R3 | 1.000 | KFL:VE 0990 9907:VASTUSVERKKO SIL | L08-3-C103M2 | 4X10K FR=0,11W FT=0,44W | BECKMA:US |
| 3751 | R4 | 1.000 | KFL:VE 0991 2517:VASTUS | 5MA0207S | 150 OHM 0,5 W 1X 50FFH | DRALOR:DE |
| 3891 | R4 | 1.000 | KFL:VE 0991 2525:VASTUS | 5MA0207S | 162 OHM 0,5 W 1X 50FFH | DRALOR:DE |
| 3901 | R34 | 1.000 | KFL:VE 0991 2550:VASTUS | 5MA0207S | 332 OHM 0,5 W 1X 50FFH | DRALOR:DE |
| 3951 | R45 | 2.000 | KFL:VE 0991 2614:VASTUS | 5MA0207S | 1.00K 0,5 W 1X 50FFH | DRALOR:DE |
| 4001 | R1 | 1.000 | KFL:VE 0991 2720:VASTUS | 5MA0207S | 0,25K 0,33W 1X 50FFH | DRALOR:DE |
| 4101 | R9,10 | 2.000 | KFL:VE 0991 2738:VASTUS | 5MA0207S | 110,0K 0,33W 1X 50FFH | DRALOR:DE |
| 4291 | R20 | 3.000 | KFL:VE 0991 2738:VASTUS | 5MA0207S | 110,0K 0,33W 1X 50FFH | DRALOR:DE |
| 4301 | R23,28 | 1.000 | KFL:VE 0991 2738:VASTUS | 5MA0207S | 110,0K 0,33W 1X 50FFH | DRALOR:DE |
| 4451 | R32,46,47 | 1.000 | KFL:VE 0991 2738:VASTUS | 5MA0207S | 110,0K 0,33W 1X 50FFH | DRALOR:DE |
| 4471 | R59 | 1.000 | KFL:VE 0991 2754:VASTUS | 5MA0207S | 115,0K 0,33W 1X 50FFH | DRALOR:DE |
| 4501 | R17 | 1.000 | KFL:VE 0991 2762:VASTUS | 5MA0207S | 118,2K 0,33W 1X 50FFH | DRALOR:DE |
| 4551 | R35 | 1.000 | KFL:VE 0991 2762:VASTUS | 5MA0207S | 118,2K 0,33W 1X 50FFH | DRALOR:DE |
| 4581 | | | | | | |
| <p>APP STRONBERG DRIVES OY FINLAND : TUNNUS : NIMIYS : TGC-VALIKORTTI</p> <p>KAYTTOKONDE SAHI B,F(V/R-SI : TYYPPI : SAMC 19 INF</p> <p>LISATIEDOT : TEKN.ARVOIT</p> | | | | | | |
| ALKUP. | A | TSTO | | | | |
| FVM | 031012 | SUUNN. | TORKKELI | | | FIKA B5150 |
| VIIH. | E | TSTO | EXX1 | | | LEHTI () KOKO A3 |
| FVM | 051010 | SUUNN. | KYLLONEN | BC-N KÄY-TIETO OJETTU RUK3-SIA JA UCC3-SIA | | FI VLS10 E |
| FVM | TARK. | TARK. | | VAIHELUKON TAIIDISTUMISNOPEUTIA FARONNETTU | | KILLI VE 5740 1409 |
| FVM | 051023 | HVV. | DANILSTRÖM | TAIDISTUS SEKA 50 ETIA 60 HZ:LLE | | |

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 2.2 External faults.....2
 2.3 Internal faults.....3
 2.4 Precharge selection.....4
 2.5 DC-voltage measurement.....4
3. Signals.....5

Appendix 1/1 SAFT 181 INF layout
Appendix 2/1 SAFT 181 INF circuit diagrams
 -2/2

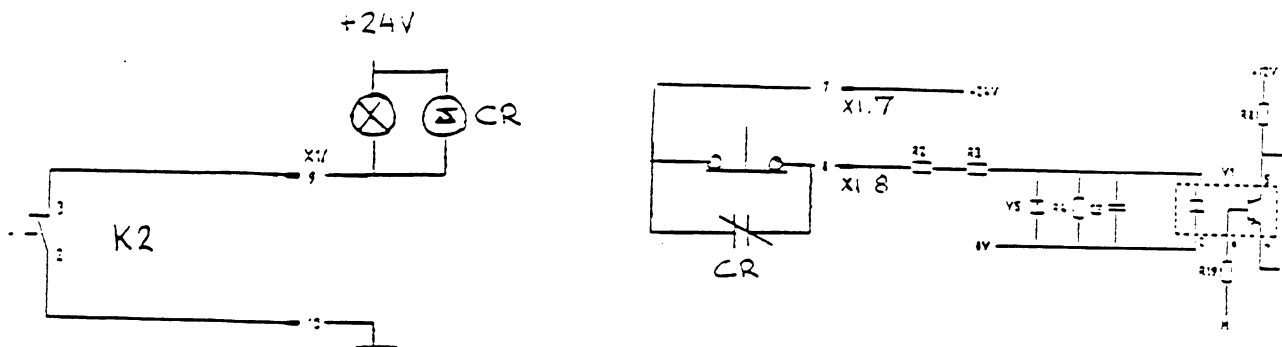


Figure 2. External RESET-button

External fault will cause also the relay K2 to trip, which will turn on the external alarm light (figure 3)

2.3 Internal faults

In the case of control power failure SAMC11POW power supply will pull the signal RESET (XA.26c) down and V15 will be turned off causing contactor K2.2-3 to close. In stand alone drives connection is normally like in figure 2 light will be on in the case of a fault.

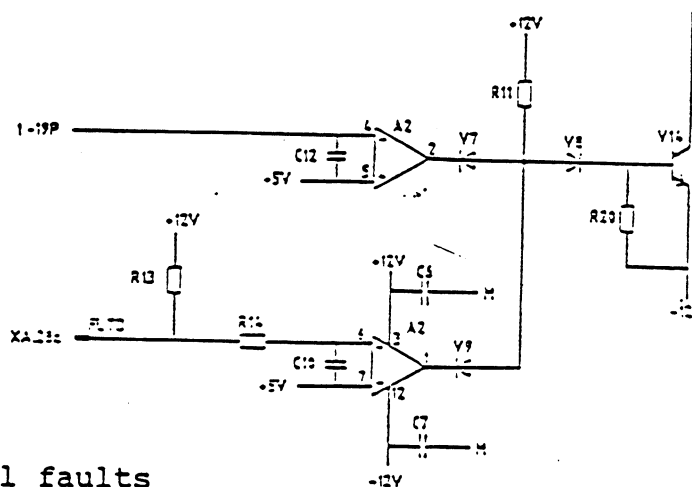


Figure 3. Internal faults

Internal faults in the control board SAMC15TBC will cause FLTD (XA.28c) to toggle and this will blink the light in the figure 2.

2.4 Precharge selection

If TBU is equipped with a precharge system S3 ought to be in position b-c; constant 50 degree angle during precharge.

If precharge system is not used S3 ought to be in position a-b; precharge starts with 111 degree angle. After precharge is done and if no START command is received angle will automatically be changed to 50 degrees; START-command will release the angle controller.

2.5 DC-voltage measurement

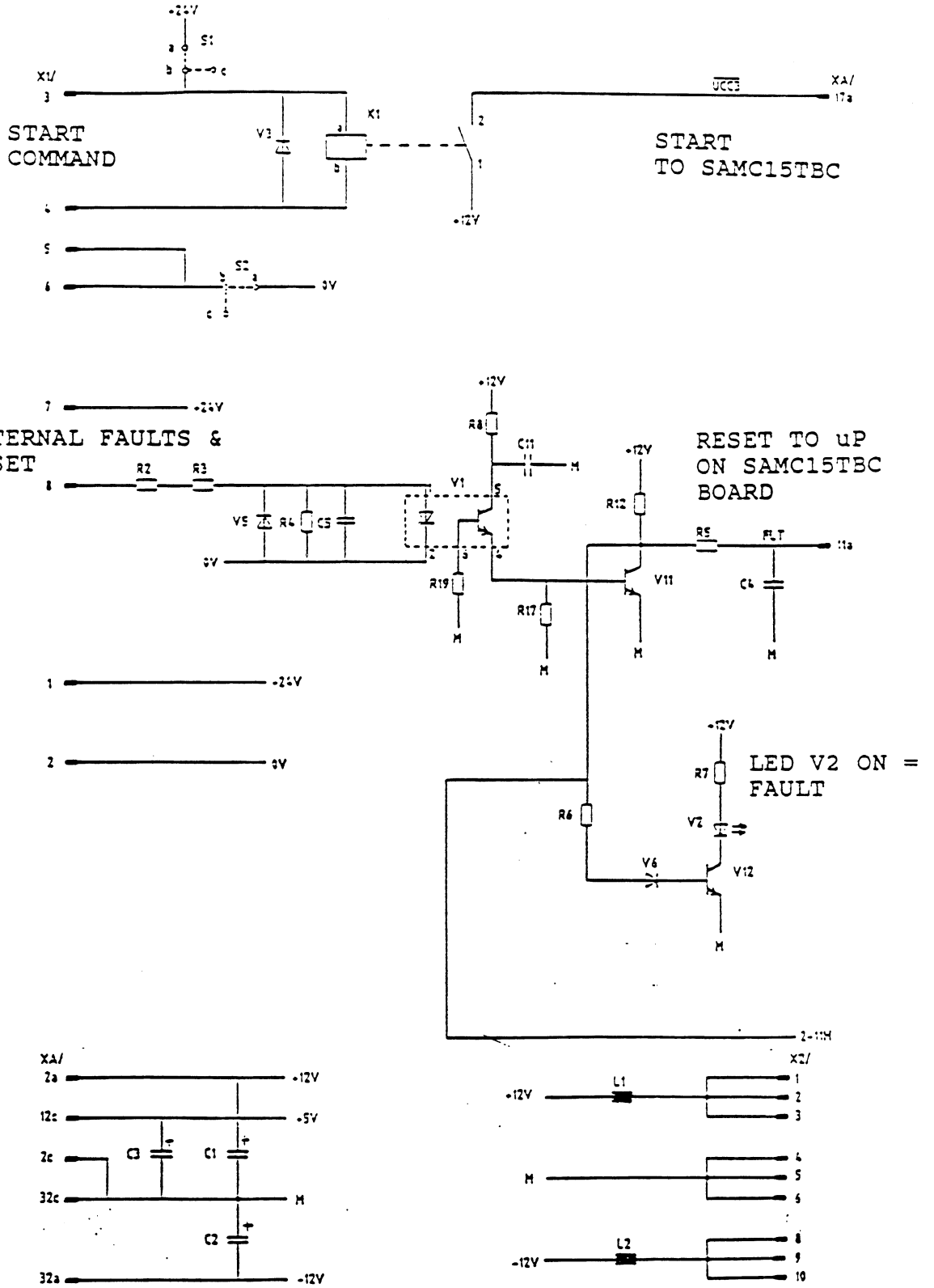
Isolated (isolation done on SAFT 183 VMC board) DC-voltage measurement from SAFT 183 VMC is inverted; inverted signal is used on SAMC19INF and SAMC15TBC boards.

Table 1. Signals on SAFT 181 INF

| Connector/s SAFT181INF | signal name | connected to (X1 and X2 are TBU module terminal blocks) |
|---------------------------|-------------|--|
| X1.1 | +24V | X2.1, X2.3 |
| X1.2 | + -0V | X2.2, X2.4 (FLT2) |
| X1.3 | RUN1 | X2.6 |
| X1.4 | RUN2 | X2.7 |
| X1.5 | RUN3 | X2.8 |
| X1.6 | RUN4 | X2.9 |
| X1.7 | FLT1 : | bridge overtemp. relay S1.1, S1.2 ! ! fan motor overload relay F6.14, F6.13 ! ! X1.2 ! ! External Reset ! ! |
| X1.8 | FLT2 | X1.1 |
| X1.9 | FLTL1 | X2.4 |
| X1.10 | FLTL2 | X2.5 |

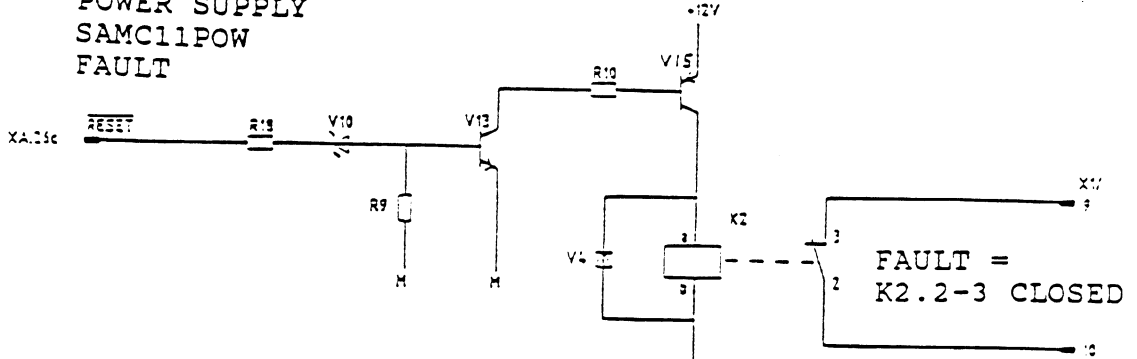
| Connector/s SAFT181INF | signal name | connected to |
|---------------------------|-------------|---|
| X2.1 | +12V | SAFT183VMC X2.1 |
| X2.2 | +12V | SAFT183VMC X2.2 |
| X2.3 | +12V | SAFT183VMC X2.3 |
| X2.4 | 0V | SAFT183VMC X2.4 |
| X2.5 | 0V | SAFT183VMC X2.5 |
| X2.6 | 0V | SAFT183VMC X2.6 |
| X2.7 | -Uc | SAFT183VMC X2.7 |
| X2.8 | -12V | SAFT183VMC X2.8 |
| X2.9 | -12V | SAFT183VMC X2.9 |
| X2.10 | -12V | SAFT183VMC X2.10 |
| XA.2a | +12V | SAMC11POW XA.2a SAMC11POW XA.4a |
| XA.10a | ADTE | SAMC15TBC XA.10c |
| XA.10c | Uc | SAMC19INF XA.10C |
| XA.11a | FLT | SAMC19INF XA.11a |
| XA.12c | +5V | SAMC15TBC XA.4a SAMC11POW XA.10c SAMC15TBC XA.30a SAMC11POW XA.12c SAMC15TBC XB.30c |
| XA.16c | -FR6 | SAMC19INF XA.16c |
| XA.17a | UCC3 | SAMC19INF XA.17a |
| XA.26c | FLTA2 | SAMC15TBC XA.6a SAMC11POW XA.6a |
| XA.28c | FLTD | SAMC15TBC XA.28c |
| XA.32a | -12V | SAMC15TBC XA.32a SAMC11POW XA.30a SAMC11POW XA.32a |
| XA.32c, XA.2c | 0V | SAMC15TBC XA.2c SAMC11POW XA.2c SAMC15TBC XB.32a SAMC11POW XA.4c SAMC15TBC XA.32c SAMC11POW XA.30c SAMC15TBC XB.32c SAMC11POW XA.32c |

APPENDIX 2/1

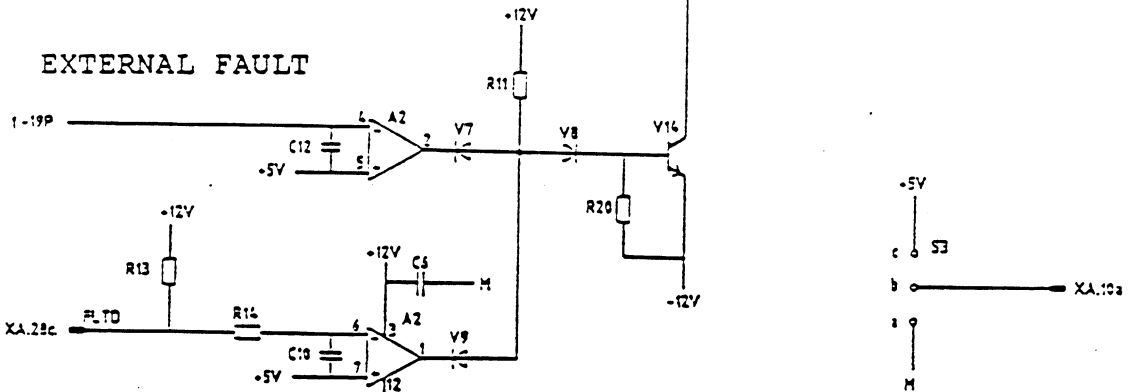


| | | | |
|--|---------------------------|---------------------------------|-----------------|
| | EXP 88-08-04 Suomen | INTERFACEKORTTI SAFT 181 INF | 1 (1-2) |
| | 28-10-03 J.S. | | A3 VE 5811 6181 |

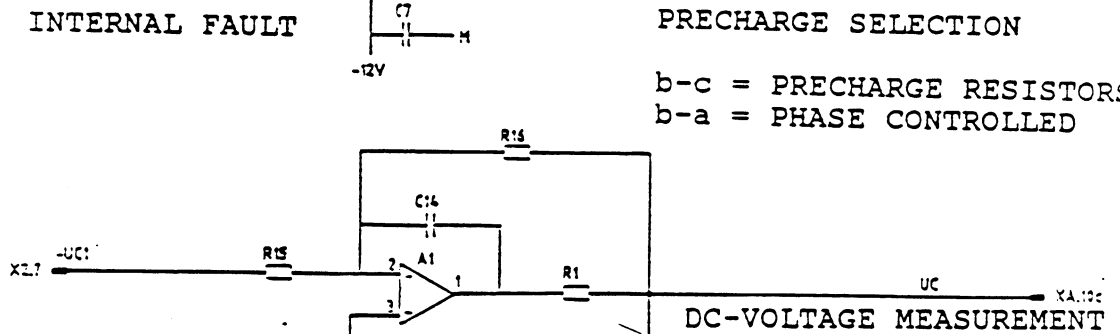
POWER SUPPLY
SAMC11POW
FAULT



EXTERNAL FAULT



INTERNAL FAULT

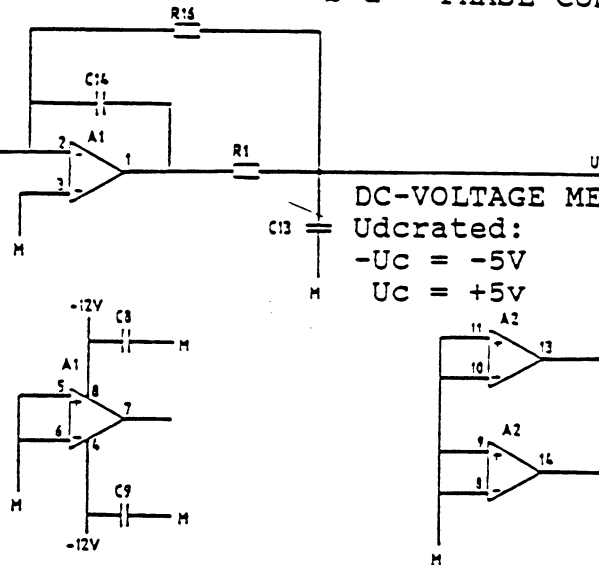


PRECHARGE SELECTION

b-c = PRECHARGE RESISTORS
b-a = PHASE CONTROLLED

DC-VOLTAGE MEASUREMENT

Udcrated:
-Uc = -5V
Uc = +5V



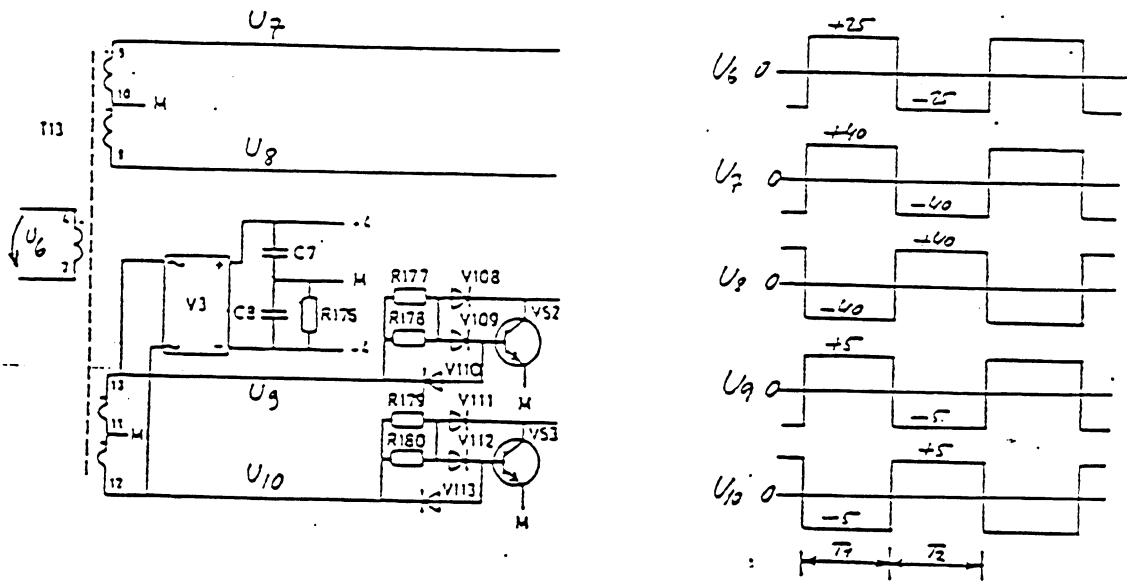
| | | | |
|------------------------------|------------------------------|---|------------------------|
| <p>STRÖMBERG FINLAND</p> | <p>88-08-06 Suominen</p> | <p>INTERFACEKORTTI SAFT 181 INF</p> | <p>A3 VE 5811 6:31</p> |
|------------------------------|------------------------------|---|------------------------|

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| Appendix 2/1 | SAMT 11 layout |
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2. Power supply portion

Connection and wave forms of the power supply in figure 2.



T1 = V52 on & V53 off
T2 = V53 on & V52 on

Figure 2. Power supply portion of SAMT 11

Input to the power supply:

- 25V 35kHz square wave U_6

Output from the power supply:

- two 40 V square waves (180 degrees phase shift)
 U_7 and U_8
- +- 4Vdc from the voltages U_9 and U_{10} ; voltages U_9 and U_{10}
control also the transistors V52 and V53

3. Pulse amplifier portion

One phase of the pulse amplifier in figure 3 and wave forms related to the pulse amplifier in figure 4.

During OFF pulse transistor V24 (figure 3) conducts and V23 is OFF and capacitor C9 will charge up through the diodes V114, V115 and through the choke L7. No power to the input of the transformer T7 which also means that gate current I_g is zero.

During ON pulse V24 is OFF and V23 is ON. C9 will be discharged through R140, V23, T7 primary winding and transistors V52 or V53. Secondary current caused by primary currents I_{V117} and I_{V119} is rectified and this rectified current is used as a gate pulse to the thyristor.

Static level of the base current is limited by the choke L7. In the beginning of the turn on pulse the level of I_g is higher than the static level to reduce the turn on losses; gate current spike value is limited by resistor R140.

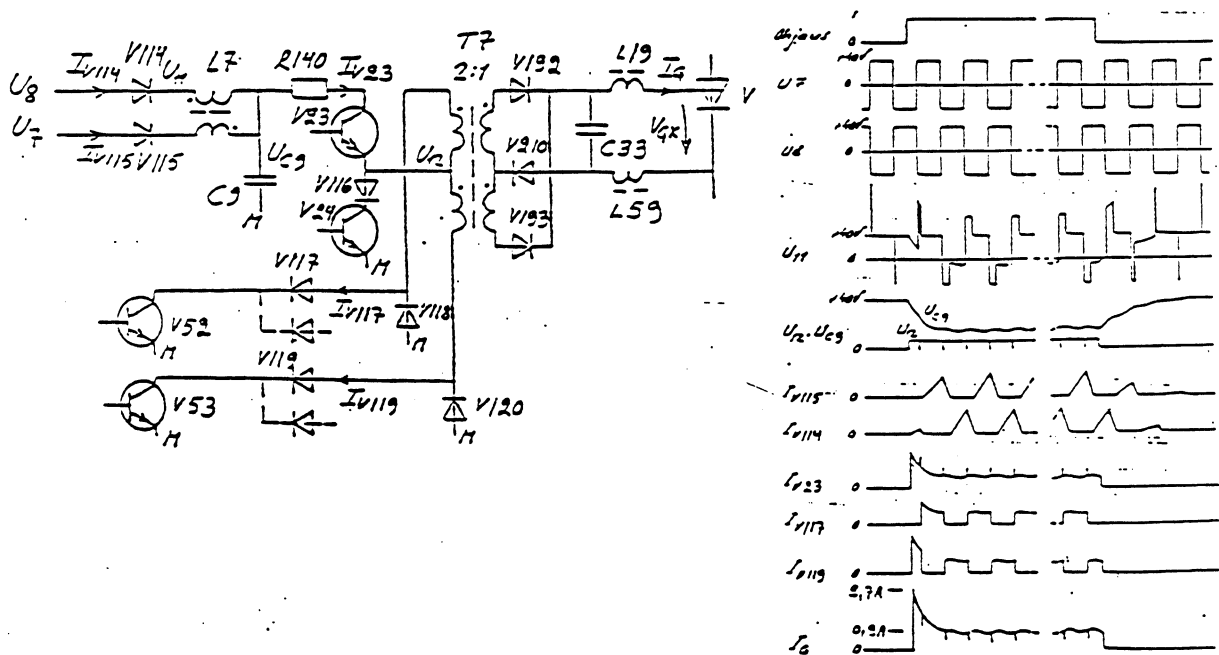


Figure 4. Wave shapes of the pulse amplifier currents and voltages (I_g value is measured with a 1 ohm resistive load)

Because transistors V52 and V53 are common for all channels, it is a must to isolate transformers of different phases by diodes. In figure 5 is the control portion of the pulse amplifier card. Zener diode V60 and resistors R110 and R122 set the threshold level of the control signal to 6.5V.

If control signal is below 6.5V:

- V46 is OFF
- V24 will get base current through R86 and V169
- V22 and V23 are OFF

If control signal is above 6.5V:

- V46 is ON
- V24 is OFF
- V22 and V23 are on

Resistor R89 will keep V24 in ON-state during START- and FAULT-situations where voltage UC9 is high, but control voltage +4 is lower than normally.

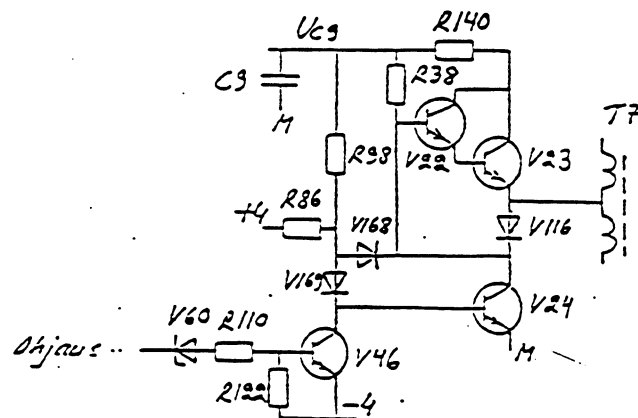


Figure 5. Control portion of the pulse amplifier

4. Connection of synchronizing transformer

SYNCHRONIZING TRANSFORMER
660/43.3V 3VA 60hZ

SAMT 11

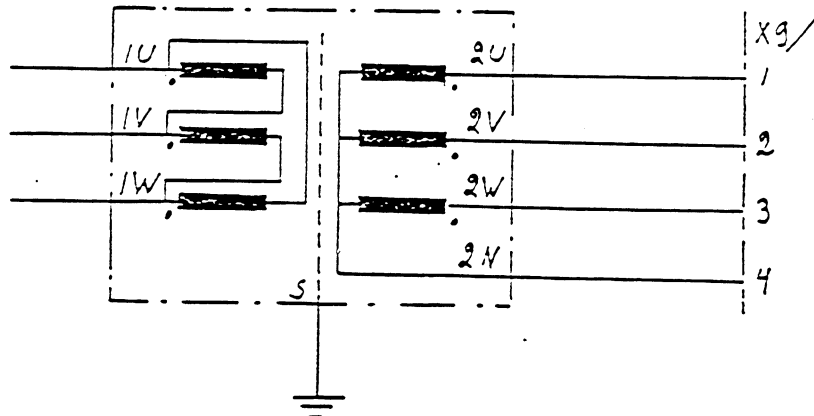


Figure 6. Synchronizing transformer connection

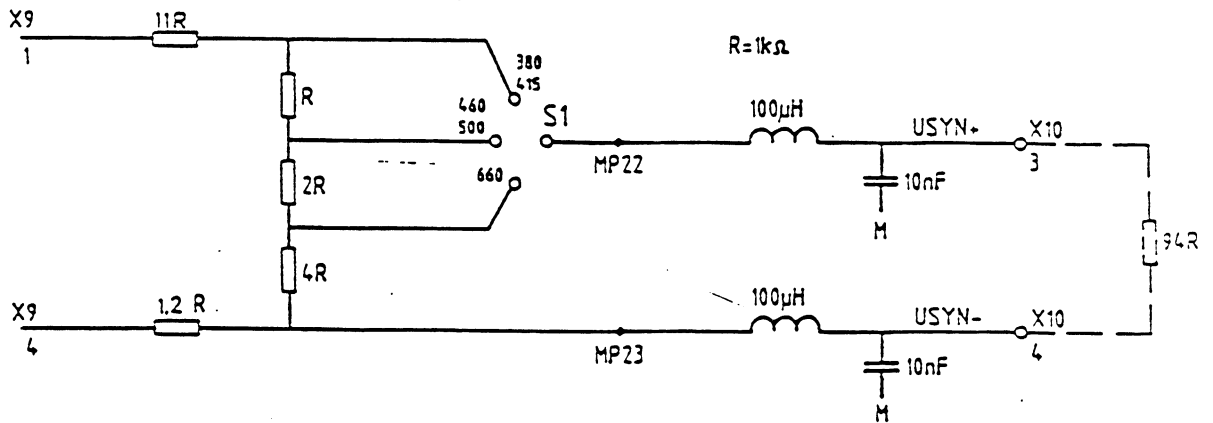


Figure 7. Scaling of the synchronizing voltage on the pulse amplifier card

Jumper S1 is used to select the scaling of the synchronizing voltage according to the main AC-line voltage. Synchronizing voltage can be measured between measurement points MP22 and MP23. Synchronizing voltage is filtered before taking it to the SAMC 19 INF card.

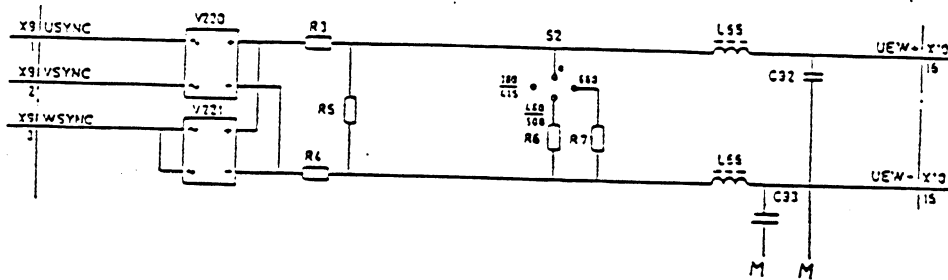


Figure 8. Scaling circuits of the line voltage

Rectified line voltage is scaled according to the position of the jumper S2. Scaled voltage is used on the SAMC 19 INF card for indication of netfailures.

5. Current measurement

Current transformers connection in figure 9. Resistance values R13...R19:

- with the current transformers nominal secondary current voltage between I+ and I- is 4.36V

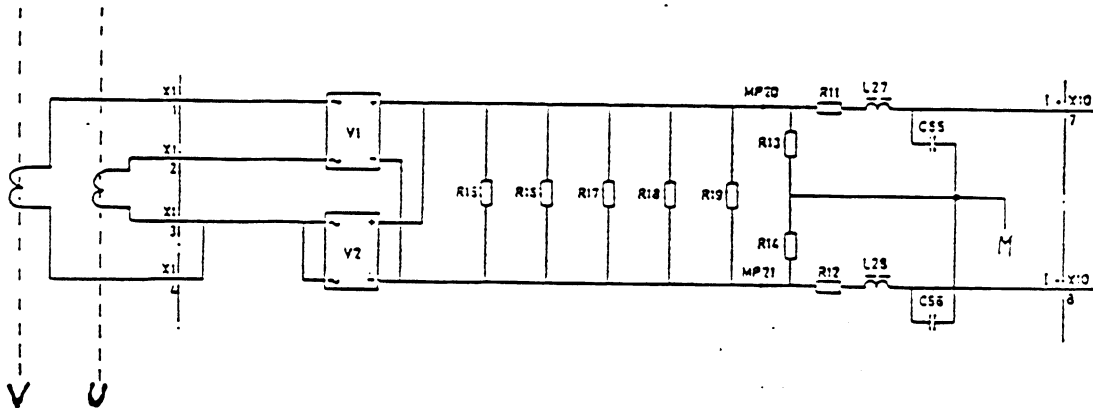
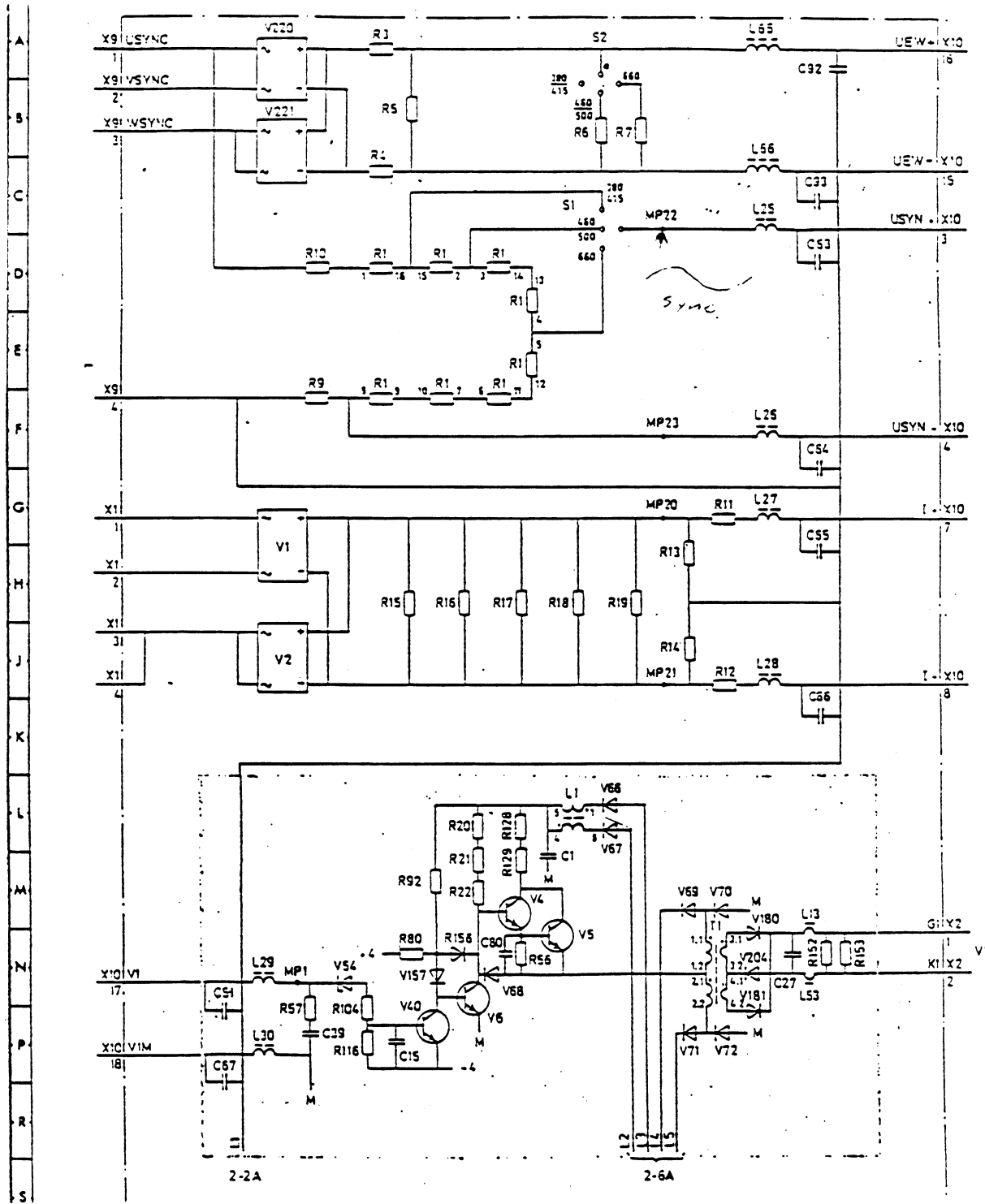


Figure 9. Connection of current transformers

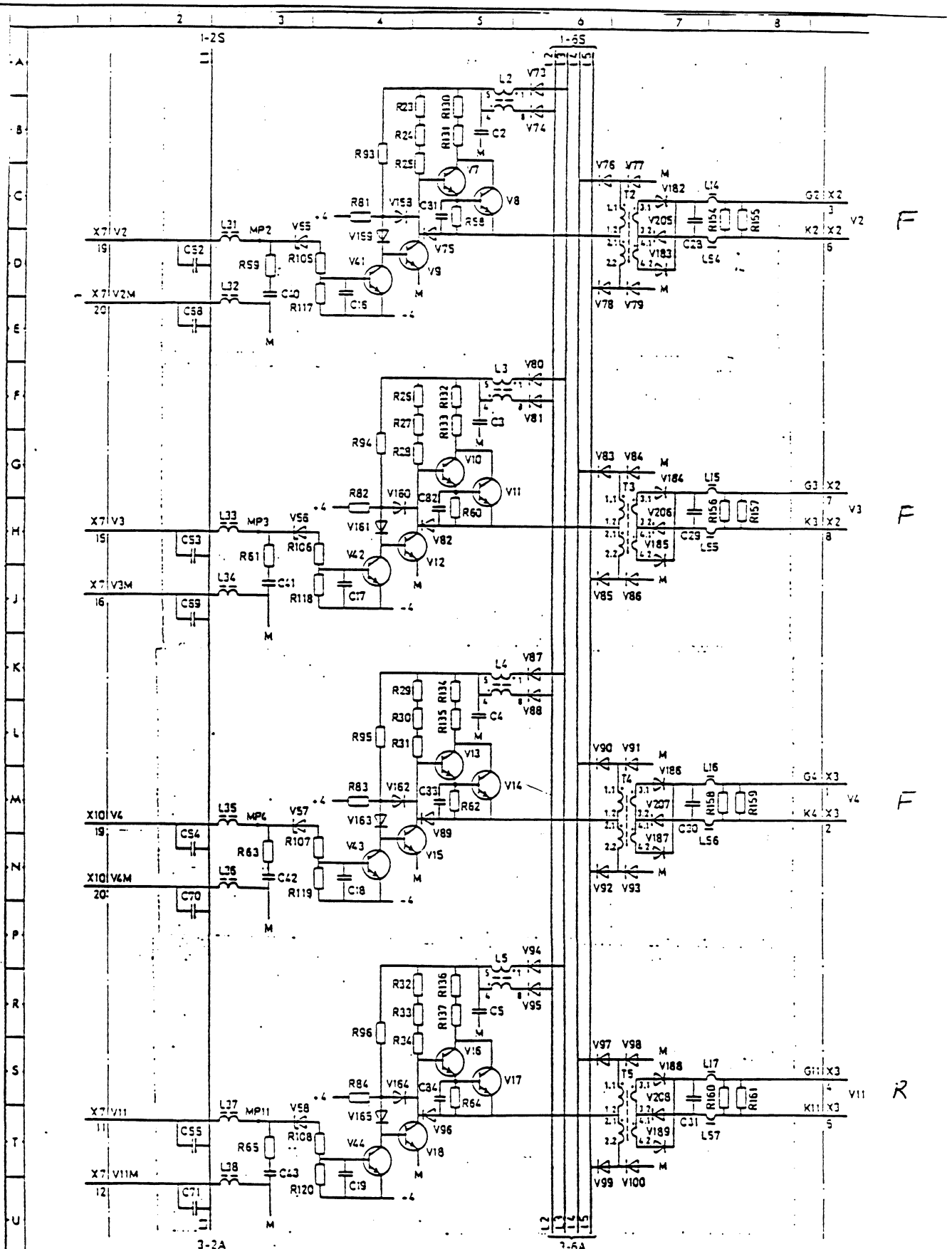
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03-16-89
R.Ahola

SAMT 11
block diagram

APPENDIX 1/1



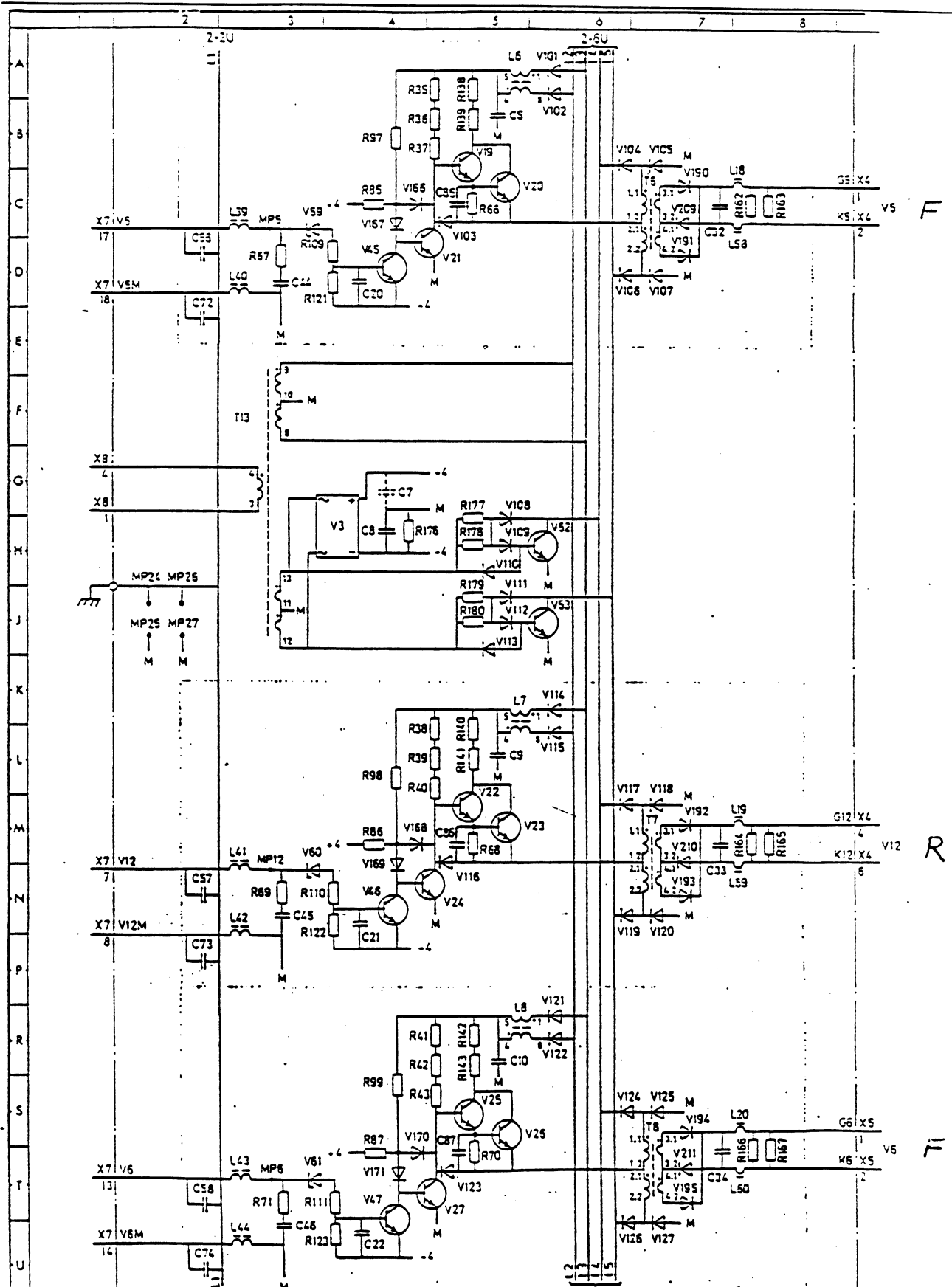
11

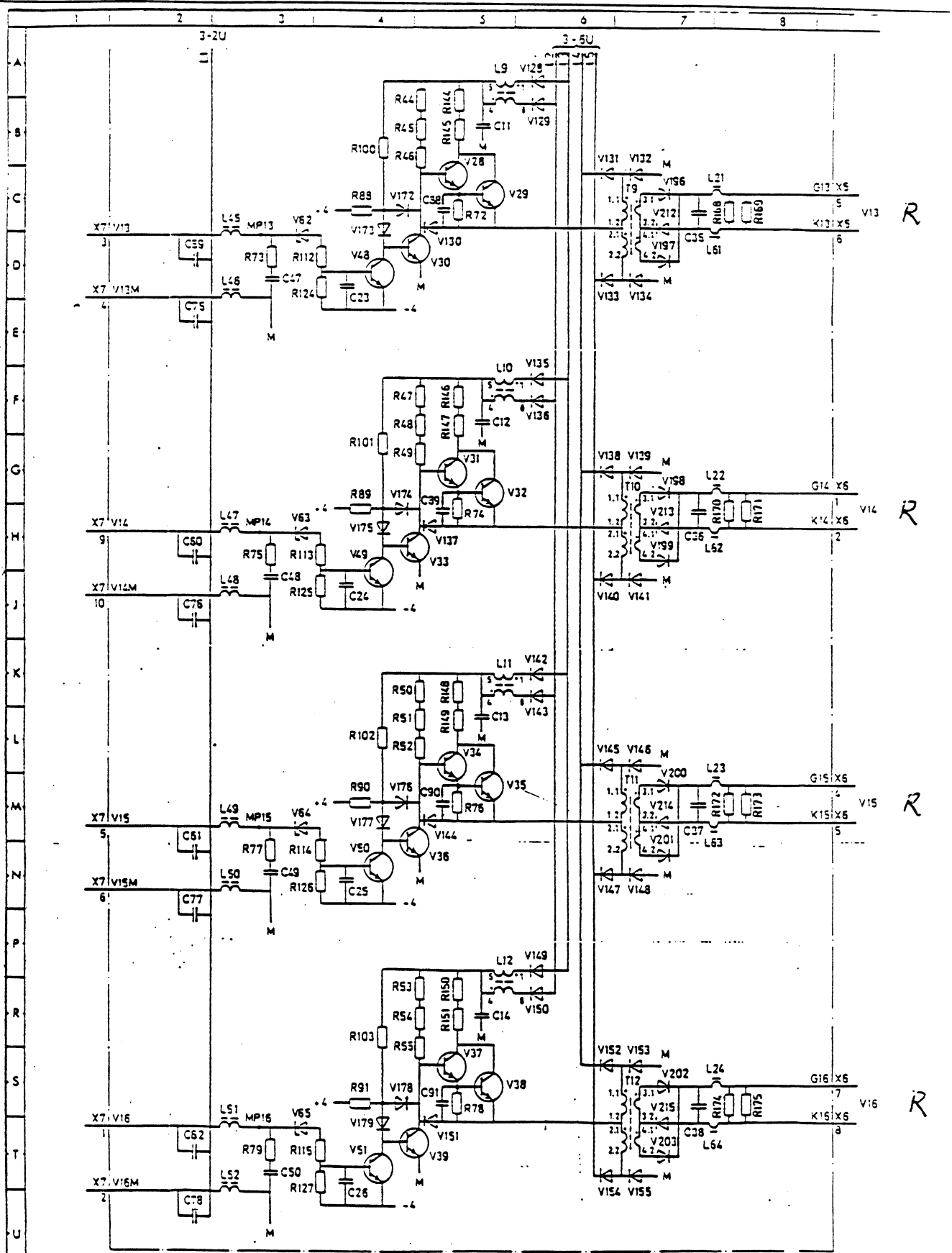


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R.Ahola

SAMT 11
circuit diagram

APPENDIX 3/3





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 03-16-89
 R.Ahola

SAMT 11
 parts list

APPENDIX 4/1

| MÄÄRÄ | YKSIÖ | KOODI | OSAN NIMI | YTYFY | TEKNISET ARVOT | VAIHT. | MAA | YKSIÖ |
|-------|--------|---------|-----------|--------------------------|---------------------|----------------------------|-----|---------|
| 10: | | | | | | | | |
| 19: | | | | | | | | |
| 20: | | | | | | | | |
| 21: | | | | | | | | |
| 29: | | | | | | | | |
| 30: | | | | | | | | |
| 40: | | | | | | | | |
| 50: | | | | | | | | |
| 99: | | | | | | | | |
| 100: | 1,000 | KPL:IVE | 5760 | 4548:PIIRILEVY | SAMT 11 | REV.H 250X475 2-LAYER | FI | 574030/ |
| 110: | 1,000 | KFL:IVE | 5714 | 2463:TARNA PIIRILEVY | FIL111 GMY LOGOLLA | 50.0X9.50H (JD-5233) | FI | |
| 140: | 1,000 | KFL:IVE | 3505 | 5398:SARJANUHEROKILFI | SARJANUHERO | 110X30H | FI | |
| 160: | 3,000 | KPL:IVE | 3506 | 4290:LIITIN.KORTTI | 2630-04A 09-74-1041 | | FI | |
| 170: | 5,000 | KFL:IVE | 3506 | 4311:LIITIN.KORTTI | 2630-00A 09-74-1081 | | FI | |
| 180: | 2,000 | KFL:IVE | 0988 | 2057:NAHIAKAFFELIITIN | FAP-2001-1204-0A8 | | FI | |
| 199: | 92,000 | KFL:IVE | 0987 | 8850:JUOTOSAFFI | RTN1.3/3.3/9.7.002 | 20-NAF. KOIKAS BUURA | FI | |
| 200: | 2,000 | KFL:IVE | 0987 | 8700:DIKOSILKUPISTOKE | 461-2071-01-03-12 | CaZn, Sn | FI | |
| 210: | 8,000 | KFL:IVE | 0987 | 8496:DIKOS.PISTOKEHOLKKI | 450-3704-01-03 | D=1.02MM, 0.8 UM AU, BRASS | FI | |
| 220: | 12,000 | KPL:IVE | 5717 | 1910:PUSSIMUUNTAJA | SAMI- | MULKKI 0.3 UM AU/ CU | FI | |
| 230: | 1,000 | KFL:IVE | 5717 | 1936:MUUNTAJA | SAMI-MUUNTAJA | | FI | |
| 240: | 12,000 | KFL:IVE | 5717 | 1901:KURISTIN | SAMI-KURISTIN | 2X270UH 1.5A | FI | |
| 250: | 12,000 | KFL:IVE | 0985 | 0091:KURISTIN | R70108-81103-K | 1UH 10Z 0.8A | FI | |
| 260: | 28,000 | KFL:IVE | 0985 | 0104:KURISTIN | R78 108-81104-J | 100 UH 4-10Z 160HA | FI | |
| 270: | 2,000 | KFL:IVE | 0985 | 0104:KURISTIN | R78 108-81104-J | 100 UH 4-10Z 160HA | FI | |
| 280: | 12,000 | KFL:IVE | 0985 | 0091:KURISTIN | R78108-81102-K | 1UH 10Z 0.8A | FI | |
| 290: | 2,000 | KFL:IVE | 0981 | 3209:DIODISILTA | SKB 1.2/04 | 0.9A 400V | FI | |
| 300: | 2,000 | KFL:IVE | 0981 | 3209:DIODISILTA | SKB 1.2/04 | 0.9A 400V | FI | |
| 310: | 1,000 | KFL:IVE | 0981 | 3187:DIODISILTA | VE20X | 1A 300V | FI | |
| 320: | 36,000 | KFL:IVE | 0980 | 6482:TRANSISTORI | B5M 60 A | MFN 1 A 150 V | FI | |
| 330: | 12,000 | KFL:IVE | 0980 | 6481:TRANSISTORI | 2R2319A | MFN 0.5 A 40 V | FI | |
| 340: | 2,000 | KFL:IVE | 3506 | 4460:TRANSISTORI | BU 406 | MFN 5A 300V | FI | |
| 350: | 12,000 | KFL:IVE | 0980 | 0248:ZENERDIODI | BZX79C8V2 | 0.2V 0.4U | FI | |
| 360: | 4,000 | KPL:IVE | 0980 | 3238:DIODI | BYV 95 B | 400V AVIA 200NS | FI | |
| 370: | 5,000 | KFL:IVE | 0980 | 3238:DIODI | BYV 95 B | 400V AVIA 200NS | FI | |
| 380: | 5,000 | KFL:IVE | 0980 | 3238:DIODI | BYV 95 B | 400V AVIA 200NS | FI | |
| 390: | 5,000 | KFL:IVE | 0980 | 3238:DIODI | BYV 95 B | 400V AVIA 200NS | FI | |
| 400: | 5,000 | KFL:IVE | 0980 | 3238:DIODI | BYV 95 B | 400V AVIA 200NS | FI | |
| 410: | 4,000 | KFL:IVE | 0980 | 3238:DIODI | BYV 95 B | 400V AVIA 200NS | FI | |
| 420: | 2,000 | KFL:IVE | 0980 | 3238:DIODI | BYV 95 B | 400V AVIA 200NS | FI | |
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| 500: | 4,000 | KFL:IVE | 0980 | 3238:DIODI | BYV 95 B | 400V AVIA 200NS | FI | |
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| 530: | 4,000 | KFL:IVE | 0980 | 3238:DIODI | BYV 95 B | 400V AVIA 200NS | FI | |
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| 1000: | 4,000 | KFL:IVE | 0980 | 3238:DIODI | BYV 95 B | 400V AVIA 200NS | FI | |

ABB STROMBERG DRIVES OY FINLAND
 ALKUF. A TSTO FOX
 FVN 011116 SUUNN. SAARINEN
 VTIH. J TSTO EXX
 FVN 8A0510 SUUNN. TARIANEN
 FVN 8A0510 TARK.
 FVN 8A0510 HYV. SAARINEN

TUNNUS
 KÄYTTÖKOODE
 LISÄTIEDOT
 VASTAANMAKYTETYN SILLAN FUUSSIVAIHTAJASTIN MF24.25 JA MF26.27 YHDISTÄÄN JUOTOSLANGALLA

NIMIYTY
 FUUSSIVAIHTAJASTIN
 SAMT 11

FIKA 05130
 LEHTE FI
 KKOUDI VE 5721 1569

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| 2. SAFT 183 VMC Operation..... | 2 |
| 3. Trouble shooting..... | 3 |

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| Appendix 1/1 SAFT 183 VMC layout | |
| Appendix 2/1 SAFT 183 VMC circuit diagram | |

1. General information

SAFT 183 VMC is used for DC-voltage measurement. Flow of the scaled and isolated signal:

SAFT181INF (X2.7)---SAFT181INF (XA.10c)----
SAMC19INF (XA.10c)--SAMC19INF (XB.14)-----
SAMC15TBC (X3.14)

2. Operation

Resistors R1...R8 will scale the DC-voltage between 0...5V:

- Example 660VAC:

DC-voltage is $U_{dc} = 1.35 \times 660VAC = 891VDC$

Voltage across R8₃ is $5.6 \times 10^{-3} \times U_{dc} = 5V$

A1₃ will isolate the signal from the DC-bus potential.

With different AC voltages different gains can be selected by the jumpers S1...S8 to scale -UC1₃ signal to be approximately -5V with nominal AC-line voltage (S8 normally in position a-b).

Table 1. -UC1 signal level with different nominal voltages

| jumper | gain | -UC1 (nominal AC voltage) |
|--------|-------|-----------------------------|
| - S1 | 1.730 | -4.99V (380VAC) |
| - S2 | 1.574 | -4.96V (415VAC) |
| - S3 | 1.484 | -4.96V (440VAC) |
| - S4 | 1.430 | -4.99V (460VAC) |
| - S5 | 1.310 | -4.97V (500VAC) |
| - S6 | 1.126 | -4.92V (575VAC) |
| - S7 | 1.000 | -5.01V (660VAC) |

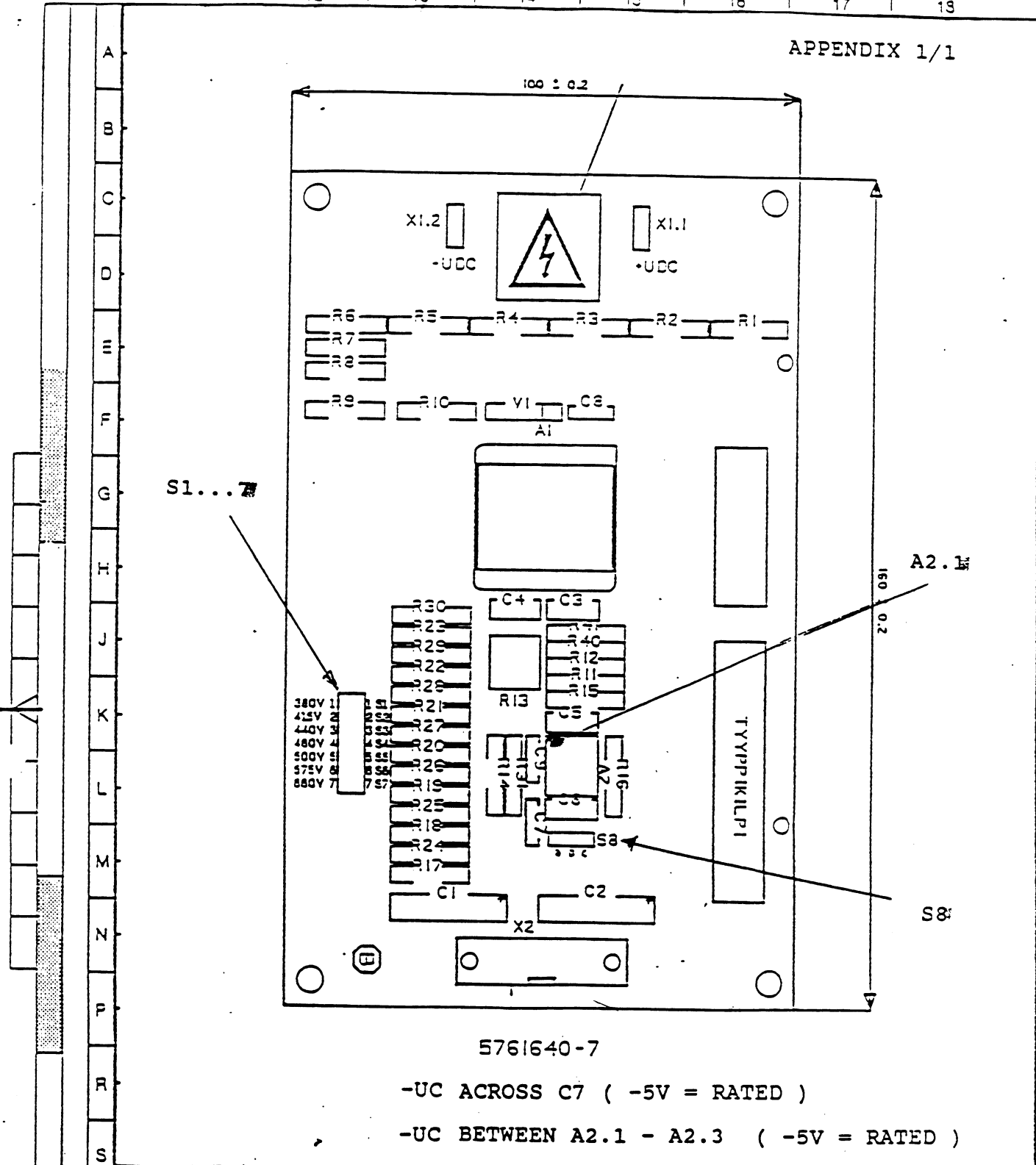
Allen-Bradley/Stromberg
R.A.
01-31-89

SAFT 183 VMC

PAGE 3 (3)

3. Trouble shooting

If there is no faults the motoring bridge pulse can be checked without charging the DC-bus by setting the jumper S8 to c-b position.



5761640-7

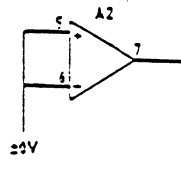
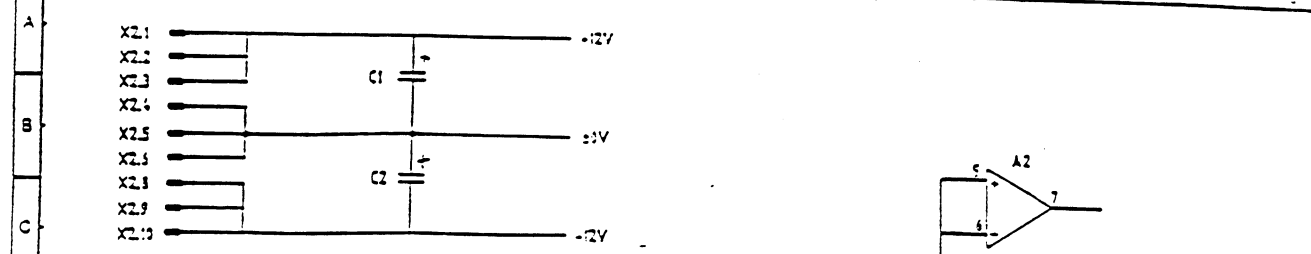
-UC ACROSS C7 (-5V = RATED)

-UC BETWEEN A2.1 - A2.3 (-5V = RATED)

KOKOONPANO
ASSEMBLY

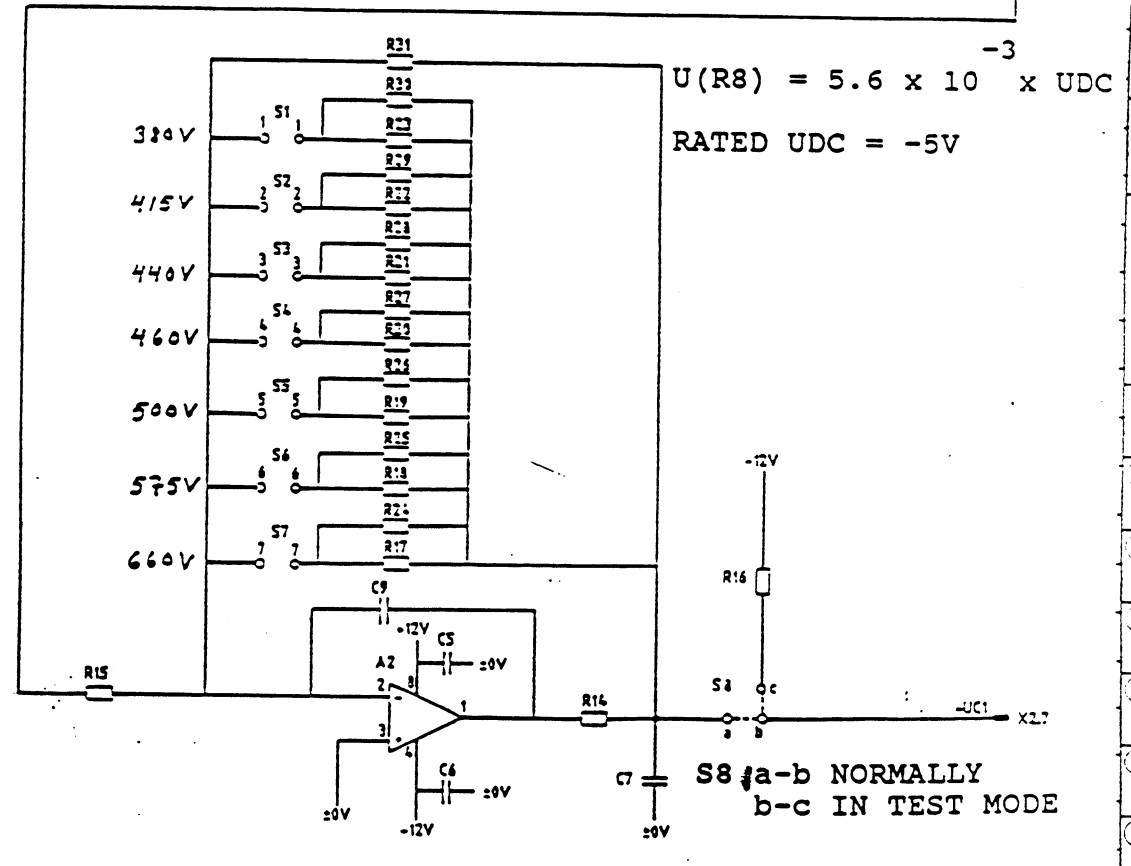
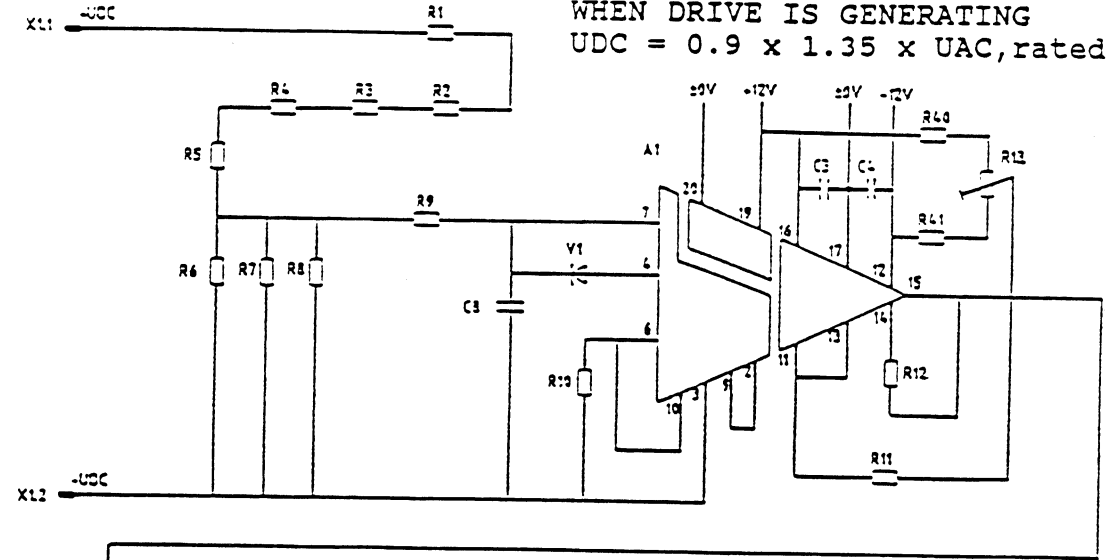
SAFT 183 YMC

| | | | | | |
|------------------------------|-------------------|-------------------|-------------------|--------------------|-------------------|
| MUTOS ANDRING REVISION | TYÖNUMERO | PROJ | TYÖNIMIKKI | LEIPIVÄ MOKKILA | CONCEPTEUR |
| | 1:1 | | EXF | | |
| STRÖMBERG FINLAND | TEHTÄVÄ GRAAFI | TEHTÄVÄ GRAAFI | TEHTÄVÄ GRAAFI | TEHTÄVÄ GRAAFI | TEHTÄVÄ GRAAFI |
| | J. SUOMINEN | J. SUOMINEN | J. SUOMINEN | J. SUOMINEN | J. SUOMINEN |
| | TEHTÄVÄ GRAAFI | TEHTÄVÄ GRAAFI | TEHTÄVÄ GRAAFI | TEHTÄVÄ GRAAFI | TEHTÄVÄ GRAAFI |
| | 82-02-04 | 82-02-04 | J.S. | A4 | 5811924-5 A |



WHEN DRIVE IS MOTORING
 $UDC = 1.35 \times UAC, \text{rated}$

WHEN DRIVE IS GENERATING
 $UDC = 0.9 \times 1.35 \times UAC, \text{rated}$



$U(R8) = 5.6 \times 10^{-3} \times UDC$
 RATED $UDC = -5V$

S8 a-b NORMALLY
 b-c IN TEST MODE

| | | | | | | | |
|--------------|--|-----------------------------|--|-------------------------------|--|-----------------|--|
| | | EXF 88-08-04 Suominen | | MITTAUSKORTTI SAFT 183 VMC | | A3 VE 5811 5487 | |
| S 701 P3 585 | | 86-10-03 75 | | SAFT 183 VMC | | A | |